### Lenovo Global Technology

**ThinkSystem SD630 V2**  
(2.30 GHz, Intel Xeon Platinum 8352M)

**SPEC CPU®2017 Integer Rate Result**  

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Jun-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

#### Software

**OS:** Red Hat Enterprise Linux 8.3  
(Ootpa)  
Kernel 4.18.0-240.el8.x86_64

**Compiler:**  
C/C++, Version 2021.1 of Intel oneAPI DPC++/C++  
Compiler Build 20201113 for Linux;  
Fortran: Version 2021.1 of Intel Fortran Compiler  
Classic Build 20201112 for Linux;

**Parallel:** No

**Firmware:** Lenovo BIOS Version U8E111A 1.02 released May-2021

**File System:** xfs

**System State:** Run level 3 (multi-user)

**Base Pointers:** 64-bit

**Peak Pointers:** Not Applicable

**Other:** None

**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage

#### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Platinum 8352M</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>3500</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2300</td>
</tr>
<tr>
<td>Enabled:</td>
<td>64 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>48 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 480 GB SATA SSD</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

#### SPECrate®2017_int_base = 413

<table>
<thead>
<tr>
<th>SPECrate®2017_int_peak = Not Run</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
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</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>128</td>
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<tr>
<td>502.gcc_r</td>
<td>128</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>128</td>
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<tr>
<td>520.omnetpp_r</td>
<td>128</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>128</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>128</td>
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<tr>
<td>531.deepsjeng_r</td>
<td>128</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>128</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>128</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>128</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base (413)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Copy 0: 40.0</td>
</tr>
<tr>
<td>Copy 1: 80.0</td>
</tr>
<tr>
<td>Copy 2: 120</td>
</tr>
<tr>
<td>Copy 3: 160</td>
</tr>
<tr>
<td>Copy 4: 200</td>
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<tr>
<td>Copy 5: 240</td>
</tr>
<tr>
<td>Copy 6: 280</td>
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<tr>
<td>Copy 7: 320</td>
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<tr>
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</tr>
<tr>
<td>Copy 16: 680</td>
</tr>
<tr>
<td>Copy 17: 720</td>
</tr>
<tr>
<td>Copy 18: 760</td>
</tr>
<tr>
<td>Copy 19: 800</td>
</tr>
<tr>
<td>Copy 20: 840</td>
</tr>
<tr>
<td>Copy 21: 880</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017

**Test Date:** Jun-2021

**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Jul-2021

**Tested by:** Lenovo Global Technology

**Software Availability:** Dec-2020

**Test Date:** Jun-2021

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**Software Availability:** Dec-2020
Lenovo Global Technology
ThinkSystem SD630 V2
(2.30 GHz, Intel Xeon Platinum 8352M)

SPECrater®2017_int_base = 413
SPECrater®2017_int_peak = Not Run

Results Table

Benchmark | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio
---|---|---|---|---|---|---|---|---|---|---|---|---|---|---

SPECrater®2017_int_base = 413
SPECrater®2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
MALLOC_CONF = "retain:true"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
**General Notes (Continued)**

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
UPI Link Disable set to Disabled 1 Link
DCU Streamer Prefetcher set to Disabled
SNC set to Enabled

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revB/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e6a6acfc64d
running on localhost.localdomain Mon Jun 28 10:03:19 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8352M CPU @ 2.30GHz
  2 "physical id"s (chips)
    128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 32
  siblings : 64
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 128
On-line CPU(s) list: 0-127
Thread(s) per core: 2
Core(s) per socket: 32

(Continued on next page)
Platform Notes (Continued)

Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8352M CPU @ 2.30GHz
Stepping: 6
CPU MHz: 2800.000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-15,64-79
NUMA node1 CPU(s): 16-31,80-95
NUMA node2 CPU(s): 32-47,96-111
NUMA node3 CPU(s): 48-63,112-127
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpteol gdtscpl mconstant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmpref cpuid pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abtm 3dmovprefetch cpuid_fault epb cat l3 invpcid_single intel_pimpin ssbd mba ibrs ibpb ibrsEnhanced tpr_shadow vmlinux flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512sfma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsxavopt xsavec xgetbv1 xsaves cqm llc cqm_occup llc cqm_mbb total cqm_mbb_local split_lock_detect wbinvd dtherm ida arat pln pts avx512vbmi umip pku ospe avx512_vbmi2 gfnl vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 49152 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79
node 0 size: 124263 MB
node 0 free: 127995 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
node 1 size: 125128 MB
node 1 free: 127754 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 96 97 98 99 100 101 102

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SPEC CPU®2017 Integer Rate Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

SPECrate®2017_int_base = 413
SPECrate®2017_int_peak = Not Run

Platform Notes (Continued)

103 104 105 106 107 108 109 110 111
node 2 size: 125740 MB
node 2 free: 128412 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 112 113 114 115 116 117
118 119 120 121 122 123 124 125 126 127
node 3 size: 125468 MB
node 3 free: 128310 MB
node distances:
node 0: 10 11 20 20
node 1: 11 10 20 20
node 2: 20 20 10 11
node 3: 20 20 11 10

From /proc/meminfo
MemTotal: 527992740 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*

NAME="Red Hat Enterprise Linux"
VERSIO="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname –a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store

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**SPECraten 2017_int_base = 413**  
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**Platform Notes (Continued)**

CVE-2017-5753 (Spectre variant 1):  
Bypass disabled via prctl and seccomp  
Mitigation: usercopy/swaps barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):  
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):  
Not affected

CVE-2019-11135 (TSX Asynchronous Abort):  
Not affected

run-level 3 Jun 28 05:21

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revB

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>372G</td>
<td>73G</td>
<td>300G</td>
<td>20%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem SD630 V2  
Product Family: ThinkSystem  
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200

BIOS:  
BIOS Vendor: Lenovo  
BIOS Version: U8E111A-1.02  
BIOS Date: 05/07/2021  
BIOS Revision: 1.2  
Firmware Revision: 1.40

(End of data from sysinfo program)

**Compiler Version Notes**

```
==============================================================================
CPU       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113
```

(Continued on next page)
Lenovo Global Technology
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SPECrone®2017_int_base = 413
SPECrone®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2021
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Compiler Version Notes (Continued)
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)       |
|         | 541.leela_r(base)     |

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| Fortran | 548.exchange2_r(base) |

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.zx_r: -DSPEC_LP64
## Base Optimization Flags

### C benchmarks:
- `-w` `-std=c11` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-O3` `-ffast-math`
- `-flto` `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

### C++ benchmarks:
- `-w` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-O3` `-ffast-math` `-flto`
- `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

### Fortran benchmarks:
- `-w` `-m64` `-Wl,-z,muldefs` `-xCORE-AVX512` `-O3` `-ipo` `-no-prec-div`
- `-qopt-mem-layout-trans=4` `-nostandard-realloc-lhs` `-align array32byte`
- `-auto` `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`
- `-lqkmalloc`

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links: