# SPEC CPU®2017 Integer Speed Result

## New H3C Technologies Co., Ltd.

H3C UniServer R5300 G5 (Intel Xeon Gold 6330)

**SPECspeed®2017_int_base = 10.7**  
**SPECspeed®2017_int_peak = 10.9**

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Test Date</th>
<th>Test Sponsor</th>
<th>Hardware Availability</th>
<th>Software Availability</th>
</tr>
</thead>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base (10.7)</th>
<th>SPECspeed®2017_int_peak (10.9)</th>
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<tbody>
<tr>
<td>600.perlbench_s 56</td>
<td>7.31</td>
<td>6.46</td>
</tr>
<tr>
<td>602.gcc_s 56</td>
<td>9.95</td>
<td>10.3</td>
</tr>
<tr>
<td>605.mcf_s 56</td>
<td>18.1</td>
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</tr>
<tr>
<td>620.omnetpp_s 56</td>
<td>12.1</td>
<td>15.3</td>
</tr>
<tr>
<td>623.xalancbmk_s 56</td>
<td>5.30</td>
<td>13.9</td>
</tr>
<tr>
<td>625.x264_s 56</td>
<td>4.30</td>
<td>17.1</td>
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<td>17.1</td>
<td>21.7</td>
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<td>641.leela_s 56</td>
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</tr>
<tr>
<td>657.xz_s 56</td>
<td>21.7</td>
<td>21.7</td>
</tr>
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</table>

### Software

- **OS:** Red Hat Enterprise Linux release 8.3 (Ootpa) 4.18.0-240.el8.x86_64
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 5.27 released Jun-2021BIOS
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage

---

**CPU Name:** Intel Xeon Gold 6330  
**Max MHz:** 3100  
**Nominal:** 2000  
**Enabled:** 56 cores, 2 chips  
**Orderable:** 1.2 Chips  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 42 MB I+D on chip per chip  
**Other:** None  
**Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-3200V-R, running at 2933)  
**Storage:** 960 GB SSD NVME  
**Other:** None

---

**Software**

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<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<tr>
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<td>56</td>
<td>260</td>
<td>18.2</td>
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<td>11.0</td>
<td>152</td>
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<td>172</td>
<td>17.1</td>
<td>56</td>
<td>172</td>
<td>17.1</td>
<td>171</td>
<td>17.2</td>
</tr>
</tbody>
</table>

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation:
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
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CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Jun-2021
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Jun-2021
Software Availability: Dec-2020

General Notes (Continued)

Platform Notes

BIOS Settings:
Set Hyper-Threading to disabled
Set Patrol Scrub to disabled
Set XPT Prefetch to disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64f6d
running on localhost.localdomain Tue Jun 29 09:21:25 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz
  2 "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 56
On-line CPU(s) list: 0-55
Thread(s) per core: 1
Core(s) per socket: 28
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz
Stepping: 6
CPU MHz: 3034.634

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Platform Notes (Continued)

- CPU max MHz: 3100.0000
- CPU min MHz: 800.0000
- BogoMIPS: 4000.00
- Virtualization: VT-x
- L1d cache: 48K
- L1i cache: 32K
- L2 cache: 1280K
- L3 cache: 43008K
- NUMA node0 CPU(s): 0-27
- NUMA node1 CPU(s): 28-55
- Flags: fpu vme de pse tsc msr pae mce cm8 apic sep mtrr pge mca cmov
- /proc/cpuinfo cache data
- cache size : 43008 KB
- From numactl --hardware
  WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
  node 0 size: 247199 MB
  node 0 free: 256822 MB
  node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
  53 54 55
  node 1 size: 246248 MB
  node 1 free: 256608 MB
  node distances:
  node 0 1
  0: 10 20
  1: 20 10

From /proc/meminfo
- MemTotal: 528007648 KB
- HugePages_Total: 0
- Hugepagesize: 2048 KB

(Continued on next page)
Platform Notes (Continued)

/sbin/tuned-adm active
   Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release*/etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.3 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.3"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
   Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 29 09:18

SPEC is set to: /home/speccpu
   Filesystem Type Size Used Avail Use% Mounted on
   /dev/mapper/rhel-home xfs 670G 84G 586G 13% /home

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**Test Date:** Jun-2021

**Hardware Availability:** Jun-2021

**Tested by:** New H3C Technologies Co., Ltd.

**Software Availability:** Dec-2020

---

**Platform Notes (Continued)**

From /sys/devices/virtual/dmi/id

Vendor: New H3C Technologies Co., Ltd.

Product: UniServer R5300 G5

Product Family: Rack

Serial: 210235A3WGH213000015

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

- 7x Hynix HMA84GR7DJR4N-XN 32 GB 2 rank 3200, configured at 2933
- 5x Micron 36ASF4G72PZ-3G2E7 32 GB 2 rank 3200, configured at 2933
- 16x NO DIMM NO DIMM
- 4x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933

**BIOS:**

- BIOS Vendor: American Megatrends International, LLC.
- BIOS Version: 5.27
- BIOS Date: 06/07/2021
- BIOS Revision: 5.22

(End of data from sysinfo program)

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**Compiler Version Notes**

```
<table>
<thead>
<tr>
<th>C</th>
<th>600.perlbench_s(peak)</th>
</tr>
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</table>
```

**Intel(R) C**

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
<table>
<thead>
<tr>
<th>C</th>
<th>600.perlbench_s(base), 602.gcc_s(base, peak), 605.mcf_s(base, peak), 625.x264_s(base, peak), 657.xz_s(base, peak)</th>
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```

**Intel(R) oneAPI DPC++/C++**

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
<table>
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<th>600.perlbench_s(peak)</th>
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### Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

| C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)  
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
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| C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)  
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<thead>
<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
</tr>
</thead>
</table>

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000  
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### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifort
New H3C Technologies Co., Ltd. | SPECspeed®2017_int_base = 10.7
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### Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

### Base Optimization Flags

#### C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

#### C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

#### Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

### Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

(Continued on next page)
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### Fortran benchmarks:
- ifort

---

### Peak Compiler Invocation (Continued)

### Peak Portability Flags

Same as Base Portability Flags

---

### Peak Optimization Flags

**C benchmarks:**

- 600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
- -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -fno-strict-overflow
- -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

- 602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
- -fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
- -Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

- 605.mcf_s: basepeak = yes

- 625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
- -xCORE-AVX512 -flto -O3 -ffast-math
- -qopt-mem-layout-trans=4 -fno-alias
- -mbranches-within-32B-boundaries
- -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

- 657.xz_s: basepeak = yes

**C++ benchmarks:**

- 620.omnetpp_s: basepeak = yes
- 623.xalancbmk_s: basepeak = yes
- 631.deepsjeng_s: basepeak = yes

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*(Continued on next page)*
## SPEC CPU®2017 Integer Speed Result

### New H3C Technologies Co., Ltd.
**H3C UniServer R5300 G5 (Intel Xeon Gold 6330)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>10.7</td>
<td>10.9</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9066  
**Test Date:** Jun-2021  
**Hardware Availability:** Jun-2021  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Software Availability:** Dec-2020

### Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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