## Lenovo Global Technology

### SPEC CPU®2017 Integer Speed Result

**Test Sponsor:** Lenovo Global Technology  
**Hardware Availability:** Jun-2021  
**Test Date:** Jun-2021  
**Software Availability:** Mar-2021

<table>
<thead>
<tr>
<th>Test</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>gcc</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>mcf</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>omnetpp</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>x264</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>deepsjeng</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>leela</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>exchange2</td>
<td>11.9</td>
<td>11.9</td>
</tr>
<tr>
<td>xz</td>
<td>11.9</td>
<td>11.9</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** AMD EPYC 7663  
- **Max MHz:** 3500  
- **Nominal:** 2000  
- **Enabled:** 56 cores, 1 chip, 2 threads/core  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 512 KB I+D on chip per core  
- **L3:** 256 MB I+D on chip per chip, 32 MB shared / 7 cores  
- **Other:** None  
- **Memory:** 256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 SP2 (x86_64)  
- **Kernel:** 5.3.18-22-default  
- **Compiler:** C/C++/Fortran: Version 3.0.0 of AOCC  
- **Parallel:** Yes  
- **Firmware:** Lenovo BIOS Version CFE125U 6.0 released May-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc: jemalloc memory allocator library v5.1.0  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perbench_s</td>
<td>56</td>
<td>260</td>
<td>6.84</td>
<td>259</td>
<td>6.84</td>
<td>258</td>
<td>6.88</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>56</td>
<td>311</td>
<td>12.8</td>
<td>310</td>
<td>12.8</td>
<td>312</td>
<td>12.7</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>56</td>
<td>239</td>
<td>19.8</td>
<td>238</td>
<td>19.8</td>
<td>238</td>
<td>19.8</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>56</td>
<td>206</td>
<td>7.90</td>
<td>204</td>
<td>7.99</td>
<td>208</td>
<td>8.75</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>56</td>
<td>108</td>
<td>13.2</td>
<td>104</td>
<td>13.6</td>
<td>104</td>
<td>13.6</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>56</td>
<td>108</td>
<td>16.4</td>
<td>108</td>
<td>16.4</td>
<td>108</td>
<td>16.3</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>56</td>
<td>235</td>
<td>6.10</td>
<td>235</td>
<td>6.09</td>
<td>236</td>
<td>6.08</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>56</td>
<td>309</td>
<td>5.52</td>
<td>308</td>
<td>5.55</td>
<td>307</td>
<td>5.56</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>56</td>
<td>131</td>
<td>22.5</td>
<td>132</td>
<td>22.4</td>
<td>131</td>
<td>22.5</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>56</td>
<td>260</td>
<td>23.8</td>
<td>261</td>
<td>23.7</td>
<td>261</td>
<td>23.7</td>
</tr>
</tbody>
</table>

SPECSpeed®2017_int_base = 11.9
SPECSpeed®2017_int_peak = 11.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at http://developer.amd.com/amd-aocc/

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
'echo 8 > /proc/sys/vm/dirty_ratio' run as root to limit dirty cache to 8% of memory.
'echo 1 > /proc/sys/vm/swappiness' run as root to limit swap usage to minimum necessary.
'echo 1 > /proc/sys/vm/zone_reclaim_mode' run as root to free node-local memory and avoid remote memory usage.
'sync; echo 3 > /proc/sys/vm/drop_caches' run as root to reset filesystem caches.
'sysctl -w kernel.randomize_va_space=0' run as root to disable address space layout randomization (ASLR) to reduce run-to-run variability.
To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/never' and
Lenovo Global Technology
ThinkSystem SR655
2.00 GHz, AMD EPYC 7663

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 9017
Test Date: Jun-2021
Test Sponsor: Lenovo Global Technology
Hardware Availability: Jun-2021
Tested by: Lenovo Global Technology
Software Availability: Mar-2021

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-111"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.8-amd-aocc300-milan-B1/amd_speed_aocc300_milan_B_lib/64;/home/cpu2017-1.1.8-amd-aocc300-milan-B1/amd_speed_aocc300_milan_B_lib/32:
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "112"

Environment variables set by runcpu during the 602.gcc_s peak run:
GOMP_CPU_AFFINITY = "0"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
LLC as NUMA Node set to Disabled

Sysinfo program /home/cpu2017-1.1.8-amd-aocc300-milan-B1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost Fri Apr 17 21:16:07 2020

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR655
2.00 GHz, AMD EPYC 7663

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name: AMD EPYC 7663 56-Core Processor
  1 "physical id"s (chips)
  112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 56
siblings: 112
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30 32 33 34 35 36 37 38 41 42 43 44 45 46 48 49 50 51 52 53 54 56 57 58 59 60 61 62

From lscpu from util-linux 2.33.1:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 48 bits physical, 48 bits virtual
CPU(s): 112
On-line CPU(s) list: 0-111
Thread(s) per core: 2
Core(s) per socket: 56
Socket(s): 1
NUMA node(s): 1
Vendor ID: AuthenticAMD
CPU family: 25
Model: 1
Model name: AMD EPYC 7663 56-Core Processor
Stepping: 1
CPU MHz: 2229.030
CPU max MHz: 2000.0000
CPU min MHz: 1500.0000
BogoMIPS: 3992.40
Virtualization: AMD-V
L1d cache: 32K
L1i cache: 32K
L2 cache: 512K
L3 cache: 32768K
NUMA node0 CPU(s): 0-111
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand
lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw

(Continued on next page)
### Lenovo Global Technology

**ThinkSystem SR655**
2.00 GHz, AMD EPYC 7663

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>11.9</td>
<td>11.9</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Date:** Jun-2021  
**Test Sponsor:** Lenovo Global Technology  
**Hardware Availability:** Jun-2021  
**Tested by:** Lenovo Global Technology  
**Software Availability:** Mar-2021

---

**Platform Notes (Continued)**

```plaintext
ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsqsbases
bm1 avx2 smep bmi2 erms invpcid cqm rdr_a rdseed adx smap clflushopt clwb sha_ni
xsaveopt xsavex xgetbv1 xssaves cqm_llc cqm_occup_llc cqm_mbms_total cqm_mbms_local
c1zero irperf xsaveerptr wmbonvd arat npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassists pausefilter pfthreshold v_vmsave_vmload vgif
umip pkp ospke vaes vpclmulqdq rdpid overflow_recover succor smca
```

/proc/cpuinfo cache data  

cache size : 512 KB  

From `numactl --hardware`  

**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  

available: 1 nodes (0)  

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111  

node 0 size: 257586 MB  

node 0 free: 256412 MB  

node distances:  

node 0  

0: 10  

From `/proc/meminfo`  

MemTotal: 263768112 kB  

HugePages_Total: 0  

Hugepagesize: 2048 kB  

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has  

performance  

/usr/bin/lsb_release -d  

SUSE Linux Enterprise Server 15 SP2  

From `/etc/*release*`  

os-release:  

NAME="SLES"  

VERSION="15-SP2"  

VERSION_ID="15.2"  

PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"  

ID="sles"  

ID_LIKE="suse"  

ANSI_COLOR="0;32"  

CPE_NAME="cpe:/o:suse:sles:15:sp2"  

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR655
2.00 GHz, AMD EPYC 7663

SPEC CPU®2017 Integer Speed Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2021
Hardware Availability: Jun-2021
Software Availability: Mar-2021

Platform Notes (Continued)

uname -a:
    Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBFP: conditional, IBRS_FW, STIBF: always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Apr 17 21:14

SPEC is set to: /home/cpu2017-1.1.8-amd-aocc300-milan-B1
From /sys/devices/virtual/dmi/id
    Vendor: Lenovo
    Product: ThinkSystem SR655 -[7Y00000000]-
    Product Family: ThinkSystem
    Serial: 0123456789

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
    8x Samsung M393A4K40DB2-CWE 32 GB 2 rank 3200
    8x Unknown Unknown

BIOS:
    BIOS Vendor: Lenovo
    BIOS Version: CFE125U
    BIOS Date: 05/28/2021
    BIOS Revision: 6.0

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR655
2.00 GHz, AMD EPYC 7663

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2021
Hardware Availability: Jun-2021
Software Availability: Mar-2021

Platform Notes (Continued)
(End of data from sysinfo program)

Compiler Version Notes

C benchmarks
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

C++ benchmarks
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

Fortran
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR655
2.00 GHz, AMD EPYC 7663

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2021
Hardware Availability: Jun-2021
Software Availability: Mar-2021

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 11.9

Base Compiler Invocation (Continued)

Fortran benchmarks:
flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-llflang -llflangrti

C++ benchmarks:
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch

(Continued on next page)
**Lenovo Global Technology**

ThinkSystem SR655
2.00 GHz, AMD EPYC 7663

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Test Date:** Jun-2021

**Hardware Availability:** Jun-2021

**Tested by:** Lenovo Global Technology

**Software Availability:** Mar-2021

### SPECspeed®2017_int_base = 11.9

### SPECspeed®2017_int_peak = 11.9

---

### Base Optimization Flags (Continued)

**C++ benchmarks (continued):**
- `--mllvm -extra-vectorizer-passes`
- `--mllvm -reduce-array-computations=3`
- `--mllvm -global-vectorize-slp=true`
- `--mllvm -convert-pow-exp-to-int=false`
- `--z muldefs`
- `--mllvm -do-block-reorder=aggressive`
- `--fvirtual-function-elimination`
- `--fvisibility=hidden`
- `--DSPEC_OPENMP`
- `--fopenmp`
- `--fopenmp=libomp`
- `--lomp`
- `--lamdlibm`
- `--ljemalloc`
- `--lflang`
- `--lflangrti`

**Fortran benchmarks:**
- `--m64`
- `--mno-adx`
- `--mno-sse4a`
- `--Wl,-mllvm -Wl,-inline-recursion=4`
- `--Wl,-mllvm -Wl,-lsr-in-nested-loop`
- `--Wl,-mllvm -Wl,-enable-iv-split`
- `--Wl,-mllvm -Wl,-region-vectorize`
- `--Wl,-mllvm -Wl,-function-specialize`
- `--Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
- `--Wl,-mllvm -Wl,-reduce-array-computations=3`
- `--O3`
- `--march=znver3`
- `--fveclib=AMDLIBM`
- `--ffast-math`
- `--flto`
- `--z muldefs`
- `--mllvm -unroll-aggressive`
- `--mllvm -unroll-threshold=150`
- `--DSPEC_OPENMP`
- `--fopenmp`
- `--fopenmp=libomp`
- `--lomp`
- `--lamdlibm`
- `--ljemalloc`
- `--lflang`
- `--lflangrti`

### Base Other Flags

**C benchmarks:**
- `--Wno-unused-command-line-argument`
- `--Wno-return-type`

**C++ benchmarks:**
- `--Wno-unused-command-line-argument`
- `--Wno-return-type`

**Fortran benchmarks:**
- `--Wno-return-type`

---

### Peak Compiler Invocation

**C benchmarks:**
- `clang`

**C++ benchmarks:**
- `clang++`

**Fortran benchmarks:**
- `flang`
# SPEC CPU®2017 Integer Speed Result

## Lenovo Global Technology

**ThinkSystem SR655**  
2.00 GHz, AMD EPYC 7663

---

### CPU2017 License: 9017

**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

---

### SPECspeed®2017_int_base = 11.9

### SPECspeed®2017_int_peak = 11.9

---

### Peak Portability Flags

Same as Base Portability Flags

---

### Peak Optimization Flags

#### C benchmarks:

600.perlbench_s: basepeak = yes


605.mcf_s: basepeak = yes

625.x264_s: basepeak = yes

657.xz_s: basepeak = yes

#### C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbnk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

#### Fortran benchmarks:

648.exchange2_s: basepeak = yes
SPEC CPU®2017 Integer Speed Result

Lenovo Global Technology
ThinkSystem SR655
2.00 GHz, AMD EPYC 7663

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2021
Hardware Availability: Jun-2021
Software Availability: Mar-2021

Peak Other Flags

C benchmarks:
-Wno-unused-command-line-argument  -Wno-return-type

C++ benchmarks:
-Wno-unused-command-line-argument  -Wno-return-type

Fortran benchmarks:
-Wno-return-type

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Milan1P-G.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Milan1P-G.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2020-04-17 09:16:06-0400.
Report generated on 2021-07-06 18:45:15 by CPU2017 PDF formatter v6442.
Originally published on 2021-07-06.