## SPEC CPU®2017 Floating Point Speed Result

### Dell Inc.

#### PowerEdge C6520 (Intel Xeon Gold 6314U, 2.30 GHz)

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jun-2021  
**Hardware Availability:** Apr-2021  
**Software Availability:** Dec-2020

### SPECspeed®2017 fp_base = 135

### SPECspeed®2017 fp_peak = 138

<table>
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<tr>
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<th>SPECspeed®2017_fp_peak</th>
<th>SPECspeed®2017_fp_base</th>
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<td>32</td>
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<td>(135)</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>32</td>
<td>179</td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>32</td>
<td>78.7</td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>32</td>
<td>155</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>32</td>
<td>143</td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>32</td>
<td>96.1</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>32</td>
<td>112</td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>32</td>
<td>232</td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>32</td>
<td>69.3</td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>32</td>
<td>152</td>
<td></td>
</tr>
</tbody>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Gold 6314U  
- **Max MHz:** 3400  
- **Nominal:** 2300  
- **Enabled:** 32 cores, 1 chip  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 48 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R)  
- **Storage:** 125 GB on tmpfs  
- **Other:** None

#### Software

- **OS:** Red Hat Enterprise Linux 8.2 (Ootpa)  
- **Compiler:**  
  - C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
  - Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 1.1.3 released Apr-2021  
- **File System:** tmpfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
Dell Inc.

PowerEdge C6520 (Intel Xeon Gold 6314U, 2.30 GHz)

SPECspeed®2017_fp_base = 135
SPECspeed®2017_fp_peak = 138

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>32</td>
<td>154</td>
<td>384</td>
<td>154</td>
<td>384</td>
<td>154</td>
<td>384</td>
<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>32</td>
<td>93.0</td>
<td>179</td>
<td>92.5</td>
<td>180</td>
<td>93.0</td>
<td>179</td>
<td>92.5</td>
<td>180</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>32</td>
<td>66.3</td>
<td>79.0</td>
<td>66.6</td>
<td>78.7</td>
<td>66.6</td>
<td>78.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>32</td>
<td>85.3</td>
<td>155</td>
<td>83.7</td>
<td>158</td>
<td>76.6</td>
<td>173</td>
<td>76.6</td>
<td>173</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>32</td>
<td>96.2</td>
<td>92.2</td>
<td>96.2</td>
<td>92.1</td>
<td>96.2</td>
<td>92.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>32</td>
<td>124</td>
<td>96.1</td>
<td>123</td>
<td>96.4</td>
<td>129</td>
<td>112</td>
<td>129</td>
<td>112</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>32</td>
<td>129</td>
<td>112</td>
<td>129</td>
<td>112</td>
<td>129</td>
<td>112</td>
<td></td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>32</td>
<td>75.4</td>
<td>232</td>
<td>75.4</td>
<td>232</td>
<td>68.5</td>
<td>255</td>
<td>68.5</td>
<td>255</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>32</td>
<td>132</td>
<td>69.3</td>
<td>131</td>
<td>69.5</td>
<td>131</td>
<td>69.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>32</td>
<td>103</td>
<td>152</td>
<td>104</td>
<td>152</td>
<td>104</td>
<td>152</td>
<td></td>
<td></td>
</tr>
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</table>

SPECspeed®2017_fp_base = 135
SPECspeed®2017_fp_peak = 138

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
- Logical Processor : Disabled
- Virtualization Technology : Disabled

- System Profile : Custom
- CPU Power Management : Maximum Performance
- C1E : Disabled
- C States : Autonomous
- Memory Patrol Scrub : Disabled
- Energy Efficiency Policy : Performance
- CPU Interconnect Bus Link
- Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Sat Jun  5 15:50:10 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
  1 "physical id"s (chips)
  32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 32
  - siblings : 32
  - physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
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PowerEdge C6520 (Intel Xeon Gold 6314U, 2.30 GHz)

SPECspeed®2017_fp_peak = 138
SPECspeed®2017_fp_base = 135

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jun-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Platform Notes (Continued)

Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
Stepping: 6

CPU MHz: 2898.350
BogoMIPS: 4600.00
Virtualization: VT-x

L1d cache: 48K
L1i cache: 32K

L2 cache: 1280K
L3 cache: 49152K

NUMA node0 CPU(s): 0-31

Flags: fpu vme pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebx cat_13 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erva invnpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512vld sha ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occupy_llc cqm_mbm_total cqm_mbm_local wbnoinvd dtherm ida arat pln pts avx512v bmi umip pku ospke avx512_vbmi gfnl vaes vpcmulqdq avx512_vnni avx512_vbitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lld arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31
node 0 size: 515453 MB
node 0 free: 499219 MB
node distances:
node 0
do: 10

(Continued on next page)
Dell Inc.

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SPECspeed®2017_fp_peak = 138

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Test Sponsor: Dell Inc.
Test Date: Jun-2021
Tested by: Dell Inc.

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1
Dell Inc.

PowerEdge C6520 (Intel Xeon Gold 6314U, 2.30 GHz)

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Platform Notes (Continued)

Filesystem | Type | Size | Used | Avail | Use% | Mounted on
---|---|---|---|---|---|---
tmpfs | tmpfs | 125G | 11G | 115G | 9% | /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge C6520
Product Family: PowerEdge
Serial: SDPT078

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
8x 00AD063200AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200
8x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.3
BIOS Date: 04/27/2021
BIOS Revision: 1.1

(End of data from sysinfo program)

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Compiler Version Notes

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)
---

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C | 644.nab_s(peak)
---

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 202011113
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---

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)

(Continued on next page)
Dell Inc. PowerEdge C6520 (Intel Xeon Gold 6314U, 2.30 GHz)

Dell Inc.

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Copyright 2017-2021 Standard Performance Evaluation Corporation

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**Compiler Version Notes (Continued)**

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<th>644.nab_s(base)</th>
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<tr>
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</tr>
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<tr>
<td>Fortran</td>
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</tr>
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</tr>
</thead>
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</tr>
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</tr>
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</table>
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</tr>
</thead>
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</tr>
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(Continued on next page)
## Base Compiler Invocation

C benchmarks:
- `icc`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

## Base Portability Flags

- `603.bwaves_s`: `-DSPEC_LP64`
- `607.cactuBSSN_s`: `-DSPEC_LP64`
- `619.lbm_s`: `-DSPEC_LP64`
- `621.wrf_s`: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
- `627.cam4_s`: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
- `628.pop2_s`: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
  - `assume byterecl`
- `638.imagick_s`: `-DSPEC_LP64`
- `644.nab_s`: `-DSPEC_LP64`
- `649.fotonik3d_s`: `-DSPEC_LP64`
- `654.roms_s`: `-DSPEC_LP64`

## Base Optimization Flags

C benchmarks:
- `-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
- `-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP`
- `-mbranches-within-32B-boundaries`

Fortran benchmarks:
- `-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`

(Continued on next page)
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<td>Dec-2020</td>
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### Base Optimization Flags (Continued)

Fortran benchmarks (continued):
- `-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs`
- `-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib`
- `-ljemalloc`

Benchmarks using both Fortran and C:
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp`
- `-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp`
- `-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

### Peak Compiler Invocation

C benchmarks (except as noted below):
- `icc`
- `644.nab_s: icx`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

### Peak Portability Flags

Same as Base Portability Flags
Dell Inc.  
PowerEdge C6520 (Intel Xeon Gold 6314U, 2.30 GHz)  

**SPECspeed®2017_fp_base = 135**  
**SPECspeed®2017_fp_peak = 138**

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### Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
           -ffloatmath=sse -funroll-loops -fiopenmp
           -DSPEC_OPENMP -qopt-mem-layout-trans=4
           -fimf-accuracy-bits=14:sqrt
           -mbranches-within-32B-boundaries
           -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
                 -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
                 -03 -no-prec-div -qopt-prefetch -ffinite-math-only
                 -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
                 -mbranches-within-32B-boundaries
                 -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

654.roms_s: basepeak = yes

### Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
           -prof-use(pass 2) -ipo -xCORE-AVX512 -03 -no-prec-div
           -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
           -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
           -mbranches-within-32B-boundaries -nostandard-realloc-lhs
           -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

### Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes
### SPEC CPU®2017 Floating Point Speed Result

**Dell Inc.**

**PowerEdge C6520 (Intel Xeon Gold 6314U, 2.30 GHz)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>135</td>
<td>138</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jun-2021  
**Hardware Availability:** Apr-2021  
**Software Availability:** Dec-2020

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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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