**SPEC CPU®2017 Integer Speed Result**

Dell Inc.

PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base = 11.8</th>
<th>SPECspeed®2017_int_peak = 12.1</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>8.27</td>
<td>10.9</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>11.3</td>
<td></td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>13.5</td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>11.4</td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>5.93</td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>19.4</td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>4.85</td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>17.0</td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>20.0</td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>23.5</td>
<td></td>
</tr>
</tbody>
</table>

---

**Hardware**

- **CPU Name:** Intel Xeon Gold 6342
- **Max MHz:** 3500
- **Nominal:** 2800
- **Enabled:** 48 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **Cache L2:** 1.25 MB I+D on chip per core
- **Cache L3:** 36 MB I+D on chip per chip
- **Other:** None
- **Memory:** 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
- **Storage:** 225 GB on tmpfs
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa) 4.18.0-240.15.1.el8_3.x86_64
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
  Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
  C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 1.2.2 released May-2021
- **File System:** tmpfs
- **System State:** Run level 5 (graphical multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
Dell Inc.

PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

SPECspeed®2017_int_base = 11.8
SPECspeed®2017_int_peak = 12.1

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>48</td>
<td>247</td>
<td>7.19</td>
<td>245</td>
<td>7.24</td>
<td>247</td>
<td>7.19</td>
<td>48</td>
<td>214</td>
<td>8.30</td>
<td>215</td>
<td>8.27</td>
<td>215</td>
<td>8.25</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>48</td>
<td>367</td>
<td>10.9</td>
<td>365</td>
<td>10.9</td>
<td>366</td>
<td>10.9</td>
<td>48</td>
<td>352</td>
<td>11.3</td>
<td>351</td>
<td>11.3</td>
<td>355</td>
<td>11.2</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>48</td>
<td>367</td>
<td>20.0</td>
<td>365</td>
<td>20.1</td>
<td>366</td>
<td>20.1</td>
<td>48</td>
<td>352</td>
<td>20.0</td>
<td>351</td>
<td>20.1</td>
<td>355</td>
<td>19.9</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>48</td>
<td>144</td>
<td>11.3</td>
<td>141</td>
<td>11.6</td>
<td>144</td>
<td>11.4</td>
<td>48</td>
<td>144</td>
<td>11.3</td>
<td>141</td>
<td>11.6</td>
<td>144</td>
<td>11.4</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>48</td>
<td>106</td>
<td>13.4</td>
<td>105</td>
<td>13.5</td>
<td>104</td>
<td>13.6</td>
<td>48</td>
<td>106</td>
<td>13.4</td>
<td>105</td>
<td>13.5</td>
<td>104</td>
<td>13.6</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>48</td>
<td>103</td>
<td>17.1</td>
<td>104</td>
<td>17.0</td>
<td>104</td>
<td>17.0</td>
<td>48</td>
<td>99.1</td>
<td>17.8</td>
<td>99.1</td>
<td>17.8</td>
<td>99.1</td>
<td>17.8</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>48</td>
<td>241</td>
<td>5.94</td>
<td>242</td>
<td>5.93</td>
<td>242</td>
<td>5.93</td>
<td>48</td>
<td>241</td>
<td>5.94</td>
<td>242</td>
<td>5.93</td>
<td>242</td>
<td>5.93</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>48</td>
<td>152</td>
<td>19.4</td>
<td>152</td>
<td>19.4</td>
<td>152</td>
<td>19.4</td>
<td>48</td>
<td>152</td>
<td>19.4</td>
<td>152</td>
<td>19.4</td>
<td>152</td>
<td>19.4</td>
</tr>
<tr>
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<td>48</td>
<td>263</td>
<td>23.5</td>
<td>263</td>
<td>23.5</td>
<td>263</td>
<td>23.5</td>
<td>48</td>
<td>263</td>
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<td>263</td>
<td>23.5</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Dell Inc.**

PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)  

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<td>Test Date: May-2021</td>
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<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: May-2021</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Feb-2021</td>
</tr>
</tbody>
</table>

**General Notes (Continued)**

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

**Platform Notes**

BIOS Settings:
- Logical Processor: Disabled
- Virtualization Technology: Disabled

System Profile: Custom
- CPU Power Management: Maximum Performance
  - C1E: Disabled
  - C States: Autonomous
  - Memory Patrol Scrub: Disabled
- Energy Efficiency Policy: Performance
- CPU Interconnect Bus Link
  - Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Mon May 31 09:36:33 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Gold 6342 CPU @ 2.80GHz
  - 2 "physical id"s (chips)
  - 48 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 24
  - siblings: 24
  - physical 0: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23
  - physical 1: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian

(Continued on next page)
Dell Inc.

PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)

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CPU2017 License: 55
Test Sponsor: Dell Inc.
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Test Date: May-2021
Hardware Availability: May-2021
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CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6342 CPU @ 2.80GHz
Stepping: 6
CPU MHz: 3302.227
BogoMIPS: 5600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46
NUMA node0 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good ntop tsc xaltos nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtc64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pcid dca ssse4_1 ssse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fs.gsbase tsc_adjust bmi1 hle avx2
smep bmi2 erms invpcid cmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clfushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsxveop xsave xgetbv1
xlsaves cmq_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local split_lock_detect wboinvd
from irda arat pln pts avx512vmbi umip pku ospke avx512_vmbi2 gfn vaes vclmulqsd
avx512_vnni avx512_bitalg tme avx512 vpvopcnteq 1a57 rdpid md_clear pconfig flush_l1d
arch_capabilities

/platform/cpuinfo cache data
cache size : 36864 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46
node 0 size: 246269 MB
node 0 free: 229579 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47
node 1 size: 247512 MB

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

Dell Inc.

PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)

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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** May-2021  
**Hardware Availability:** May-2021  
**Software Availability:** Feb-2021

---

**Platform Notes (Continued)**

- node 1 free: 256730 MB
- node distances:
  - node 0 1
  - 0: 10 20
  - 1: 20 10

From `/proc/meminfo`
- MemTotal: 527811948 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

/sbin/tuned-adm active
- Current active profile: throughput-performance

From `/etc/*release*` /etc/*version*

- os-release:
  - NAME="Red Hat Enterprise Linux"
  - VERSION="8.3 (Ootpa)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="8.3"
  - PLATFORM_ID="platform:el8"
  - PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
  - ANSI_COLOR="0;31"

- redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
- system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
- Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

**Kernel self-reported vulnerability status:**

- CVE-2018-12207 (iTLB Multihit): Not affected
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: userscopy/swapsgs barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

(Continued on next page)
Dell Inc.
PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)

SPECspeed®2017_int_base = 11.8
SPECspeed®2017_int_peak = 12.1

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: May-2021
Hardware Availability: May-2021
Tested by: Dell Inc.
Software Availability: Feb-2021

Platform Notes (Continued)
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
run-level 5 May 31 09:35
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 225G 13G 213G 6% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R750xa
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x 002C069D002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200
16x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.2.2
BIOS Date: 05/14/2021
BIOS Revision: 1.2

(End of data from sysinfo program)

Compiler Version Notes
==============================================================================
C | 600.perlbench_s(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113

(Continued on next page)
Dell Inc.  
PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)  

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**Test Sponsor:** Dell Inc.  
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**Hardware Availability:** May-2021  
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**Software Availability:** Feb-2021

---

**Compiler Version Notes (Continued)**

---

```
C       | 600.perlbench_s(peak)
```

---

```
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

---

```
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
```

---

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

---

```
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
```

---

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

---

```
Fortran | 648.exchange2_s(base, peak)
```

---

```
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

---

**Base Compiler Invocation**

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

(Continued on next page)
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CPU2017 License: 55
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Tested by: Dell Inc.

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -ftlo -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-\L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-ftlo -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-\L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-\lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

(Continued on next page)
Dell Inc.
PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)

SPEC®2017_int_base = 11.8
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<td>Feb-2021</td>
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</table>

Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Peak Compiler Invocation (Continued)

600.perlbench_s: icc

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-stdlib=libc++
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdatalib -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-stdlib=libc++
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-stdlib=libc++
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

(Continued on next page)
## SPEC CPU®2017 Integer Speed Result

### Dell Inc.

**Dell Inc.**

PowerEdge R750xa (Intel Xeon Gold 6342, 2.80 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>11.8</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>12.1</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Date:** May-2021  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Hardware Availability:** May-2021  
**Software Availability:** Feb-2021

### Peak Optimization Flags (Continued)

- 620.omnetpp_s: basepeak = yes
- 623.xalancbmk_s: basepeak = yes
- 631.deepsjeng_s: basepeak = yes
- 641.leela_s: basepeak = yes
- Fortran benchmarks:
  - 648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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Tested with SPEC CPU®2017 v1.1.5 on 2021-05-30 21:36:33-0400.  
Originally published on 2021-07-06.