### New H3C Technologies Co., Ltd.

**H3C UniServer R6900 G5 (Intel Xeon Platinum 8354H)**

| Test Date: | Jun-2021 |
| Test Sponsor: | New H3C Technologies Co., Ltd. |
| Tested by: | New H3C Technologies Co., Ltd. |
| CPU2017 License: | 9066 |
| Hardware Availability: | Sep-2020 |
| Software Availability: | Dec-2020 |

#### SPEC CPU®2017 Integer Speed Result

| SPECspeed®2017_int_base | 11.8 |
| SPECspeed®2017_int_peak | 12.1 |

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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<tbody>
<tr>
<td>perlbench_s</td>
<td>72</td>
<td>7.17</td>
<td>12.1</td>
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<tr>
<td>gcc_s</td>
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<td>9.98</td>
<td>19.4</td>
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<td>mcf_s</td>
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<td>10.4</td>
<td>14.8</td>
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<tr>
<td>omnetpp_s</td>
<td>72</td>
<td>8.87</td>
<td>17.7</td>
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<td>xalancbmk_s</td>
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<td>14.8</td>
<td>18.5</td>
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<td>x264_s</td>
<td>72</td>
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<td>26.0</td>
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<tr>
<td>deepsjeng_s</td>
<td>72</td>
<td>6.39</td>
<td>15.3</td>
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<tr>
<td>leela_s</td>
<td>72</td>
<td>5.41</td>
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<tr>
<td>exchange2_s</td>
<td>72</td>
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<tr>
<td>xz_s</td>
<td>72</td>
<td>26.0</td>
<td></td>
</tr>
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</table>

### CPU Name:

Intel Xeon Platinum 8354H

### Max MHz:

4300

### Nominal:

3100

### Enabled:

72 cores, 4 chips

### Orderable:

1,2,3,4 chips

### Cache L1:

32 KB I + 32 KB D on chip per core

### L2:

1 MB I+D on chip per core

### L3:

24.75 MB I+D on chip per chip

### Memory:

768 GB (48 x 16 GB 2Rx8 PC4-3200V-R)

### Storage:

1 x 1.0 TB SATA SSD

### Other:

None

### OS:

Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64

### Compiler:

C/C++: Version 2021.1 of Intel oneAPI

### Compiler Build:

20201113 for Linux;

Fortran: Version 2021.1 of Intel Fortran Compiler

### Classic Build:

20201112 for Linux;

### Parallel:

Yes

### Firmware:

Version 5.15 released Mar-2021 BIOS

### File System:

xfs

### System State:

Run level 3 (multi-user)

### Base Pointers:

64-bit

### Peak Pointers:

64-bit

### Other:

jemalloc memory allocator V5.0.1

### Power Management:

BIOS set to prefer performance at the cost of additional power usage.
**SPEC CPU®2017 Integer Speed Result**

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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
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<th>Seconds</th>
<th>Ratio</th>
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<td>400</td>
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<td>72</td>
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<td>184</td>
<td>8.87</td>
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<td>240</td>
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<td>237</td>
<td>26.0</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"  
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"  
Malloc_CONF = "retain:true"  
OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9–7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3>/proc/sys/vm/drop_caches  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
New H3C Technologies Co., Ltd.  
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**SPECspeed®2017_int_base = 11.8**

**SPECspeed®2017_int_peak = 12.1**

**General Notes (Continued)**


**Platform Notes**

BIOS Settings:
Set Hyper-Threading to Disabled
Set Power Performance Tuning to BIOS Controls EPB
Set Energy Performance BIAS to Performance
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acfc64d
running on localhost.localdomain Fri Jun 11 18:40:05 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```plaintext
model name : Intel(R) Xeon(R) Platinum 8354H CPU @ 3.10GHz
  4 "physical id"s (chips)
  72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
siblings : 18
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
```

From lscpu from util-linux 2.32.1:

```plaintext
Architecture:        x86_64
CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              72
On-line CPU(s) list: 0-71
Thread(s) per core:  1
Core(s) per socket:  18
Socket(s):           4
NUMA node(s):        4
Vendor ID:           GenuineIntel
CPU family:          6
Model:               85
Model name:          Intel(R) Xeon(R) Platinum 8354H CPU @ 3.10GHz
Stepping:            11
```

(Continued on next page)
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**SPECspeed®2017_int_base = 11.8**  
**SPECspeed®2017_int_peak = 12.1**  

---  

**Platform Notes (Continued)**

- CPU MHz: 1891.262  
- CPU max MHz: 4300.0000  
- CPU min MHz: 1000.0000  
- BogomIPS: 6200.00  
- Virtualization: VT-x  
- L1d cache: 32K  
- L1i cache: 32K  
- L2 cache: 1024K  
- L3 cache: 25344K  
- NUMA node0 CPU(s): 0-17  
- NUMA node1 CPU(s): 18-35  
- NUMA node2 CPU(s): 36-53  
- NUMA node3 CPU(s): 54-71  
- Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abml dmab lg2aprefetch cpuid_fault epb cat_l3 cdpl3 invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmm flexpriority tpd fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2  
- /proc/cpuinfo cache data  
- cache size: 25344 KB  

From numactl --hardware  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
- available: 4 nodes (0-3)  
- node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17  
- node 0 size: 191857 MB  
- node 0 free: 191334 MB  
- node 1 cpus: 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35  
- node 1 size: 193531 MB  
- node 1 free: 193092 MB  
- node 2 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53  
- node 2 size: 193504 MB  
- node 2 free: 191290 MB  
- node 3 cpus: 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71  
- node 3 size: 193531 MB  
- node 3 free: 193259 MB  
- node distances:  
  - node 0 1 2 3  
  - 0: 10 20 20 20  

(Continued on next page)
New H3C Technologies Co., Ltd.

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CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.  
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Test Date: Jun-2021  
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Platform Notes (Continued)

1:  20 10 20 20  
2:  20 20 10 20  
3:  20 20 20 10

From /proc/meminfo  
MemTotal: 790962740 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

/sbin/tuned-adm active  
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*  
NAME="Red Hat Enterprise Linux"  
VERSION="8.2 (Ootpa)"  
ID="rhel"  
ID_LIKE="fedora"  
VERSION_ID="8.2"  
PLATFORM_ID="platform:el8"  
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"  
ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)  
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:  
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020  
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitation
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.

H3C UniServer R6900 G5 (Intel Xeon Platinum 8354H)

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SPECspeed®2017_int_base = 11.8
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Test Date: Jun-2021
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Platform Notes (Continued)

CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 11 18:30

SPEC is set to: /home/speccpu

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 876G 200G 676G 23% /home

From /sys/devices/virtual/dmi/id

Vendor: New H3C Technologies Co., Ltd.
Product: H3C UniServer R6900 G5
Product Family: SYSTEM_FAMILY
Serial: 210235A2RBH213000003

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
48x Micron 18ASF2G72PDZ-3G2E1 16 GB 2 rank 3200

BIOS:
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 5.15
BIOS Date: 03/01/2021
BIOS Revision: 5.19

(End of data from sysinfo program)

---

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113

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New H3C Technologies Co., Ltd.

H3C UniServer R6900 G5 (Intel Xeon Platinum 8354H)

**SPEC CPU®2017 Integer Speed Result**

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**Copyright 2017-2021 Standard Performance Evaluation Corporation**

**SPECspeed®2017_int_base = 11.8**

**SPECspeed®2017_int_peak = 12.1**

**Compiler Version Notes (Continued)**

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<tr>
<th>C</th>
<th>600.perlbench_s(peak)</th>
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Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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<th>C++</th>
<th>620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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---------------------------------------------------------------

**Base Compiler Invocation**

C benchmarks:
icx

C++ benchmarks:
icpx

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SPEC CPU®2017 Integer Speed Result
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New H3C Technologies Co., Ltd.
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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-03 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -03 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -03 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECspeed®2017_int_base = 11.8
H3C UniServer R6900 G5 (Intel Xeon Platinum 8354H) | SPECspeed®2017_int_peak = 12.1

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Jun-2021
Hardware Availability: Sep-2020
Software Availability: Dec-2020

Peak Compiler Invocation (Continued)

600.perlbench_s: icc
C++ benchmarks:
icpx
Fortran benchmarks:
ifort

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes
C++ benchmarks:

(Continued on next page)
New H3C Technologies Co., Ltd.  

H3C UniServer R6900 G5 (Intel Xeon Platinum 8354H)

SPECspeed®2017_int_base = 11.8
SPECspeed®2017_int_peak = 12.1

Peak Optimization Flags (Continued)

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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