 SPEC CPU®2017 Integer Rate Result

Dell Inc.
PowerEdge R650 (Intel Xeon Platinum 8352S, 2.20 GHz)

SPECraten®2017_int_base = 423
SPECraten®2017_int_peak = 440

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

Hardware

CPU Name: Intel Xeon Platinum 8352S
Max MHz: 3400
Nominal: 2200
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 48 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
Storage: 225 GB on tmpfs
Other: None

Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)
4.18.0-240.15.1.el8_3.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
Parallel: No
Firmware: Version 1.1.2 released Apr-2021
File System: tmpfs
System State: Run level 5 (graphical multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

jemalloc memory allocator V5.0.1
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>128</td>
<td>678</td>
<td>301</td>
<td>678</td>
<td>301</td>
<td>128</td>
<td>583</td>
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<tr>
<td>502.gcc_r</td>
<td>128</td>
<td>547</td>
<td>331</td>
<td>549</td>
<td>330</td>
<td>128</td>
<td>456</td>
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<tr>
<td>505.mcf_r</td>
<td>128</td>
<td>304</td>
<td>679</td>
<td>304</td>
<td>680</td>
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<td>304</td>
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<tr>
<td>520.omnetpp_r</td>
<td>128</td>
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<td>248</td>
<td>675</td>
<td>249</td>
<td>128</td>
<td>676</td>
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<tr>
<td>523.xalancbmk_r</td>
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<td>259</td>
<td>522</td>
<td>258</td>
<td>523</td>
<td>128</td>
<td>259</td>
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<tr>
<td>525.x264_r</td>
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<td>897</td>
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<td>898</td>
<td>128</td>
<td>238</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
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<td>440</td>
<td>333</td>
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<tr>
<td>541.leela_r</td>
<td>128</td>
<td>644</td>
<td>329</td>
<td>644</td>
<td>329</td>
<td>128</td>
<td>644</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>128</td>
<td>379</td>
<td>886</td>
<td>376</td>
<td>892</td>
<td>128</td>
<td>379</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>128</td>
<td>570</td>
<td>242</td>
<td>570</td>
<td>242</td>
<td>128</td>
<td>570</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
"/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```
sync; echo 3>/proc/sys/vm/drop_caches
```
General Notes (Continued)

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
   Sub NUMA Cluster : 2-Way Clustering
   Virtualization Technology : Disabled

   System Profile : Custom
   CPU Power Management : Maximum Performance
   C1E : Disabled
   C States : Autonomous
   Memory Patrol Scrub : Disabled
   Energy Efficiency Policy : Performance
   CPU Interconnect Bus Link
   Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Thu May  6 09:56:49 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8352S CPU @ 2.20GHz
   2  "physical id"s (chips)
   128 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
   cpu cores : 32

(Continued on next page)
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Platform Notes (Continued)

siblings : 64
physical 0: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31
physical 1: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 128
On-line CPU(s) list: 0-127
Thread(s) per core: 2
Core(s) per socket: 32
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8352S CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2800.000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s):
0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84,88,92,96,100,104,108,
112,116,120,124
NUMA node1 CPU(s):
2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86,90,94,98,102,106,110
,114,118,122,126
NUMA node2 CPU(s):
1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85,89,93,97,101,105,109,
113,117,121,125
NUMA node3 CPU(s):
,115,119,123,127
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdxtsc
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrivr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_pнии ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2

(Continued on next page)
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: May-2021
Software Availability: Feb-2021

**Platform Notes (Continued)**

```
smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma 
clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 
xsaves cqm_llc cqm_occ_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd 
dtherm ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 qfn1 vaes vpclmulqdq 
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d 
arch_capabilities

/process/cpuintinfo cache data
cache size : 49152 KB
```

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus:  0  4  8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76 80 84 88 92 96
  100 104 108 112 116 120 124
node 0 size: 125061 MB
node 0 free: 126768 MB
node 1 cpus:  2  6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86 90 94 98
  102 106 110 114 118 122 126
node 1 size: 125563 MB
node 1 free: 128226 MB
node 2 cpus:  1  5  9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77 81 85 89 93 97
  101 105 109 113 117 121 125
node 2 size: 125563 MB
node 2 free: 128562 MB
node 3 cpus:  3  7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79 83 87 91 95 99
  103 107 111 115 119 123 127
node 3 size: 125884 MB
node 3 free: 114132 MB
node distances:
  node  0   1   2   3
  0:  10  11  20  20
  1:  11  10  20  20
  2:  20  20  10  11
  3:  20  20  11  10

From `/proc/meminfo`
MemTotal: 527794132 kB
HugePages_Total:    0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
```

(Continued on next page)
## Platform Notes (Continued)

```
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
```

```
uname -a:
 Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

<table>
<thead>
<tr>
<th>CVE-18-2207 (iTLB Multihit):</th>
<th>Not affected</th>
</tr>
</thead>
<tbody>
<tr>
<td>CVE-18-3620 (L1 Terminal Fault):</td>
<td>Not affected</td>
</tr>
<tr>
<td>Microarchitectural Data Sampling:</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-17-5754 (Meltdown):</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-18-3639 (Speculative Store Bypass):</td>
<td>Mitigation: Speculative Store Bypass disabled via prctl and seccomp</td>
</tr>
<tr>
<td></td>
<td>Mitigation: usercopy/swapsgs barriers and __user pointer sanitization</td>
</tr>
<tr>
<td>CVE-17-5753 (Spectre variant 1):</td>
<td>Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling</td>
</tr>
<tr>
<td>CVE-17-5715 (Spectre variant 2):</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-20-0543 (Special Register Buffer Data Sampling):</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-19-11135 (TSX Asynchronous Abort):</td>
<td>Not affected</td>
</tr>
</tbody>
</table>

run-level 5 May 6 09:55

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1

```
Filesystem     Type    Size  Used  Avail Use% Mounted on
tmpfs          tmpfs   225G   6.9G  219G   4% /mnt/ramdisk
```

From /sys/devices/virtual/dmi/id

<table>
<thead>
<tr>
<th>Vendor:</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Product:</td>
<td>PowerEdge R650</td>
</tr>
<tr>
<td>Product Family:</td>
<td>PowerEdge</td>
</tr>
<tr>
<td>Serial:</td>
<td>1234567</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are
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Platform Notes (Continued)

frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
7x 00AD00B300AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200
9x 00AD063200AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200
16x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.2
BIOS Date: 04/09/2021
BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

C       | 500.perlbench_r(peak)
---------|------------------

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C       | 502.gcc_r(peak)
---------|------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C       | 500.perlbench_r(peak)
---------|------------------

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000

(Continued on next page)
Compiler Version Notes (Continued)

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 502.gcc_r(peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
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==============================================================================
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==============================================================================
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
(Continued on next page)
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Compiler Version Notes (Continued)
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r = -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r = -DSPEC_LP64
505.mcf_r = -DSPEC_LP64
520.omnetpp_r = -DSPEC_LP64
523.xalancmk_r = -DSPEC_LP64 -DSPEC_LINUX
525.x264_r = -DSPEC_LP64
531.deepsjeng_r = -DSPEC_LP64
541.leela_r = -DSPEC_LP64
548.exchange2_r = -DSPEC_LP64
557.xz_r = -DSPEC_LP64
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Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icx
500.perlbench_r: icc

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

(Continued on next page)
### Peak Portability Flags (Continued)

- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leet_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Peak Optimization Flags

#### C benchmarks:


- 505.mcf_r: basepeak = yes


- 557.xz_r: basepeak = yes

#### C++ benchmarks:

- 520.omnetpp_r: basepeak = yes
- 523.xalancbmk_r: basepeak = yes
- 531.deepsjeng_r: basepeak = yes
Dell Inc.
PowerEdge R650 (Intel Xeon Platinum 8352S, 2.20 GHz)

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Peak Optimization Flags (Continued)

541.leela_r: basepeak = yes

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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