## Dell Inc. PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** May-2021  
**Hardware Availability:** Apr-2021  
**Software Availability:** Dec-2020  

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base = 181</th>
<th>SPECrate®2017_fp_peak = 182</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dell Inc.</td>
<td></td>
</tr>
</tbody>
</table>

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<tr>
<th>503.bwaves_r</th>
<th>182</th>
<th>507.cactuBSSN_r</th>
</tr>
</thead>
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<td>108</td>
<td>112</td>
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</tr>
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<td></td>
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</tr>
</thead>
</table>

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</tr>
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<th>180</th>
</tr>
</thead>
</table>

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<tr>
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<th>180</th>
</tr>
</thead>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6334</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>3700</td>
</tr>
<tr>
<td>Nominal:</td>
<td>3600</td>
</tr>
<tr>
<td>Enabled:</td>
<td>16 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>18 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

| Memory:   | 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R) |
| Storage:  | 125 GB on tmpfs |
| Other:    | None |

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux 8.2 (Ootpa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Version 1.1.2 released Apr-2021</td>
</tr>
<tr>
<td>File System:</td>
<td>tmpfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>657</td>
<td>488</td>
<td>654</td>
<td>490</td>
<td>16</td>
<td>343</td>
<td>468</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>184</td>
<td>220</td>
<td>184</td>
<td>220</td>
<td>32</td>
<td>184</td>
<td>220</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td>282</td>
<td>108</td>
<td>282</td>
<td>108</td>
<td>32</td>
<td>282</td>
<td>108</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
<td>746</td>
<td>112</td>
<td>747</td>
<td>112</td>
<td>16</td>
<td>340</td>
<td>123</td>
<td>340</td>
<td>123</td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>459</td>
<td>163</td>
<td>459</td>
<td>163</td>
<td>32</td>
<td>397</td>
<td>188</td>
<td>399</td>
<td>187</td>
<td></td>
</tr>
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<td>519.lbm_r</td>
<td>32</td>
<td>187</td>
<td>180</td>
<td>187</td>
<td>180</td>
<td>32</td>
<td>187</td>
<td>180</td>
<td>187</td>
<td>180</td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
<td>419</td>
<td>171</td>
<td>415</td>
<td>173</td>
<td>16</td>
<td>255</td>
<td>140</td>
<td>253</td>
<td>142</td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>323</td>
<td>151</td>
<td>321</td>
<td>152</td>
<td>32</td>
<td>323</td>
<td>151</td>
<td>321</td>
<td>152</td>
<td></td>
</tr>
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<td>32</td>
<td>349</td>
<td>160</td>
<td>348</td>
<td>161</td>
<td>32</td>
<td>349</td>
<td>160</td>
<td>348</td>
<td>161</td>
<td></td>
</tr>
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<td>538.imagick_r</td>
<td>32</td>
<td>207</td>
<td>385</td>
<td>207</td>
<td>385</td>
<td>32</td>
<td>207</td>
<td>385</td>
<td>207</td>
<td>385</td>
<td></td>
</tr>
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<td>32</td>
<td>215</td>
<td>250</td>
<td>216</td>
<td>249</td>
<td>32</td>
<td>212</td>
<td>254</td>
<td>213</td>
<td>252</td>
<td></td>
</tr>
<tr>
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<td>32</td>
<td>782</td>
<td>160</td>
<td>783</td>
<td>159</td>
<td>32</td>
<td>782</td>
<td>160</td>
<td>783</td>
<td>159</td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>554</td>
<td>91.7</td>
<td>557</td>
<td>91.3</td>
<td>16</td>
<td>257</td>
<td>98.9</td>
<td>255</td>
<td>99.8</td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
    "/mnt/ramdisk/cpu2017-1.1.1-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.1-ic2021.1/je5.0.1-64"

MALLOCONF = "retain:true"
```

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 181
SPECrate®2017_fp_peak = 182

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: May-2021
Tested by: Dell Inc.
Hardware Availability: Apr-2021
Software Availability: Dec-2020

General Notes (Continued)

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3>       /proc/sys/vm/drop_caches
    runcpu command invoked through numactl i.e.:
    numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
- Sub NUMA Cluster : 2-Way Clustering
- Virtualization Technology : Disabled

- System Profile : Custom
  - CPU Power Management : Maximum Performance
  - C1E : Disabled
  - C States : Autonomous
  - Memory Patrol Scrub : Disabled
  - Energy Efficiency Policy : Performance
  - CPU Interconnect Bus Link Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Tue May  4 06:55:52 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  - model name : Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

```
CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
```

```
Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020
```

**Platform Notes (Continued)**

2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
Architectures: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz
Stepping: 6
CPU MHz: 800.818
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 18432K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28
NUMA node1 CPU(s): 2,6,10,14,18,22,26,30
NUMA node2 CPU(s): 1,5,9,13,17,21,25,29
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrn pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmmi flexpriority ept vpid fsgsbse
	tsc_adjust bmi1 hle avx2 smep bmi2 ibrms invpcid rtm cqm rdt_a aperfmperf
	bts bmi2 ibrms invpcid rtm cqm rdt_a avx512f avx512dq
	rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw
	avx512vl xsaveopt xsaves xsavec xgetbv1 xsavevs cqm_llc cqm_occput llc cqm_mbm_total
	cqm_mbm_local wbinvd dtherm ida arat pln pts avx512vmbmi umip pku ospke
	avx512_vmbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
(Continued on next page)
Dell Inc.  

PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

SPEC CPU®2017 Floating Point Rate Result

SPECrate®2017_fp_peak = 182
SPECrate®2017_fp_base = 181

CPU2017 License: 55
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Software Availability: Dec-2020

Platform Notes (Continued)

la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size : 18432 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
  node 0 cpus: 0 4 8 12 16 20 24 28
  node 0 size: 128415 MB
  node 0 free: 120208 MB
  node 1 cpus: 2 6 10 14 18 22 26 30
  node 1 size: 128994 MB
  node 1 free: 125533 MB
  node 2 cpus: 1 5 9 13 17 21 25 29
  node 2 size: 129021 MB
  node 2 free: 116488 MB
  node 3 cpus: 3 7 11 15 19 23 27 31
  node 3 size: 129019 MB
  node 3 free: 125640 MB
  node distances:
    node   0   1   2   3
    0:  10  11  20  20
    1:  11  10  20  20
    2:  20  20  10  11
    3:  20  20  11  10

From /proc/meminfo
  MemTotal:       527822548 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release*/etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.2 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.2"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

(Continued on next page)
# SPEC CPU®2017 Floating Point Rate Result

## Dell Inc.

**PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)**

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<tr>
<th>SPECrate®2017_fp_base</th>
<th>SPECrate®2017_fp_peak</th>
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</tr>
</tbody>
</table>

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### Platform Notes (Continued)

```
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
    Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

<table>
<thead>
<tr>
<th>Vulnerability</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>CVE-2018-12207 (iTlb Multihit)</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2018-3620 (L1 Terminal Fault)</td>
<td>Not affected</td>
</tr>
<tr>
<td>Microarchitectural Data Sampling</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2017-5754 (Meltdown)</td>
<td>Not affected</td>
</tr>
<tr>
<td>CVE-2018-3639 (Speculative Store Bypass)</td>
<td>Mitigation: Speculative Store Bypass disabled via prctl and seccomp</td>
</tr>
<tr>
<td>CVE-2017-5753 (Spectre variant 1)</td>
<td>Mitigation: usecory/swapgs barriers and __user pointer sanitation</td>
</tr>
<tr>
<td>CVE-2017-5715 (Spectre variant 2)</td>
<td>Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling</td>
</tr>
<tr>
<td>CVE-2020-0543 (Special Register Buffer Data Sampling):</td>
<td>No status reported</td>
</tr>
<tr>
<td>CVE-2019-11135 (TSX Asynchronous Abort):</td>
<td>Not affected</td>
</tr>
</tbody>
</table>

run-level 3 May 4 02:41

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1

```

From /sys/devices/virtual/dmi/id

<table>
<thead>
<tr>
<th>Field</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Vendor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Product</td>
<td>PowerEdge MX750c</td>
</tr>
<tr>
<td>Product Family</td>
<td>PowerEdge</td>
</tr>
<tr>
<td>Serial</td>
<td>1234567</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

1x 002C00B3002C 18ASF4G72PD2-3G2E1 32 GB 2 rank 3200
15x 00AD063200AD HMAA4GR7A08R8N-XN 32 GB 2 rank 3200
16x Not Specified Not Specified

BIOS:

<table>
<thead>
<tr>
<th>Field</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>BIOS Vendor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>BIOS Version</td>
<td>1.1.2</td>
</tr>
</tbody>
</table>

(Continued on next page)
Dell Inc.

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SPECrater®2017_fp_peak = 182

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Platform Notes (Continued)

BIOS Date: 04/09/2021

BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) |
|                 | 544.nab_r(base, peak) |

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C++             | 508.namd_r(base, peak) 510.parest_r(base, peak) |

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C++, C          | 511.povray_r(peak) |

Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C++, C          | 511.povray_r(base) 526.blender_r(base, peak) |

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
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Compiler Version Notes (Continued)

C++, C | 511.povray_r(peak)

Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
554.roms_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
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Fortran, C | 521.wrf_r(peak)

(Continued on next page)
# SPEC CPU®2017 Floating Point Rate Result

## Dell Inc.

### SPECrate®2017_fp_base = 181

### SPECrate®2017_fp_peak = 182

<table>
<thead>
<tr>
<th>Dell Inc.</th>
<th>PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)</th>
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<tbody>
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### Base Compiler Invocation

C benchmarks:

icx

(Continued on next page)
Dell Inc. | SPEC CPU®2017 Floating Point Rate Result
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**Base Compiler Invocation (Continued)**

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

---

**Base Portability Flags**

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbraces-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)
**SPEC CPU®2017 Floating Point Rate Result**

**Dell Inc.**

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### Base Optimization Flags (Continued)

**C++ benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto`
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Fortran benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div`
- `-qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte -auto`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both Fortran and C:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both C and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using Fortran, C, and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

### Peak Compiler Invocation

**C benchmarks:**
- `icx`

**C++ benchmarks:**
- `icpx`

(Continued on next page)
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PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

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SPECrater®2017_fp_peak = 182

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Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
521.wrf_r: ifort icc
527.cam4_r: ifort icx

Benchmarks using both C and C++:
511.povray_r: icpc icc
526.blender_r: icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes

C++ benchmarks:
508.namd_r: basepeak = yes
510.parest_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops

(Continued on next page)
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Peak Optimization Flags (Continued)

510.parest_r (continued):
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

549.fotonik3d_r: basepeak = yes

554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:

521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-nostandard-realloc-lhs -align array32byte -auto
-LO/usr/local/jemalloc64-5.0.1/lib -ljemalloc

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-LO/usr/local/jemalloc64-5.0.1/lib -ljemalloc

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at
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You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.7 on 2021-05-04 07:55:50-0400.
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