# Lenovo Global Technology

ThinkSystem SR650 V2
(2.40 GHz, Intel Xeon Platinum 8368)

<table>
<thead>
<tr>
<th>SPEC CPU 2017 Floating Point Rate Result</th>
<th>Lenovo Global Technology</th>
<th>SPECrate\textsuperscript{\textregistered}2017_fp_base = 467</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Date:</td>
<td>May-2021</td>
<td>SPECrate\textsuperscript{\textregistered}2017_fp_peak = Not Run</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2021</td>
<td></td>
</tr>
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<td>Feb-2021</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate\textsuperscript{\textregistered}2017_fp_base (467)</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r 76</td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r 76</td>
<td></td>
</tr>
<tr>
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<td></td>
</tr>
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<td>510.parest_r 76</td>
<td></td>
</tr>
<tr>
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<td></td>
</tr>
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<td></td>
</tr>
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<td></td>
</tr>
<tr>
<td>526.blender_r 76</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r 76</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r 76</td>
<td></td>
</tr>
<tr>
<td>544.nab_r 76</td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r 76</td>
<td></td>
</tr>
<tr>
<td>554.roms_r 76</td>
<td></td>
</tr>
</tbody>
</table>

## Hardware

- **CPU Name:** Intel Xeon Platinum 8368
- **Max MHz:** 3400
- **Nominal:** 2400
- **Enabled:** 76 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 57 MB I+D on chip per chip
- **Other:** None
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

## Software

- **OS:** SUSE Linux Enterprise Server 15 SP2 (x86_64) Kernel 5.3.18-22-default
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
- **Parallel:** No
- **Firmware:** Lenovo BIOS Version AFE109PT1 1.00 released Apr-2021
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
Lenovo Global Technology
ThinkSystem SR650 V2
(2.40 GHz, Intel Xeon Platinum 8368)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: May-2021
Hardware Availability: Jul-2021
Software Availability: Feb-2021

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>76</td>
<td>1043</td>
<td>730</td>
<td>1043</td>
<td>730</td>
<td>1043</td>
<td>731</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>76</td>
<td>148</td>
<td>650</td>
<td>147</td>
<td>654</td>
<td>147</td>
<td>653</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>76</td>
<td>171</td>
<td>423</td>
<td>173</td>
<td>417</td>
<td>167</td>
<td>432</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>76</td>
<td>679</td>
<td>293</td>
<td>679</td>
<td>293</td>
<td>680</td>
<td>293</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>76</td>
<td>272</td>
<td>653</td>
<td>273</td>
<td>650</td>
<td>273</td>
<td>649</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>76</td>
<td>297</td>
<td>270</td>
<td>299</td>
<td>268</td>
<td>298</td>
<td>269</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>76</td>
<td>448</td>
<td>380</td>
<td>447</td>
<td>381</td>
<td>446</td>
<td>382</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>76</td>
<td>240</td>
<td>482</td>
<td>240</td>
<td>483</td>
<td>240</td>
<td>482</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>76</td>
<td>251</td>
<td>530</td>
<td>251</td>
<td>530</td>
<td>253</td>
<td>525</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>76</td>
<td>139</td>
<td>1360</td>
<td>138</td>
<td>1370</td>
<td>140</td>
<td>1350</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>76</td>
<td>165</td>
<td>775</td>
<td>165</td>
<td>777</td>
<td>166</td>
<td>772</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>76</td>
<td>1268</td>
<td>234</td>
<td>1269</td>
<td>233</td>
<td>1271</td>
<td>233</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>76</td>
<td>603</td>
<td>200</td>
<td>598</td>
<td>202</td>
<td>600</td>
<td>201</td>
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SPECrate®2017_fp_base = 467
SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/home/cpu2017-1.1.5-ic2021.1-revA-update1/lib/intel64:/home/cpu2017-1.1.5-ic2021.1-revA-update1/je5.0.1-64"
MALLOCS_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7940X CPU + 64GB RAM memory using openSUSE Leap 15.2
Transparent Huge Pages enabled by default

(Continued on next page)
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General Notes (Continued)
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes
BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disabled
DCU Streamer Prefetcher set to Disabled
DCU IP Prefetcher set to Disabled
Patrol Scrub set to Disabled

Sysinfo program /home/cpu2017-1.1.5-ic2021.1-revA-update1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost Mon May 10 07:19:08 2021
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8368 CPU @ 2.40GHz
    2 "physical id"s (chips)
      76 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 38
    siblings : 38
    physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37
    physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37

From lscpu:
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

 SPECrate®2017_fp_base = 467
 SPECrate®2017_fp_peak = Not Run

Test date: May-2021
Hardware Availability: Jul-2021
Software Availability: Feb-2021

Platform Notes (Continued)

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 46 bits physical, 57 bits virtual
CPU(s): 76
On-line CPU(s) list: 0-75
Thread(s) per core: 1
Core(s) per socket: 38
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8368 CPU @ 2.40GHz
Stepping: 6
CPU Mhz: 3200.000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 58368K
NUMA node0 CPU(s): 0-37
NUMA node1 CPU(s): 38-75
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pbe goodnop placel3topology nonstop_tsc cpuid
aperfmpref pfni pcmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 ssse fma cx16 xtpr pdcm
rdseed adx smap avx2 smep bmi2 64bit erms invpcid flexpriority ept vpid ept_ad fsgsbase
fdesc_adjust bmi1 hle avx2 smep bmi2 64bit erms invpcid rtm cgx rdrt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw
avx512vl xsaveopt xsavec xgetbv1 xsaveas cgx llc cgx_min llc cgx_min_total
cgx_local cgx_local cgx_local cgx_local cgx_local cgx_local cgx_local cgx_local cgx_local
la57 rdpid md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31 32 33 34 35 36 37
node 0 size: 515677 MB

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Platform Notes (Continued)

node 0 free: 515135 MB
node 1 cpus: 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62
63 64 65 66 67 68 69 70 71 72 73 74 75
node 1 size: 516049 MB
node 1 free: 515453 MB
node distances:
node 0 1
  0: 10 20
  1: 20 10

From /proc/meminfo
MemTotal: 1056488632 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 15 SP2

From /etc/*release* /etc/*version*
os-release:
  NAME="SLES"
  VERSION="15-SP2"
  VERSION_ID="15.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15:sp2"

uname -a:
  Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

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Platform Notes (Continued)

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 May 10 07:18

SPEC is set to: /home/cpu2017-1.1.5-ic2021.1-revA-update1
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb2 xfs 893G 82G 812G 10% /

From /sys/devices/virtual/dmi/id
Vendor: Lenovo
Product: ThinkSystem SR650 V2 MB
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200

BIOS:
BIOS Vendor: Lenovo
BIOS Version: AFE109PT1-1.00
BIOS Date: 04/28/2021
BIOS Revision: 1.0
Firmware Revision: 1.0

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
==============================================================================
Intel(R) oneAPI DPC+/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

C++            | 508.namd_r(base) 510.parest_r(base)
==============================================================================
Intel(R) oneAPI DPC+/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113

(Continued on next page)
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Compiler Version Notes (Continued)

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C++, C | 511.povray_r(base) 526.blender_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
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## Base Compiler Invocation

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifort

Benchmarks using both Fortran and C:
- ifort icx

Benchmarks using both C and C++:
- icpx icx

Benchmarks using Fortran, C, and C++:
- icpx icx ifort

## Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64
- 507.cactuBSSN_r: -DSPEC_LP64
- 508.namd_r: -DSPEC_LP64
- 510.parest_r: -DSPEC_LP64
- 511.povray_r: -DSPEC_LP64
- 519.lbm_r: -DSPEC_LP64
- 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64

## Base Optimization Flags

C benchmarks:
- -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries -ljemalloc
- -L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)
Base Optimization Flags (Continued)

C++ benchmarks:
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto`
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

Fortran benchmarks:
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div`
- `-qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte -auto`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both Fortran and C:
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both C and C++:
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using Fortran, C, and C++:
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-D.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-D.xml
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
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SPEC CPU®2017 Floating Point Rate Result

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.5 on 2021-05-09 19:19:08-0400.