## SPEC CPU®2017 Floating Point Speed Result

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)

### SPECspeed®2017.fp_base = 215

### SPECspeed®2017.fp_peak = 216

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date</td>
<td>Apr-2021</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Apr-2021</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Feb-2021</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017.fp_base (215)</th>
<th>SPECspeed®2017.fp_peak (216)</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s 64</td>
<td>734</td>
<td>738</td>
</tr>
<tr>
<td>607.cactuBSSN_s 64</td>
<td>264</td>
<td>734</td>
</tr>
<tr>
<td>619.lbm_s 64</td>
<td>136</td>
<td>199</td>
</tr>
<tr>
<td>621.wrf_s 64</td>
<td>208</td>
<td>199</td>
</tr>
<tr>
<td>627.cam4_s 64</td>
<td>161</td>
<td>161</td>
</tr>
<tr>
<td>628.pop2_s 64</td>
<td>87.6</td>
<td>204</td>
</tr>
<tr>
<td>638.imagick_s 64</td>
<td>204</td>
<td>425</td>
</tr>
<tr>
<td>644.nab_s 64</td>
<td>472</td>
<td>472</td>
</tr>
<tr>
<td>649.fotonik3d_s 64</td>
<td>274</td>
<td>274</td>
</tr>
<tr>
<td>654.roms_s 64</td>
<td>115</td>
<td>115</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Platinum 8358P
- **Max MHz:** 3400
- **Nominal:** 2600
- **Enabled:** 64 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 48 MB I+D on chip per chip
- **Other:** None
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)
- **Storage:** 125 GB on tmpfs
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa) 4.18.0-240.el8.x86_64
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
  C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 1.1.2 released Apr-2021
- **File System:** tmpfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed®2017_fp_base = 215
SPECspeed®2017_fp_peak = 216

Results Table

| Benchmark     | Threads | Seconds | Ratio | Threads | Seconds | Ratio | Seconds | Ratio 
|---------------|---------|---------|-------|---------|---------|-------|---------|-------
| 603.bwaves_s  | 64      | 79.9    | 80.0  | 738     | 80.0    | 738   |
| 607.cactuBSSN_s | 64   | 62.7    | 63.1  | 266     | 63.1    | 264   |
| 619.lbm_s     | 64      | 38.4    | 35.8  | 136     | 35.8    | 146   |
| 621.wrf_s     | 64      | 63.4    | 63.7  | 209     | 63.7    | 208   |
| 627.cam4_s    | 64      | 55.2    | 55.1  | 161     | 55.1    | 161   |
| 628.pop2_s    | 64      | 135     | 135   | 87.6    | 135     | 87.6  |
| 638.imagick_s | 64      | 70.5    | 70.8  | 205     | 70.8    | 204   |
| 644.nab_s     | 64      | 41.1    | 40.9  | 425     | 40.9    | 427   |
| 649.fotonik3d_s | 64 | 79.3    | 79.2  | 115     | 79.2    | 115   |
| 654.roms_s    | 64      | 57.5    | 57.1  | 274     | 57.1    | 276   |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1.2/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1.2/lib/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Specspeed®2017_fp_peak = 216
Specspeed®2017_fp_base = 215

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

General Notes (Continued)

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
Logical Processor : Disabled
Virtualization Technology : Disabled

System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C States : Autonomous
Memory Patrol Scrub : Disabled
Energy Efficiency Policy : Performance
CPU Interconnect Bus Link
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Thu Apr 22 06:39:43 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8358P CPU @ 2.60GHz
2 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.

PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>215</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>216</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

<table>
<thead>
<tr>
<th>Test Date</th>
<th>Apr-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability: Apr-2021</td>
<td></td>
</tr>
<tr>
<td>Software Availability: Feb-2021</td>
<td></td>
</tr>
</tbody>
</table>

Platform Notes (Continued)

From lscpu:

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 64
- On-line CPU(s) list: 0-63
- Thread(s) per core: 1
- Core(s) per socket: 32
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 106
- Model name: Intel(R) Xeon(R) Platinum 8358P CPU @ 2.60GHz
- Stepping: 6
- CPU MHz: 1118.333
- BogoMIPS: 5200.00
- Virtualization: VT-x
- L1d cache: 48K
- L1i cache: 32K
- L2 cache: 1280K
- L3 cache: 49152K
- NUMA node0 CPU(s): 0-31
- NUMA node1 CPU(s): 32-63

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpte1gb rdtsscp
lm constant_64_kta arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm tsx_tie xsave ctors smt cmov0 cx16 xtpr pdcm pcid dca
sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pinn ssbd
mba ibrs ibpb stibp ibrs_enhanced fsbgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid cmcm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clfushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsavesopt xsavec
xgetbv1 xsaves cmqllc cmq_occup_llc cmq_mbb_total cmq_mbb_local split_lock_detect
wbinvd dtherm ida arat pln pts avx512vbmcl umip pku ospke avx512_vbmi2 gfn1 vaes
cpumulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear
pconfig flush_lld
arch_capabilities

/proc/cpuinfo cache data

- cache size: 49152 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond
to a physical chip.

- available: 2 nodes (0-1)

- node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
- node 0 size: 489620 MB

(Continued on next page)
### Platform Notes (Continued)

- Node 0:
  - Free memory: 504770 MB
- Node 1:
  - CPU cores: 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63
  - Node size: 488625 MB
  - Free memory: 509998 MB
- Node distances:
  - Node 0: 10, 20
  - Node 1: 20, 10

From `/proc/meminfo`
- `MemTotal`: 1056288880 kB
- `HugePages_Total`: 0
- `Hugepagesize`: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From `/etc/*release*` and `/etc/*version*`

```plaintext
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
```

uname -a:
```
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- **CVE-2018-12207 (iTLB Multihit):** Not affected
- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swaps barriers and __user pointer
Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2):  
sanitization  
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Apr 22 03:37

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1
Filesystem     Type   Size  Used Avail Use% Mounted on
tmpfs          tmpfs  125G   11G  115G   9% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor:         Dell Inc.
Product:        PowerEdge MX750c
Product Family: PowerEdge
Serial:         1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x 00AD063200AD HMMA8GR7AJR4N-XN 64 GB 2 rank 3200
16x Not Specified Not Specified

BIOS:
BIOS Vendor:    Dell Inc.
BIOS Version:   1.1.2
BIOS Date:      04/09/2021
BIOS Revision:  1.1

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
<table>
<thead>
<tr>
<th>C</th>
<th>619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)</th>
</tr>
</thead>
</table>
 -----------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2021
Hardware Availability: Apr-2021
Software Availability: Feb-2021

Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>C</th>
<th>644.nab_s(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>C</th>
<th>619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>C</th>
<th>644.nab_s(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>C++, C, Fortran</th>
<th>607.cactuBSSN_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fortran</th>
<th>603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Specspeed®2017_fp_base = 215**  
**Specspeed®2017_fp_peak = 216**

---

### Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>Fortran, C</th>
<th>621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000 Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

---

### Base Compiler Invocation

**C benchmarks:**

- icc

**Fortran benchmarks:**

- ifort

**Benchmarks using both Fortran and C:**

- ifort icc

**Benchmarks using Fortran, C, and C++:**

- icpc icc ifort

---

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64
Base Optimization Flags

C benchmarks:
- m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- mbranches-within-32B-boundaries

Fortran benchmarks:
- m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
- no-prec-div -qopt-prefetch -ffinite-math-only
- qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
- mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
- ljemalloc

Benchmarks using both Fortran and C:
- m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
- m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

644.nab_s: icx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
**SPEC CPU®2017 Floating Point Speed Result**

**Dell Inc.**
PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base = 215</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak = 216</td>
</tr>
</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes


Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

(Continued on next page)
Dell Inc.  
PowerEdge MX750c (Intel Xeon Platinum 8358P, 2.60 GHz)  

| SPECspeed®2017_fp_base = 215 |
| SPECspeed®2017_fp_peak = 216 |

| CPU2017 License: 55 | Test Date: | Apr-2021 |
| Test Sponsor: Dell Inc. | Hardware Availability: | Apr-2021 |
| Tested by: Dell Inc. | Software Availability: | Feb-2021 |

**Peak Optimization Flags (Continued)**

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.7 on 2021-04-22 06:39:42-0400.
Originally published on 2021-05-18.