SPEC CPU®2017 Integer Speed Result

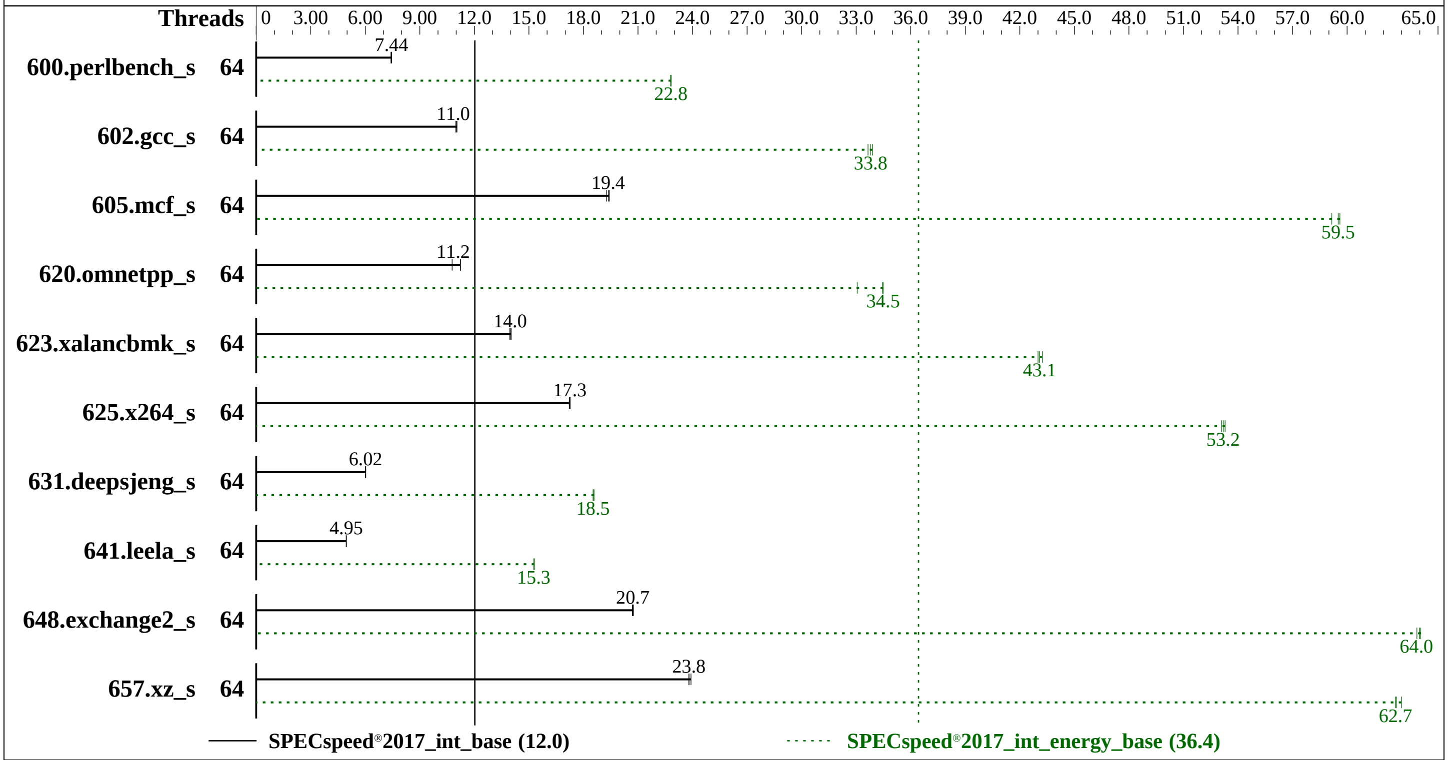
Fujitsu

PRIMERGY RX2540 M6, Intel Xeon Gold 6346, 3.10GHz

SPECspeed®2017_int_base =	12.0
SPECspeed®2017_int_energy_base =	36.4
SPECspeed®2017_int_peak =	Not Run
SPECspeed®2017_int_energy_peak =	Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2021
Hardware Availability: May-2021
Software Availability: Aug-2020



Hardware

CPU Name:	Intel Xeon Gold 6346
Max MHz:	3600
Nominal:	3100
Enabled:	32 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	1.25 MB I+D on chip per core
L3:	36 MB I+D on chip per chip
Other:	None
Memory:	1 TB (32 x 32 GB 2Rx4 PC4-3200AA-R)
Storage:	1 x SATA M.2 SSD, 480GB
Other:	None

Software

OS:	Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64
Compiler:	C/C++: Version 19.1.2.275 of Intel C/C++ Compiler for Linux; Fortran: Version 19.1.2.275 of Intel Fortran Compiler for Linux
Parallel:	Yes
Firmware:	Fujitsu BIOS Version V1.0.0.0 R1.4.0 for D3891-A1x. Released May-2021 tested as V1.0.0.0 R1.2.0 for D3891-A1x Apr-2021
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS set to balance power and performance

Power

Max. Power (W):	636.4
Idle Power (W):	338.56
Min. Temperature (C):	27.44
Elevation (m):	11

(Continued on next page)

SPEC CPU®2017 Integer Speed Result

Fujitsu

PRIMERGY RX2540 M6, Intel Xeon Gold 6346, 3.10GHz

SPECspeed®2017_int_base =	12.0
SPECspeed®2017_int_energy_base =	36.4
SPECspeed®2017_int_peak =	Not Run
SPECspeed®2017_int_energy_peak =	Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2021
Hardware Availability: May-2021
Software Availability: Aug-2020

Power (Continued)

Line Standard: 200 V / 50 Hz / 1 phase / 2 wires
Provisioning: Line-powered

Power Settings

Management FW: Version 3.20i for D3891-A1x of Fujitsu BMC Firmware
Memory Mode: Normal

Power-Relevant Hardware

Power Supply: 1 x 900 W (non-redundant)
Details: Standard power supply part of base unit S26113-E629-V50-1
Backplane: 24 x 2.5inch HDD back plan
Other Storage: Embedded SATA Controller
Storage Model #s: S26361-F5706
NICs Installed: 1 x Intel I350-T4 @ 1 Gb
NICs Enabled (FW/OS): 4 / 4
NICs Connected/Speed: 1 @ 1 Gb
Other HW Model #s: None

Power Analyzer

Power Analyzer: 10.26.120.153:8888
Hardware Vendor: Hioki
Model: Hioki PW3336:1-Channel
Serial Number: 170213562
Input Connection: USB via USB-Serial CH340
Metrology Institute: NICT
Calibration By: HIOKI E.E. CORPORATION
Calibration Label: H06400088
Calibration Date: 25-Jun-2020
PTDaemon® Version: 1.9.1 (a2d19f26; 2019-07-17)
Setup Description: Connected to PSU 1
Current Ranges Used: 5A
Voltage Range Used: 300V

Temperature Meter

Temperature Meter: 10.26.120.153:8889
Hardware Vendor: Digi International Inc.
Model: DigiWATCHPORT_H
Serial Number: W 613 66209
Input Connection: USB
PTDaemon Version: 1.9.1 (a2d19f26; 2019-07-17)
Setup Description: 5 mm in front of SUT main air intake

Base Results Table

Benchmark	Threads	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
600.perlbench_s	64	239	7.41	84.5	22.8	353	362	**238**	**7.44**	**84.4**	**22.8**	**354**	**364**	238	7.45	84.4	22.8	354	363
602.gcc_s	64	361	11.0	128	33.9	354	367	363	11.0	129	33.7	355	367	**361**	**11.0**	**128**	**33.8**	**354**	**365**
605.mcf_s	64	**244**	**19.4**	**86.5**	**59.5**	**355**	**365**	245	19.3	87.1	59.2	356	364	243	19.4	86.4	59.6	355	365
620.omnetpp_s	64	151	10.8	53.7	33.1	355	363	**145**	**11.2**	**51.5**	**34.5**	**354**	**364**	145	11.2	51.5	34.4	355	363
623.xalancbmk_s	64	102	13.9	35.8	43.0	352	361	101	14.0	35.6	43.2	352	360	**101**	**14.0**	**35.7**	**43.1**	**352**	**362**
625.x264_s	64	102	17.2	36.1	53.1	353	359	**102**	**17.3**	**36.1**	**53.2**	**353**	**362**	102	17.3	36.0	53.3	353	361
631.deepsjeng_s	64	**238**	**6.02**	**84.0**	**18.5**	**353**	**363**	238	6.01	84.0	18.5	353	365	238	6.03	83.8	18.6	353	363
641.leela_s	64	344	4.96	121	15.3	351	361	344	4.95	121	15.3	351	361	**344**	**4.95**	**121**	**15.3**	**351**	**361**
648.exchange2_s	64	142	20.7	50.1	63.8	353	360	**142**	**20.7**	**50.0**	**64.0**	**352**	**361**	142	20.7	49.9	64.0	352	362
657.xz_s	64	**260**	**23.8**	**107**	**62.7**	**414**	**636**	260	23.8	107	62.7	413	635	259	23.9	107	63.0	413	636

SPECspeed®2017_int_base = **12.0**

SPECspeed®2017_int_energy_base = **36.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

SPEC CPU®2017 Integer Speed Result

Fujitsu

PRIMERGY RX2540 M6, Intel Xeon Gold 6346, 3.10GHz

SPECspeed®2017_int_base =	12.0
SPECspeed®2017_int_energy_base =	36.4
SPECspeed®2017_int_peak =	Not Run
SPECspeed®2017_int_energy_peak =	Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Apr-2021
Hardware Availability: May-2021
Software Availability: Aug-2020

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
echo 1000 > /proc/sys/kernel/sched_migration_cost_ns
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH =
     "/home/PVT/speccpu-1.1.5/lib/intel64:/home/PVT/speccpu-1.1.5/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS configuration:
CPU C1E Support = Disabled
UPI Link Frequency Select = 10.4 GT/s

Sysinfo program /home/PVT/speccpu-1.1.5/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Wed Apr  7 13:34:04 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
```

(Continued on next page)

Platform Notes (Continued)

```
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6346 CPU @ 3.10GHz
       2  "physical id"s (chips)
       64 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 16
       siblings  : 32
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
       physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

 From lscpu:
      Architecture:          x86_64
      CPU op-mode(s):        32-bit, 64-bit
      Byte Order:            Little Endian
      CPU(s):                64
      On-line CPU(s) list: 0-63
      Thread(s) per core:    2
      Core(s) per socket:    16
      Socket(s):             2
      NUMA node(s):          2
      Vendor ID:             GenuineIntel
      CPU family:            6
      Model:                 106
      Model name:            Intel(R) Xeon(R) Gold 6346 CPU @ 3.10GHz
      Stepping:              6
      CPU MHz:               1154.028
      CPU max MHz:           3600.0000
      CPU min MHz:           800.0000
      BogoMIPS:              6200.00
      Virtualization:        VT-x
      L1d cache:             48K
      L1i cache:             32K
      L2 cache:              1280K
      L3 cache:              36864K
      NUMA node0 CPU(s):     0-15,32-47
      NUMA node1 CPU(s):     16-31,48-63
      Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
      aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
      xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
      avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
```

(Continued on next page)

Platform Notes (Continued)

```
      mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase
      tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq
      rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw
      avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
      cqm_mbm_local wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
      hwp_pkg_req avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
      avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d
      arch_capabilities

 /proc/cpuinfo cache data
    cache size : 36864 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 2 nodes (0-1)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 32 33 34 35 36 37 38 39 40 41 42 43
   44 45 46 47
   node 0 size: 515509 MB
   node 0 free: 514829 MB
   node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 48 49 50 51 52 53 54 55 56
   57 58 59 60 61 62 63
   node 1 size: 516058 MB
   node 1 free: 515040 MB
   node distances:
   node   0   1
     0:  10  20
     1:  20  10

 From /proc/meminfo
    MemTotal:       1056325748 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 /sbin/tuned-adm active
     Current active profile: throughput-performance

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.2 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
```

(Continued on next page)

Platform Notes (Continued)

```
      VERSION_ID="8.2"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
   Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                        Not affected
CVE-2018-3620 (L1 Terminal Fault):                     Not affected
Microarchitectural Data Sampling:                      Not affected
CVE-2017-5754 (Meltdown):                              Not affected
CVE-2018-3639 (Speculative Store Bypass):              Mitigation: Speculative Store
                                                       Bypass disabled via prctl and
                                                       seccomp
CVE-2017-5753 (Spectre variant 1):                     Mitigation: usercopy/swapgs
                                                       barriers and __user pointer
                                                       sanitization
CVE-2017-5715 (Spectre variant 2):                    Mitigation: Enhanced IBRS, IBPB:
                                                       conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort):               Not affected


SPEC is set to: /home/PVT/speccpu-1.1.5
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda3      xfs   330G   88G  243G  27% /home

From /sys/devices/virtual/dmi/id
    Vendor:         FUJITSU
    Product:        PRIMERGY RX2540 M6
    Product Family: SERVER
    Serial:         EWAAxxxxxx

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  Memory:
```

(Continued on next page)

Platform Notes (Continued)

```
     32x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200

 BIOS:
    BIOS Vendor:       FUJITSU
    BIOS Version:      V1.0.0.0 R1.2.0 for D3891-A1x
    BIOS Date:         04/01/2021
    BIOS Revision:     1.2
    Firmware Revision: 3.20

 (End of data from sysinfo program)
```

Power Settings Notes

```
PTDaemon to measure power and temperature was run on a PRIMERGY RX2530 M5 as a controller
with 2x Intel Xeon Platinum 8280 CPU and 768 GB of memory using Windows Server 2012 R2.
Power management in the BIOS was default except for any settings mentioned in BIOS Configuration.
No power management settings were set in the management firmware.
The optional optical drive was not installed.
The run was started and observed through the management firmware.
```

Compiler Version Notes

```
==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base)
        | 625.x264_s(base) 657.xz_s(base)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version
  19.1.2.275 Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++     | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
        | 641.leela_s(base)
------------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version
  19.1.2.275 Build 20200604
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran | 648.exchange2_s(base)
------------------------------------------------------------------------------
```

(Continued on next page)

Compiler Version Notes (Continued)

```
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.1.2.275 Build 20200623
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Base Portability Flags

600.perlbench_s: `-DSPEC_LP64 -DSPEC_LINUX_X64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LP64 -DSPEC_LINUX`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX2 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

(Continued on next page)

Base Optimization Flags (Continued)

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.3.275/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX2
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ICL-RevA.html
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ICL-RevA.xml
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.5 on 2021-04-07 13:34:04-0400.
Report generated on 2021-04-27 16:23:35 by CPU2017 PS formatter v6442.
Originally published on 2021-04-27.