### SPEC CPU®2017 Integer Speed Result

**Dell Inc.**  
PowerEdge C6525 (AMD EPYC 7313 16-Core Processor)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>12.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>12.4</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Mar-2021  
**Hardware Availability:** Mar-2021  
**Software Availability:** Mar-2021

<table>
<thead>
<tr>
<th>Test</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>6.60</td>
<td>13.3</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>6.73</td>
<td>13.4</td>
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<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>8.24</td>
<td>20.7</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>8.29</td>
<td>20.8</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>32</td>
<td>14.0</td>
<td>23.7</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>32</td>
<td>14.1</td>
<td>23.7</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>6.35</td>
<td>24.9</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>32</td>
<td>5.86</td>
<td>25.1</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>5.86</td>
<td>25.1</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>6.35</td>
<td>25.1</td>
</tr>
</tbody>
</table>

---

**Hardware**

- **CPU Name:** AMD EPYC 7313  
- **Max MHz:** 3700  
- **Nominal:** 3000  
- **Enabled:** 32 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 512 KB I+D on chip per core  
- **L3:** 128 MB I+D on chip per chip, 32 MB shared / 4 cores  
- **Other:** None  
- **Memory:** 2 TB (16 x 128 GB 4Rx4 PC4-3200AA-R)  
- **Storage:** 1002 GB on tmpfs  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
- **Compiler:** C/C++/Fortran: Version 3.0.0 of AOCC  
- **Parallel:** Yes  
- **Firmware:** Version 2.2.2 released Mar-2021  
- **File System:** tmpfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc: jemalloc memory allocator library v5.1.0  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>269</td>
<td>6.60</td>
<td>268</td>
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<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>299</td>
<td>13.3</td>
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<td>13.3</td>
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<tr>
<td>605.mcf_s</td>
<td>32</td>
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<td>20.8</td>
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<td>20.8</td>
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<td>620.omnetpp_s</td>
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<td>14.1</td>
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<tr>
<td>625.x264_s</td>
<td>32</td>
<td>102</td>
<td>17.3</td>
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<tr>
<td>631.deepsjeng_s</td>
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<td>6.35</td>
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<tr>
<td>641.leela_s</td>
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<td>5.88</td>
<td>291</td>
<td>5.86</td>
<td>291</td>
<td>5.86</td>
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<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>124</td>
<td>23.7</td>
<td>124</td>
<td>23.7</td>
<td>124</td>
<td>23.7</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>248</td>
<td>24.9</td>
<td>246</td>
<td>25.1</td>
<td>246</td>
<td>25.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at http://developer.amd.com/amd-aocc/

Submit Notes

The config file option 'submit' was used. 'numactl' was used to bind copies to the cores. See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

'echo 8 > /proc/sys/vm/dirty_ratio' run as root to limit dirty cache to 8% of memory.
'echo 1 > /proc/sys/vm/swappiness' run as root to limit swap usage to minimum necessary.
'echo 1 > /proc/sys/vm/zone_reclaim_mode' run as root to free node-local memory and avoid remote memory usage.
'sync; echo 3 > /proc/sys/vm/drop_caches' run as root to reset filesystem caches.
'/sysctl -w kernel.randomize_va_space=0' run as root to disable address space layout randomization (ASLR) to reduce run-to-run variability.
Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-31"
LD_LIBRARY_PATH =
"/dev/shm/cpu2017-1.1.5/amd_speed_aocc300_milan_B_lib/64;/dev/shm/cpu2017-1.1.5/amd_speed_aocc300_milan_B_lib/32;"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "32"

Environment variables set by runcpu during the 600.perlbench_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 602.gcc_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 605.mcf_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 620.omnetpp_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 625.x264_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 641.leela_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 648.exchange2_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 657.xz_s peak run:
GOMP_CPU_AFFINITY = "0-31"
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**PowerEdge C6525 (AMD EPYC 7313 16-Core Processor)**

**SPECspeed®2017_int_base = 12.3**

**SPECspeed®2017_int_peak = 12.4**

<table>
<thead>
<tr>
<th>Metric</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License:</td>
<td>55</td>
</tr>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Mar-2021</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Mar-2021</td>
</tr>
</tbody>
</table>

---

**General Notes**

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 1002 GB ramdisk created with the cmd: "mount -t tmpfs -o size=1002G tmpfs /mnt/ramdisk"

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2

---

**Platform Notes**

BIOS settings:

- Logical processor: Disabled
- L3 Cache as NUMA Domain: Enabled
- Virtualization Technology: Disabled
- DRAM Refresh Delay: Performance
- System Profile: Custom
  - CPU Power Management: Maximum Performance
  - Memory Patrol Scrub: Disabled
  - PCI ASPM L1 Link Power Management: Disabled

Sysinfo program /dev/shm/cpu2017-1.1.5/bin/sysinfo

Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Fri Mar 19 14:35:36 2021

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name: AMD EPYC 7313 16-Core Processor
- 2 "physical id"s (chips)
- 32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 16

(Continued on next page)
Dell Inc.

PowerEdge C6525 (AMD EPYC 7313 16-Core Processor)

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Tested by: Dell Inc.
Software Availability: Mar-2021

Platform Notes (Continued)

/proc/cpuinfo cache data
    cache size : 512 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0 1 2 3
    node 0 size: 257603 MB
    node 0 free: 255465 MB
    node 1 cpus: 4 5 6 7
    node 1 size: 258038 MB
    node 1 free: 256056 MB
    node 2 cpus: 8 9 10 11
    node 2 size: 258000 MB
    node 2 free: 255643 MB
    node 3 cpus: 12 13 14 15
    node 3 size: 245927 MB
    node 3 free: 245739 MB
    node 4 cpus: 16 17 18 19
    node 4 size: 258014 MB
    node 4 free: 257961 MB
    node 5 cpus: 20 21 22 23
    node 5 size: 258026 MB
    node 5 free: 257971 MB
    node 6 cpus: 24 25 26 27
    node 6 size: 258018 MB
    node 6 free: 257971 MB
    node 7 cpus: 28 29 30 31
    node 7 size: 258006 MB
    node 7 free: 257967 MB

node distances:
    node  0  1  2  3  4  5  6  7
    0:  10 11 11 11 32 32 32 32
    1:  11 10 11 11 32 32 32 32
    2:  11 11 10 11 32 32 32 32
    3:  11 11 11 10 32 32 32 32
    4:  32 32 32 32 10 11 11 11
    5:  32 32 32 32 11 10 11 11
    6:  32 32 32 32 11 11 10 11
    7:  32 32 32 32 11 11 11 10

From /proc/meminfo
    MemTotal: 2101029088 kB
    HugePages_Total: 0
    Hugepagesize: 2048 kB

/sbin/tuned-adm active

(Continued on next page)
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Platform Notes (Continued)

Current active profile: throughput-performance

From /etc/*release* /etc/*version*

```
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
```

```
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
```

```
uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling:
CVE-2017-5754 (Meltdown):
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: disabled, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Nov 26 12:59
SPEC is set to: /dev/shm/cpu2017-1.1.5

```
Filesystem  Type      Size  Used Avail Use% Mounted on
tmpfs       tmpfs     1002G  5.7G  997G    1%  /dev/shm
```

```
From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge C6525
```

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Platform Notes (Continued)**

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
- 16x 802C8632802C 72ASS16G72LZ-3G2B3 128 GB 4 rank 3200

BIOS:
- BIOS Vendor: Dell Inc.
- BIOS Version: 2.2.2
- BIOS Date: 03/02/2021
- BIOS Revision: 2.2

(End of data from sysinfo program)

**Compiler Version Notes**

```plaintext
<table>
<thead>
<tr>
<th>Language</th>
<th>Benchmark</th>
<th>Benchmark</th>
<th>Benchmark</th>
<th>Benchmark</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>600.perlbench_s(base, peak)</td>
<td>602.gcc_s(base, peak)</td>
<td>605.mcf_s(base, peak)</td>
<td>625.x264_s(base, peak)</td>
</tr>
<tr>
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<td>620.omnetpp_s(base, peak)</td>
<td>623.xalancbmk_s(base, peak)</td>
<td>631.deepsjeng_s(base, peak)</td>
<td>641.leela_s(base, peak)</td>
</tr>
<tr>
<td>Fortran</td>
<td>648.exchange2_s(base, peak)</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
```

**Notes**

AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on LLVM Mirror.Version.12.0.0)
- Target: x86_64-unknown-linux-gnu
- Thread model: posix
- InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

(Continued on next page)
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**Compiler Version Notes (Continued)**

Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin

---

### Base Compiler Invocation

**C benchmarks**:  
clang

**C++ benchmarks**:  
clang++

**Fortran benchmarks**:  
flang

---

### Base Portability Flags

- 600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64  
- 602.gcc_s: -DSPEC_LP64  
- 605.mcf_s: -DSPEC_LP64  
- 620.omnetpp_s: -DSPEC_LP64  
- 623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64  
- 625.x264_s: -DSPEC_LP64  
- 631.deepsjeng_s: -DSPEC_LP64  
- 641.leela_s: -DSPEC_LP64  
- 648.exchange2_s: -DSPEC_LP64  
- 657.xz_s: -DSPEC_LP64

---

### Base Optimization Flags

**C benchmarks**:  
- -m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition  
- -Wl,-mlllvm -Wl,-enable-lcm-vrp -Wl,-mlllvm -Wl,-region-vectorize  
- -Wl,-mlllvm -Wl,-function-specialize  
- -Wl,-mlllvm -Wl,-align-all-nofallthru-blocks=6  
- -Wl,-mlllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
- -fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5  
- -mlllvm -unroll-threshold=50 -mlllvm -inline-threshold=1000  
- -fremap-arrays -mlllvm -function-specialize -flv-function-specialization  
- -mlllvm -enable-gvn-hoist -mlllvm -global-vectorize-slp=true

(Continued on next page)
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Base Optimization Flags (Continued)

C benchmarks (continued):
-mlvm -enable-licm-vrp -mlvm -reduce-array-computations=3 -z muldefs
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang -lflangrti

C++ benchmarks:
-m64 -std=c++98 -mno-adx -mno-sse4a
-W1, -mlvm -W1, -do-block-reorder=aggressive
-W1, -mlvm -W1, -region-vectorize -W1, -mlvm -W1, -function-specialize
-W1, -mlvm -W1, -align-all-nofallthru-blocks=6
-W1, -mlvm -W1, -reduce-array-computations=3 -03 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -mlvm -enable-partial-unswitch
-mlvm -unroll-threshold=100 -finline-aggressive
-fllvm-function-specialization -mlvm -loop-unswitch-threshold=200000
-mlvm -reroll-loops -mlvm -aggressive-loop-unswitch
-mlvm -extra-vectorizer-passes -mlvm -reduce-array-computations=3
-mlvm -global-vectorize-slp=true -mlvm -convert-pow-exp-to-int=false
-z muldefs -mlvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
-lflangrti

Fortran benchmarks:
-m64 -mno-adx -mno-sse4a -W1, -mlvm -W1, -inline-recursion=4
-W1, -mlvm -W1, -isr-in-nested-loop -W1, -mlvm -W1, -enable-iv-split
-W1, -mlvm -W1, -region-vectorize -W1, -mlvm -W1, -function-specialize
-W1, -mlvm -W1, -align-all-nofallthru-blocks=6
-W1, -mlvm -W1, -reduce-array-computations=3 -03 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -z muldefs
-mlvm -unroll-aggressive -mlvm -unroll-threshold=150 -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
-lflangrti

Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument -Wno-return-type

C++ benchmarks:
-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:
-Wno-return-type
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**Peak Compiler Invocation**

**C benchmarks:**
clang

**C++ benchmarks:**
clang++

**Fortran benchmarks:**
flang

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

**C benchmarks:**
- `-m64`  
- `-mno-adx`  
- `-mno-sse4a`  
- `-Wl,-allow-multiple-definition`  
- `-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-function-specialize`  
- `-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`  
- `-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3`  
- `-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5`  
- `-mllvm -unroll-threshold=50 -fremap-arrays -flv-function-specialization`  
- `-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist`  
- `-mllvm -global-vectorize-slp=true -mllvm -function-specialize`  
- `-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3`  
- `-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc`  
- `-lflang`

**C++ benchmarks:**
- `620.omnetpp_s`  
- `-std=c++98`  
- `-m64`  
- `-mno-adx`  
- `-mno-sse4a`  
- `-Wl,-do-block-reorder=aggressive`  
- `-Wl,-mllvm -Wl,-function-specialize`  
- `-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`  
- `-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast`  
- `-march=znver3`  
- `-fveclib=AMDLIBM -ffast-math -flto`  
- `-finline-aggressive -mllvm -unroll-threshold=100`  
- `-flv-function-specialization -mllvm -enable-licm-vrp`  
- `-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch`  
- `-mllvm -reduce-array-computations=3`  
- `-mllvm -global-vectorize-slp=true`  
- `-mllvm -do-block-reorder=aggressive`

(Continued on next page)
# SPEC CPU®2017 Integer Speed Result

## Dell Inc.

PowerEdge C6525 (AMD EPYC 7313 16-Core Processor)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
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<td>Test Sponsor:</td>
<td>Dell Inc.</td>
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<td>Tested by:</td>
<td>Dell Inc.</td>
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### SPECspeed®2017_int_base = 12.3

### SPECspeed®2017_int_peak = 12.4

<table>
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<th>Mar-2021</th>
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</table>

## Peak Optimization Flags (Continued)

620.omnetpp_s (continued):
- fvirtual-function-elimination -fvisibility=hidden
- DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm
- ljemalloc -lflang

623.xalancbmk_s: Same as 620.omnetpp_s

631.deepsjeng_s: basepeak = yes

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:
- m64 -mno-adx -mno-sse4a -Wl,-mlllvm -Wl,-inline-recursion=4
- Wl,-mlllvm -Wl,-lsr-in-nested-loop -Wl,-mlllvm -Wl,-enable-iv-split
- Wl,-mlllvm -Wl,-function-specialize
- Wl,-mlllvm -Wl,-align-all-nofallthru-blocks=6
- Wl,-mlllvm -Wl,-reduce-array-computations=3 -O3 -march=znerver3
- fveclib=AMDLIBM -ffast-math -flto -mlllvm -unroll-aggressive
- mlllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -fopenmp=libomp
- lomp -lamdlibm -ljemalloc -lflang

## Peak Other Flags

C benchmarks:
- Wno-unused-command-line-argument -Wno-return-type

C++ benchmarks:
- Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:
- Wno-return-type

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


**Dell Inc.**

PowerEdge C6525 (AMD EPYC 7313 16-Core Processor)

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