## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**  
PowerEdge MX750c (Intel Xeon Platinum 8358, 2.60 GHz)

<table>
<thead>
<tr>
<th>Name</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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<td>19.4</td>
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<td>deepsjeng_s</td>
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<tr>
<td>leela_s</td>
<td>64</td>
<td>18.8</td>
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</table>

### Hardware
- **CPU Name:** Intel Xeon Platinum 8358  
- **Max MHz:** 3400  
- **Nominal:** 2600  
- **Enabled:** 64 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 48 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 1 TB (32 x 32 GB 2Rx4 PC4-3200AA-R)  
- **Storage:** 225 GB on tmpfs  
- **Other:** None

### Software
- **OS:** Red Hat Enterprise Linux 8.2 (Ootpa)  
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
- **Parallel:** Yes  
- **Firmware:** Version 1.1.0 released Mar-2021  
- **File System:** tmpfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
## SPEC CPU®2017 Integer Speed Result

Dell Inc.  
PowerEdge MX750c (Intel Xeon Platinum 8358, 2.60 GHz)  

### SPECspeed®2017 int_base = 11.6

### SPECspeed®2017 int_peak = 11.9

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### Results Table

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<td></td>
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<td></td>
<td></td>
<td></td>
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</tbody>
</table>

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP_AFFINITY = "granularity=fine,scatter"  
LD_LIBRARY_PATH = 
"/mnt/ramdisk/cpu2017-ic19.1ul/lib/intel64:/mnt/ramdisk/cpu2017-ic19.1ul
 /je5.0.1-64"  
MALLOC_CONF = "retain:true"  
OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8358, 2.60 GHz)

SPEC CPU®2017 Integer Speed Result
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SPECspeed®2017_int_base = 11.6
SPECspeed®2017_int_peak = 11.9

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2021
Hardware Availability: Apr-2021
Software Availability: Mar-2021

General Notes (Continued)
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes
BIOS Settings:
Logical Processor : Disabled
Virtualization Technology : Disabled
System Profile : Custom
CPU Power Management : Maximum Performance
C1E : Disabled
C States : Autonomous
Memory Patrol Scrub : Disabled
Energy Efficiency Policy : Performance
CPU Interconnect Bus Link
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-ic19.1u1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edc1e6e46a485a0011
running on localhost.localdomain Thu Mar 25 20:04:13 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8358 CPU @ 2.60GHz
 2 "physical id"s (chips)
 64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
siblings : 32
physical 0: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
physical 1: cores 0 1 2 3 4 5 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

(Continued on next page)
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## Dell Inc.

### PowerEdge MX750c (Intel Xeon Platinum 8358, 2.60 GHz)

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</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

---

**Platform Notes (Continued)**

From `lscpu`:

- **Architecture:** x86_64
- **CPU op-mode(s):** 32-bit, 64-bit
- **Byte Order:** Little Endian
- **CPU(s):** 64
- **On-line CPU(s) list:** 0-63
- **Thread(s) per core:** 1
- **Core(s) per socket:** 32
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 106
- **Model name:** Intel(R) Xeon(R) Platinum 8358 CPU @ 2.60GHz
- **Stepping:** 6
- **CPU MHz:** 2459.285
- **BogoMIPS:** 5200.00
- **Virtualization:** VT-x
- **L1d cache:** 48K
- **L1i cache:** 32K
- **L2 cache:** 1280K
- **L3 cache:** 49152K
- **NUMA node0 CPU(s):** 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62
- **NUMA node1 CPU(s):** 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59,61,63
- **Flags:** fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma cmipshopt clwb intel_pt avx512vd avx512bw avx512vl xsaveopt xsave xsavec xclass cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local wbinvd dtherm ida arat pln pts avx512vpmi umip pku ospke avx512_vbmi2 gfn vi vaes vpmismode avx512_vnni avx512_vbitalg tme avx512_vpopcntdq la57 pdip md_clear pconf flush_l1d arch_capabilities

/proc/cpuinfo cache data

- **cache size:** 49152 KB

From `numactl --hardware`  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
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Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2021
Hardware Availability: Apr-2021
Software Availability: Mar-2021

Platform Notes (Continued)

available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50
52 54 56 58 60 62
node 0 size: 515484 MB
node 0 free: 514896 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51
53 55 57 59 61 63
node 1 size: 516058 MB
node 1 free: 495777 MB
node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo
MemTotal: 1056299712 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR=\"0;31\"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

* Itlb_multihit: Not affected
* CVE-2018-3620 (L1 Terminal Fault): Not affected
* Microarchitectural Data Sampling: Not affected
* CVE-2017-5754 (Meltdown): Not affected
* CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
* CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization

(Continued on next page)
Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
tsx_async_abort: Not affected

run-level 3 Mar 25 18:04

SPEC is set to: /mnt/ramdisk/cpu2017-ic19.1ul
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 225G 9.6G 216G 5% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
BIOS: Dell Inc. 1.1.0 03/25/2021
Vendor: Dell Inc.
Product: PowerEdge MX750c
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
15x 00AD063200AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200
17x 00AD063200AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200

(End of data from sysinfo program)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8358, 2.60 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPEC CPU®2017 Integer Speed Result

SPECspeed®2017_int_base = 11.6
SPECspeed®2017_int_peak = 11.9

Compiler Version Notes (Continued)

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<th>C</th>
<th>600.perlbench_s(peak)</th>
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<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
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<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
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<table>
<thead>
<tr>
<th>C</th>
<th>600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
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<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
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Base Compiler Invocation

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifort
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<td>Tested by:</td>
<td>Dell Inc.</td>
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**Specspeed®2017_int_base = 11.6
Specspeed®2017_int_peak = 11.9**

**Base Portability Flags**

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

Fortran benchmarks:
-m64 -xcORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

**Peak Compiler Invocation**

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx
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CPU2017 License: 55  Test Date: Mar-2021
Test Sponsor: Dell Inc.  Hardware Availability: Apr-2021
Tested by: Dell Inc.  Software Availability: Mar-2021

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass1) -prof-use(pass2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass1)
-fprofile-use=default.profdata(pass2) -xCORE-AVX512 -flto
-Ofast(pass1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:
620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes

(Continued on next page)
Dell Inc.
PowerEdge MX750c (Intel Xeon Platinum 8358, 2.60 GHz)

<table>
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<tr>
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<td>55</td>
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</table>

#### Peak Optimization Flags (Continued)

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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