# SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G3 (Intel Xeon Gold 6244)

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed 2017 int_base</th>
<th>SPECspeed 2017 int_peak</th>
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<tr>
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<tr>
<td>gcc_s</td>
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<tr>
<td>mcf_s</td>
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<tr>
<td>omnetpp_s</td>
<td>14.5</td>
<td>17.7</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>6.30</td>
<td>18.3</td>
</tr>
<tr>
<td>x264_s</td>
<td>5.30</td>
<td></td>
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<tr>
<td>deepsjeng_s</td>
<td>6.30</td>
<td></td>
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<tr>
<td>leela_s</td>
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<td></td>
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<tr>
<td>exchange2_s</td>
<td>18.3</td>
<td></td>
</tr>
<tr>
<td>xz_s</td>
<td>25.0</td>
<td></td>
</tr>
</tbody>
</table>

## Hardware
- **CPU Name:** Intel Xeon Gold 6244  
- **Max MHz:** 4400  
- **Nominal:** 3600  
- **Enabled:** 32 cores, 4 chips  
- **Orderable:** 1,2,3,4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 24.75 MB I+D on chip per chip  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2933V-R)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

## Software
- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux
- **Parallel:** Yes
- **Firmware:** Version 2.00.33 released Aug-2019 BIOS  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
New H3C Technologies Co., Ltd.

H3C UniServer R6900 G3 (Intel Xeon Gold 6244)

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<td>247</td>
<td>25.0</td>
<td>247</td>
<td>25.0</td>
</tr>
</tbody>
</table>

Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
General Notes (Continued)

is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
    built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Set Hyper Threading to Disabled
Set Patrol Scrub to Disabled
Set IMC Interleaving to 2-way Interleave

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed1be6e46a485a0011
running on localhost.localdomain Tue Nov  3 16:15:28 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
    4 "physical id"s (chips)
    32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 8
    siblings : 8
    physical 0: cores 1 2 9 17 19 20 26 27
    physical 1: cores 2 4 8 9 18 19 20 25
    physical 2: cores 2 4 9 18 19 20 24 25
    physical 3: cores 1 2 4 9 18 19 25 27

From lscpu:
    Architecture: x86_64
    CPU op-mode(s): 32-bit, 64-bit
    Byte Order: Little Endian
    CPU(s): 32
    On-line CPU(s) list: 0-31
    Thread(s) per core: 1
    Core(s) per socket: 8
    Socket(s): 4

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R6900 G3 (Intel Xeon Gold 6244)

| SPECspeed®2017_int_base = 11.7 | SPECspeed®2017_int_peak = 12.0 |

| CPU2017 License: | 9066 |
| Test Sponsor: | New H3C Technologies Co., Ltd. |
| Tested by: | New H3C Technologies Co., Ltd. |
| Test Date: | Nov-2020 |
| Hardware Availability: | Jun-2019 |
| Software Availability: | Apr-2020 |

**Platform Notes (Continued)**

NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
Stepping: 6
CPU MHz: 2334.180
CPU max MHz: 4400.0000
CPU min MHz: 1200.0000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0-7
NUMA node1 CPU(s): 8-15
NUMA node2 CPU(s): 16-23
NUMA node3 CPU(s): 24-31

Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_p pir ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmq mxp rd t_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaveprec xsavec qsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida ar at pfn pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear flush_lld arch_capabilities

/proc/cpuinfo cache data

cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

  available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 3 4 5 6 7
  node 0 size: 95072 MB
  node 0 free: 94639 MB
  node 1 cpus: 8 9 10 11 12 13 14 15
  node 1 size: 96765 MB
  node 1 free: 96477 MB
  node 2 cpus: 16 17 18 19 20 21 22 23
  node 2 size: 96765 MB
  node 2 free: 96516 MB

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

New H3C Technologies Co., Ltd.
H3C UniServer R6900 G3 (Intel Xeon Gold 6244)

SPECspeed®2017_int_base = 11.7
SPECspeed®2017_int_peak = 12.0

Node Notes (Continued)

node 3 cpus: 24 25 26 27 28 29 30 31
node 3 size: 96738 MB
node 3 free: 96087 MB
node distances:
node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo
MemTotal: 394590420 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

itlb_multihit: KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
tsx_async_abort: Vulnerable: Clear CPU buffers attempted, no microcode; SMT disabled

(Continued on next page)
Platform Notes (Continued)

run-level 3 Nov 3 16:11

SPEC is set to: /home/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 839G 141G 698G 17% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. 2.00.33 08/22/2019
Vendor: New H3C Technologies Co., Ltd.
Product: H3C UniServer R6900 G3
Product Family: Rack
Serial: 210235A3T0H20400004

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
24x Micron 18ASF2G72PDZ-2G9E1 16 GB 2 rank 2933
24x NO DIMM NO DIMM

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
   | 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 600.perlbench_s(peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
   | 625.x264_s(base, peak) 657.xz_s(base, peak)
(Continued on next page)
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H3C UniServer R6900 G3 (Intel Xeon Gold 6244)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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</thead>
<tbody>
<tr>
<td>11.7</td>
<td>12.0</td>
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CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.  
Tested by: New H3C Technologies Co., Ltd.  
Test Date: Nov-2020  
Hardware Availability: Jun-2019  
Software Availability: Apr-2020

## Compiler Version Notes (Continued)

```
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
  NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C       | 600.perlbench_s(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
         | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
  NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

## Base Compiler Invocation

C benchmarks:  
  icc

C++ benchmarks:  
  icpc

Fortran benchmarks:  
  ifort
New H3C Technologies Co., Ltd. SPEC CPU®2017 Integer Speed Result
H3C UniServer R6900 G3 (Intel Xeon Gold 6244)

| SPECspeed®2017_int_base = 11.7 |
| SPECspeed®2017_int_peak = 12.0 |

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

| Test Date: Nov-2020 |
| Hardware Availability: Jun-2019 |
| Software Availability: Apr-2020 |

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC/OpenMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

(Continued on next page)
Peak Compiler Invocation (Continued)

Fortran benchmarks:
  ifort

Peak Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>-DSPEC_LP64(*) -DSPEC_LP64</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>-DSPEC_LP64 -DSPEC_LINUX</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>-DSPEC_LP64</td>
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</table>

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -fno-strict-overflow -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>-m64 -qnextgen -std=c11 -fuse-ld=gold -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profdatal -xCORE-AVX512 -flto -Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4 -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>basepeak = yes</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>-m64 -qnextgen -std=c11  -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math -fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc</td>
</tr>
</tbody>
</table>
New H3C Technologies Co., Ltd.
H3C UniServer R6900 G3 (Intel Xeon Gold 6244)

| SPECspeed®2017_int_base = 11.7 |
| SPECspeed®2017_int_peak = 12.0 |

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Jun-2019
Software Availability: Apr-2020

**Peak Optimization Flags (Continued)**

657.xz_s: basepeak = yes

C++ benchmarks:
620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-11-03 03:15:28-0500.