### Supermicro

SuperServer SYS-240P-TNRT
(X12QCH+, Intel Xeon Platinum 8380H)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>112</td>
<td>853</td>
<td>854</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>112</td>
<td>40.0</td>
<td>847</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>112</td>
<td>558</td>
<td>564</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>112</td>
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<tr>
<td>627.cam4_s</td>
<td>112</td>
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<tr>
<td>628.pop2_s</td>
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<tr>
<td>638.imagick_s</td>
<td>112</td>
<td>240</td>
<td>240</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>112</td>
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<td>598</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
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</tr>
<tr>
<td>654.roms_s</td>
<td>112</td>
<td>372</td>
<td>372</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Platinum 8380H
- **Max MHz:** 4300
- **Nominal:** 2900
- **Enabled:** 112 cores, 4 chips
- **Orderable:** 1,2,4 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 38.5 MB I+D on chip per chip
- **Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)
- **Storage:** 1 x 200 GB SATA III SSD
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux 8.2
- **Kernel:** 4.18.0-193.el8.x86_64
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux;
  Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Version 1.0 released Sep-2020
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
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<th>Seconds</th>
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<td>603.bwaves_s</td>
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<td>863</td>
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<tr>
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<tr>
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<td>28.0</td>
<td>187</td>
<td>28.8</td>
<td>182</td>
<td>112</td>
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<td>84.3</td>
<td>157</td>
<td>85.0</td>
<td>156</td>
<td>82.6</td>
<td>160</td>
<td>112</td>
<td>84.5</td>
<td>156</td>
<td>83.7</td>
<td>158</td>
<td>83.0</td>
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<td>200</td>
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<td>202</td>
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<td>628.pop2_s</td>
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<td>240</td>
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<td>65.4</td>
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<td>42.4</td>
<td>372</td>
<td>42.8</td>
<td>368</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = 243

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain: true"
OMP_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation

(Continued on next page)
Supermicro
SuperServer SYS-240P-TNRT (X12QCH+, Intel Xeon Platinum 8380H)

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = 243

CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Sep-2020
Hardware Availability: Sep-2020
Tested by: Supermicro
Software Availability: Apr-2020

General Notes (Continued)
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Performance
Hyper-Thread = Disable
Stale AtoS = Disable
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7eddb46a485a0011
running on X12QCH-01 Sat Sep 19 05:51:00 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8380H CPU @ 2.90GHz
  4 "physical id"s (chips)
  112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
  siblings : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
  physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30

From lscpu:
Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Byte Order:           Little Endian
CPU(s):               112
On-line CPU(s) list:  0-111
Thread(s) per core:   1
Core(s) per socket:   28

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Supermicro
SuperServer SYS-240P-TNRT (X12QCH+ , Intel Xeon Platinum 8380H)

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = 243

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Platform Notes (Continued)

Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8380H CPU @ 2.90GHz
Stepping: 11
CPU MHz: 3454.296
CPU max MHz: 4300.0000
CPU min MHz: 1000.0000
BogoMIPS: 5800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 39424K
NUMA node0 CPU(s): 0-27
NUMA node1 CPU(s): 28-55
NUMA node2 CPU(s): 56-83
NUMA node3 CPU(s): 84-111

Flags: fpu vme de pse sse mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid pti msr pae mce cx8 apic se pm rpl pmxs vmmxor svm xsave f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrsenhanced tpr_shadow vnumm xsaveopt sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aea xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>Available</th>
<th>CPU(s)</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>4 nodes</td>
<td>0-27</td>
<td>385578 MB</td>
</tr>
<tr>
<td>4 nodes</td>
<td>28-55</td>
<td>385310 MB</td>
</tr>
<tr>
<td>4 nodes</td>
<td>56-83</td>
<td>387065 MB</td>
</tr>
<tr>
<td>4 nodes</td>
<td>56-83</td>
<td>386865 MB</td>
</tr>
<tr>
<td>4 nodes</td>
<td>56-83</td>
<td>386865 MB</td>
</tr>
</tbody>
</table>

/proc/cpuinfo cache data
cache size : 39424 KB

(Continued on next page)
Platform Notes (Continued)

81 82 83
node 2 size: 387065 MB
node 2 free: 386882 MB
node 3 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111
node 3 size: 387065 MB
node 3 free: 378463 MB
node distances:
node 0 1 2 3
0: 10 20 20 20
1: 20 10 20 20
2: 20 20 10 20
3: 20 20 20 10

From /proc/meminfo
MemTotal:       1583897056 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux"
VERSION="8.2 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.2"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga
uname -a:
Linux X12QCH-01 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

itelb_multihit: Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization

(Continued on next page)
# SPEC CPU®2017 Floating Point Speed Result

**Supermicro**

**SuperServer SYS-240P-TNRT**  
(X12QCH+, Intel Xeon Platinum 8380H)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base = 241</th>
<th>CPU2017 License: 001176</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak = 243</td>
<td>Test Sponsor: Supermicro</td>
</tr>
</tbody>
</table>

**Tested by:** Supermicro  

**Hardware Availability:** Sep-2020  

**Software Availability:** Apr-2020  

## Platform Notes (Continued)

- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling  
- **tsx_async_abort:** Not affected  

**run-level 3 Sep 19 02:06**  

**SPEC is set to:** /home/cpu2017  

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/mapper/rhel-home</td>
<td>xfs</td>
<td>125G</td>
<td>17G</td>
<td>108G</td>
<td>14%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id  

- **BIOS:** American Megatrends International, LLC. 1.0 09/02/2020  
- **Vendor:** Supermicro  
- **Product:** Super Server  
- **Product Family:** Family  
- **Serial:** 0123456789  

Additional information from dmidecode follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  

- **Memory:**  
  - 48x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200  

(End of data from sysinfo program)

## Compiler Version Notes

```
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)  
| 644.nab_s(base, peak)  

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
```

```
C++, C, Fortran | 607.cactuBSSN_s(base, peak)  

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
```

(Continued on next page)
Supermicro
SuperServer SYS-240P-TNRT
(X12QCH+ , Intel Xeon Platinum 8380H)

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = 243

Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

(Continued on next page)
Supermicro
SuperServer SYS-240P-TNRT (X12QCH+, Intel Xeon Platinum 8380H)

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = 243

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro
Test Date: Sep-2020
Hardware Availability: Sep-2020
Software Availability: Apr-2020

Base Portability Flags (Continued)

607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
   -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
   -mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -03
   -no-prec-div -qopt-prefetch -ffinite-math-only
   -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
   -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
   -ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
   -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
   -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
   -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -03 -no-prec-div
   -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
   -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
   -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc

(Continued on next page)
Supermicro  
SuperServer SYS-240P-TNRT  
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SPECspeed®2017_fp_base = 241  
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CPU2017 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Sep-2020  
Hardware Availability: Sep-2020  
Software Availability: Apr-2020

Peak Compiler Invocation (Continued)

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
ifort icc

Benchmarks using Fortran, C, and C++:  
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:  
619.lbm_s: basepeak = yes  
638.imagick_s: basepeak = yes  
644.nab_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:  
603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)  
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512  
-03 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:
# SPEC CPU®2017 Floating Point Speed Result

## Supermicro

SuperServer SYS-240P-TNRT  
(X12QCH+, Intel Xeon Platinum 8380H)

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**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Sep-2020  
**Hardware Availability:** Sep-2020  
**Software Availability:** Apr-2020

## Peak Optimization Flags (Continued)

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)  
-prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4  
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP  
-mbranches-within-32B-boundaries -nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

## Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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