



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Altos Computing Inc.

BrainSphere W2050h-W275h F4 (AMD EPYC 7402)

CPU2017 License: 97

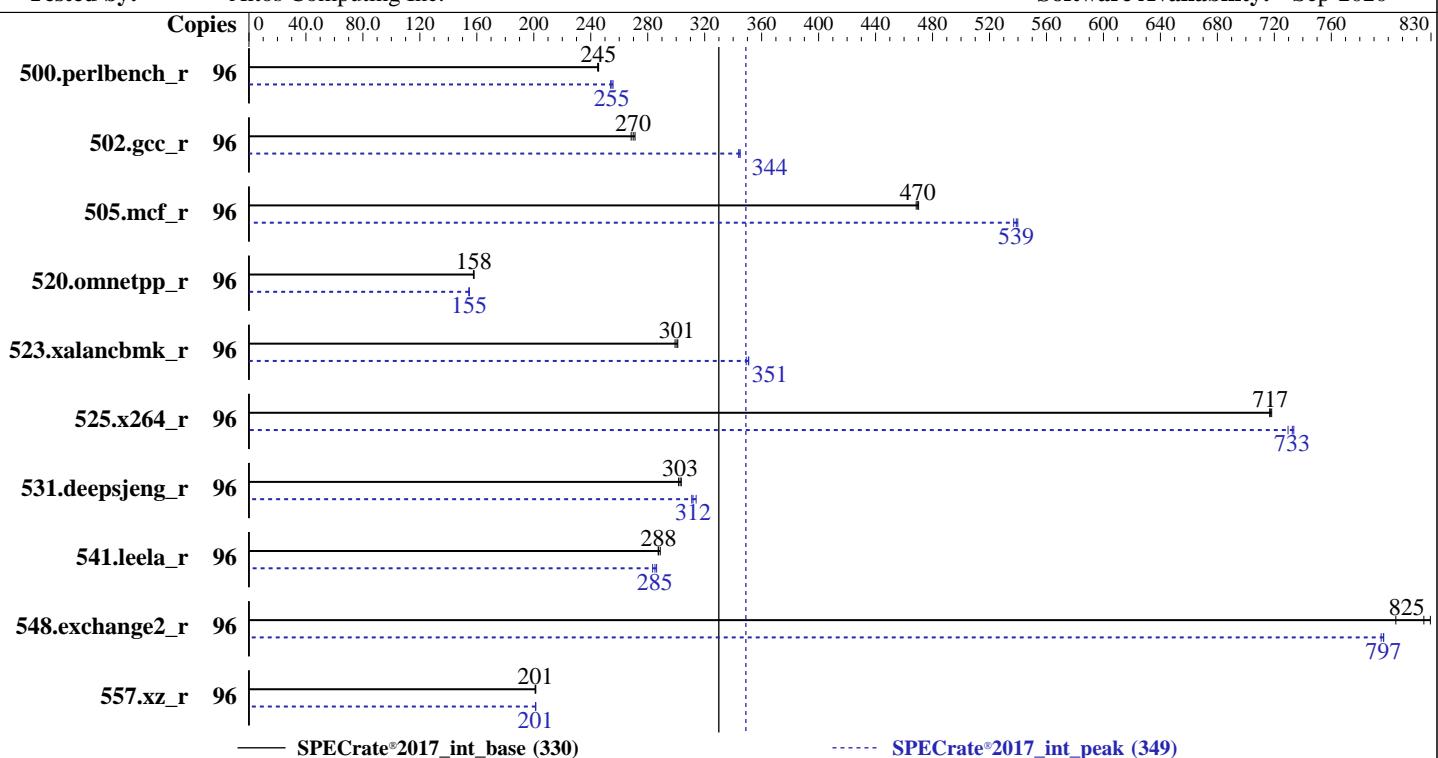
Test Sponsor: Altos Computing Inc.

Tested by: Altos Computing Inc.

Test Date: Sep-2020

Hardware Availability: Dec-2019

Software Availability: Sep-2020



Hardware		Software	
CPU Name:	AMD EPYC 7402	OS:	Ubuntu 18.04.2 LTS
Max MHz:	3350	Compiler:	kernel 4.15.0-117-generic
Nominal:	2800	Parallel:	C/C++/Fortran: Version 2.0.0 of AOCC
Enabled:	48 cores, 2 chips, 2 threads/core	Firmware:	No
Orderable:	1,2 chips	File System:	Version R11 released Oct-2019
Cache L1:	32 KB I + 32 KB D on chip per core	System State:	ext4
L2:	512 KB I+D on chip per core	Base Pointers:	Run level 5 (multi-user)
L3:	128 MB I+D on chip per chip, 16 MB shared / 3 cores	Peak Pointers:	64-bit
Other:	None	Other:	32/64-bit
Memory:	512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)	Power Management:	jemalloc: jemalloc memory allocator library v5.2.0
Storage:	1 x 1.6 TB SATA SSD		BIOS set to prefer performance at the cost of additional power usage
Other:	None		



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**SPECrate®2017\_int\_base = 330**

**SPECrate®2017\_int\_peak = 349**

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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	622	246	<b>623</b>	<b>245</b>	624	245	96	598	256	602	254	<b>600</b>	<b>255</b>		
502.gcc_r	96	507	268	501	271	<b>504</b>	<b>270</b>	96	<b>395</b>	<b>344</b>	395	344	394	345		
505.mcf_r	96	331	469	330	470	<b>330</b>	<b>470</b>	96	<b>288</b>	<b>539</b>	289	537	287	540		
520.omnetpp_r	96	<b>798</b>	<b>158</b>	797	158	799	158	96	<b>815</b>	<b>155</b>	815	155	815	155		
523.xalancbmk_r	96	<b>337</b>	<b>301</b>	337	301	339	299	96	290	349	289	351	<b>289</b>	<b>351</b>		
525.x264_r	96	235	717	<b>234</b>	<b>717</b>	234	718	96	229	734	230	730	<b>229</b>	<b>733</b>		
531.deepsjeng_r	96	362	304	365	302	<b>363</b>	<b>303</b>	96	350	314	<b>353</b>	<b>312</b>	354	311		
541.leela_r	96	553	287	550	289	<b>553</b>	<b>288</b>	96	555	286	<b>558</b>	<b>285</b>	561	283		
548.exchange2_r	96	312	805	<b>305</b>	<b>825</b>	303	830	96	316	797	<b>316</b>	<b>797</b>	316	795		
557.xz_r	96	<b>516</b>	<b>201</b>	516	201	515	201	96	<b>515</b>	201	515	201	<b>515</b>	<b>201</b>		

**SPECrate®2017\_int\_base = 330**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
 'numactl' was used to bind copies to the cores.  
 See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
 numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
 Set swappiness=1 to swap only if necessary  
 Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory sync then drop\_caches=3 to reset caches before invoking runcpu

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

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## Operating System Notes (Continued)

Transparent huge pages set to 'always' for this run (OS default)

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_rate_aocc200_rome_C_lib/64;/home/cpu2017/amd_rate_aoc
    c200_rome_C_lib/32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -fllto  
jemalloc 5.2.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

## Platform Notes

BIOS Configuration:

Power Policy Quick Settings set to Best Performance

NUMA Nodes Per Socket set to NPS4

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on r365f4 Mon Sep 14 07:01:24 2020
```

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : AMD EPYC 7402 24-Core Processor
  2 "physical id"s (chips)
  96 "processors"
```

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 24
siblings   : 48
physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
physical 1: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
```

From lscpu:

```
Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                96
On-line CPU(s) list:  0-95
Thread(s) per core:   2
Core(s) per socket:   24
Socket(s):            2
NUMA node(s):          8
Vendor ID:             AuthenticAMD
CPU family:            23
Model:                 49
Model name:            AMD EPYC 7402 24-Core Processor
Stepping:               0
CPU MHz:               1923.794
CPU max MHz:           2800.0000
CPU min MHz:           1500.0000
BogoMIPS:              5600.37
Virtualization:        AMD-V
L1d cache:             32K
L1i cache:             32K
L2 cache:              512K
L3 cache:              16384K
NUMA node0 CPU(s):    0-5,48-53
NUMA node1 CPU(s):    6-11,54-59
NUMA node2 CPU(s):    12-17,60-65
NUMA node3 CPU(s):    18-23,66-71
NUMA node4 CPU(s):    24-29,72-77
NUMA node5 CPU(s):    30-35,78-83
NUMA node6 CPU(s):    36-41,84-89
NUMA node7 CPU(s):    42-47,90-95
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                      pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                      constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmpfperf pnpi
                      pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
                      rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                      osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
                      cat_13 cdp_13 hw_pstate sme ssbd ibrs ibpb stibp vmmcall fsgsbase bmil avx2 smep
                      bmi2 cqmq rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
```

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### Platform Notes (Continued)

```
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr arat npt  
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v_vmsave_vmlload vgif umip rdpid overflow_recov succor smca
```

```
/proc/cpuinfo cache data  
cache size : 512 KB
```

From numactl --hardware    WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)  
node 0 cpus: 0 1 2 3 4 5 48 49 50 51 52 53  
node 0 size: 64387 MB  
node 0 free: 64025 MB  
node 1 cpus: 6 7 8 9 10 11 54 55 56 57 58 59  
node 1 size: 64505 MB  
node 1 free: 64152 MB  
node 2 cpus: 12 13 14 15 16 17 60 61 62 63 64 65  
node 2 size: 64505 MB  
node 2 free: 64146 MB  
node 3 cpus: 18 19 20 21 22 23 66 67 68 69 70 71  
node 3 size: 64472 MB  
node 3 free: 64028 MB  
node 4 cpus: 24 25 26 27 28 29 72 73 74 75 76 77  
node 4 size: 64505 MB  
node 4 free: 64253 MB  
node 5 cpus: 30 31 32 33 34 35 78 79 80 81 82 83  
node 5 size: 64505 MB  
node 5 free: 64262 MB  
node 6 cpus: 36 37 38 39 40 41 84 85 86 87 88 89  
node 6 size: 64505 MB  
node 6 free: 64261 MB  
node 7 cpus: 42 43 44 45 46 47 90 91 92 93 94 95  
node 7 size: 64503 MB  
node 7 free: 64261 MB  
node distances:  
node 0 1 2 3 4 5 6 7  
0: 10 12 12 12 32 32 32 32  
1: 12 10 12 12 32 32 32 32  
2: 12 12 10 12 32 32 32 32  
3: 12 12 12 10 32 32 32 32  
4: 32 32 32 32 10 12 12 12  
5: 32 32 32 32 12 10 12 12  
6: 32 32 32 32 12 12 10 12  
7: 32 32 32 32 12 12 12 10
```

From /proc/meminfo  
MemTotal: 528272868 kB

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## Platform Notes (Continued)

HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Ubuntu 18.04.2 LTS

```
From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
  NAME="Ubuntu"
  VERSION="18.04.2 LTS (Bionic Beaver)"
  ID=ubuntu
  ID_LIKE=debian
  PRETTY_NAME="Ubuntu 18.04.2 LTS"
  VERSION_ID="18.04"
  HOME_URL="https://www.ubuntu.com/"
  SUPPORT_URL="https://help.ubuntu.com/"
```

uname -a:  
Linux r365f4 4.15.0-117-generic #118-Ubuntu SMP Fri Sep 4 20:02:41 UTC 2020 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

itlb_multihit:	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Full AMD retrpoline, IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling
srbds:	Not affected
tsx_async_abort:	Not affected

run-level 5 Sep 14 06:57

SPEC is set to: /home/cpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 ext4 1.5T 336G 1.1T 25% /

From /sys/devices/virtual/dmi/id  
BIOS: GIGABYTE R11 10/25/2019  
Vendor: Acer

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## Platform Notes (Continued)

Product: Altos R365 F4

Product Family: Server

Serial: DJ7002921A0002

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200

16x Unknown Unknown

(End of data from sysinfo program)

## Compiler Version Notes

```
=====
C      | 502.gcc_r(peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
    AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base, peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
    AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C      | 502.gcc_r(peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
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```

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```
=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
      | 525.x264_r(base, peak) 557.xz_r(base, peak)
-----
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    AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C++     | 523.xalancbmk_r(peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
    AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
      | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
    AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

=====
C++     | 523.xalancbmk_r(peak)
-----
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
    AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
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=====
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
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```

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AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

---

=====  
Fortran | 548.exchange2\_r(base, peak)

---

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

---

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

502.gcc\_r: -DSPEC\_LP64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64



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## Base Optimization Flags

C benchmarks:

```
-floop -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math  
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50  
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp  
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000  
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc  
-lflang
```

C++ benchmarks:

```
-floop -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2  
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC  
-mllvm -unroll-threshold=100 -flv-function-specialization  
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm  
-ljemalloc -lflang
```

Fortran benchmarks:

```
-floop -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops  
-Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs  
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive  
-mllvm -unroll-threshold=150 -lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang



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## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -D_FILE_OFFSET_BITS=64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang

502.gcc_r: -m32 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -fgnu89-inline -ljemalloc
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

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## Altos Computing Inc.

BrainSphere W2050h-W275h F4 (AMD EPYC 7402)

SPECrate®2017\_int\_base = 330

SPECrate®2017\_int\_peak = 349

CPU2017 License: 97

Test Sponsor: Altos Computing Inc.

Tested by: Altos Computing Inc.

Test Date: Sep-2020

Hardware Availability: Dec-2019

Software Availability: Sep-2020

## Peak Optimization Flags (Continued)

```
505.mcf_r: -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver2 -mno-sse4a -fstruct-layout=5  
-mllvm -vectorize-memory-aggressively  
-mllvm -function-specialize -mllvm -enable-gvn-hoist  
-mllvm -unroll-threshold=50 -fremap-arrays  
-mllvm -vector-library=LIBMVEC  
-mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000  
-flv-function-specialization -lmvec -lamdlibm -ljemalloc  
-lflang
```

525.x264\_r: Same as 500.perlbench\_r

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

```
520.omnetpp_r: -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver2 -flv-function-specialization  
-mllvm -unroll-threshold=100  
-mllvm -enable-partial-unswitch  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -vector-library=LIBMVEC  
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc  
-lflang
```

```
523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver2 -flv-function-specialization  
-mllvm -unroll-threshold=100  
-mllvm -enable-partial-unswitch  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -vector-library=LIBMVEC  
-mllvm -inline-threshold=1000 -ljemalloc
```

531.deepsjeng\_r: Same as 520.omnetpp\_r

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SPECrate®2017\_int\_base = 330

SPECrate®2017\_int\_peak = 349

CPU2017 License: 97

Test Sponsor: Altos Computing Inc.

Tested by: Altos Computing Inc.

Test Date: Sep-2020

Hardware Availability: Dec-2019

Software Availability: Sep-2020

## Peak Optimization Flags (Continued)

541.leela\_r: Same as 520.omnetpp\_r

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mllvm -vector-library=LIBMVEC
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Other Flags

C benchmarks:

502.gcc\_r: -L/sppo/dev/cpu2017/v110/amd\_rate\_aocc200\_rome\_C\_lib/32

C++ benchmarks:

523.xalancbmk\_r: -L/sppo/dev/cpu2017/v110/amd\_rate\_aocc200\_rome\_C\_lib/32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-C3.html>

<http://www.spec.org/cpu2017/flags/Altos-Platform-Settings-V1.0-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-C3.xml>

<http://www.spec.org/cpu2017/flags/Altos-Platform-Settings-V1.0-revC.xml>

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