## SPEC CPU®2017 Floating Point Speed Result

**Supermicro**

SuperStorage 6029P-E1CR24H  
(X11DSC+, Intel Xeon Gold 5220R)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>137</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>138</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Sep-2020  
**Hardware Availability:** Feb-2020  
**Software Availability:** Apr-2020

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>48</td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>48</td>
<td></td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>48</td>
<td></td>
<td></td>
</tr>
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<td>48</td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
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<td>48</td>
<td></td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td>48</td>
<td></td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>48</td>
<td></td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>48</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5220R  
- **Max MHz:** 4000  
- **Nominal:** 2200  
- **Enabled:** 48 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 35.75 MB I+D on chip per chip  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 200 GB SATA III SSD  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.1  
- **Kernel:** 4.18.0-147.el8.x86_64  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 3.2 released Oct-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
Spec CPU® 2017 Floating Point Speed Result

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Supermicro
SuperStorage 6029P-E1CR24H
(X11DSC+, Intel Xeon Gold 5220R)

SPECspeed®2017_fp_base = 137
SPECspeed®2017_fp_peak = 138

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>48</td>
<td>122</td>
<td>483</td>
<td>120</td>
<td>491</td>
<td>122</td>
<td>485</td>
<td>122</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>48</td>
<td>99.6</td>
<td>167</td>
<td>102</td>
<td>163</td>
<td>103</td>
<td>162</td>
<td>102</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>48</td>
<td>55.1</td>
<td>95.0</td>
<td>55.0</td>
<td>95.2</td>
<td>58.7</td>
<td>89.2</td>
<td>55.0</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>48</td>
<td>102</td>
<td>129</td>
<td>103</td>
<td>128</td>
<td>102</td>
<td>129</td>
<td>102</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>48</td>
<td>81.8</td>
<td>108</td>
<td>81.3</td>
<td>109</td>
<td>81.5</td>
<td>109</td>
<td>81.5</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>48</td>
<td>181</td>
<td>65.5</td>
<td>183</td>
<td>65.0</td>
<td>184</td>
<td>64.5</td>
<td>183</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>48</td>
<td>140</td>
<td>103</td>
<td>140</td>
<td>103</td>
<td>140</td>
<td>103</td>
<td>140</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>48</td>
<td>69.0</td>
<td>253</td>
<td>69.0</td>
<td>253</td>
<td>69.0</td>
<td>253</td>
<td>69.0</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>48</td>
<td>112</td>
<td>81.1</td>
<td>109</td>
<td>83.8</td>
<td>108</td>
<td>84.2</td>
<td>107</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>48</td>
<td>99.7</td>
<td>158</td>
<td>99.5</td>
<td>158</td>
<td>101</td>
<td>156</td>
<td>101</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes
The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain=true"
OMP_STACKSIZE = "192M"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
Supermicro
SuperStorage 6029P-E1CR24H
(X11DSC+, Intel Xeon Gold 5220R)

| SPECspeed®2017_fp_base = 137 |
| SPECspeed®2017_fp_peak = 138 |

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.


Platform Notes

BIOS Settings:
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Performance
Hyper-Threading = Disable
Stale AtoS = Disable
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011
running on RHEL81-01 Tue Sep 1 06:00:10 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5220R CPU @ 2.20GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
 siblings : 24
 physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
 physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1

(Continued on next page)
Supermicro
SuperStorage 6029P-E1CR24H
(X11DSC+, Intel Xeon Gold 5220R)

SPECspeed®2017_fp_base = 137
SPECspeed®2017_fp_peak = 138

Platform Notes (Continued)

Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5220R CPU @ 2.20GHz
Stepping: 7
CPU MHz: 1000.429
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-23
NUMA node1 CPU(s): 24-47

Flags:

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

(Continued on next page)
Platform Notes (Continued)

From /proc/meminfo
MemTotal: 394877396 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION=8.1 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.1"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga
uname -a:
Linux RHEL81-01 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user
pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional,
RSB filling

run-level 3 Sep 1 01:58
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 185G 73G 112G 40% /

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. 3.2 10/18/2019
Vendor: pm_2019-10-08_18:11:34
Product: ppm_2019-10-08_18:11:37
Serial: ps_2019-10-08_18:11:38
Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
- 12x NO DIMM NO DIMM
- 12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

========================================================================================================
<table>
<thead>
<tr>
<th>C</th>
<th>619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)</th>
</tr>
</thead>
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<tr>
<td></td>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,</td>
</tr>
<tr>
<td></td>
<td>Version 19.1.1.217 Build 20200306</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td></td>
<td>========================================================================</td>
</tr>
<tr>
<td>C++, C, Fortran</td>
<td>607.cactuBSSN_s(base, peak)</td>
</tr>
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<td></td>
<td>Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,</td>
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<td></td>
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</tr>
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<td></td>
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</tr>
<tr>
<td></td>
<td>64, Version 19.1.1.217 Build 20200306</td>
</tr>
<tr>
<td></td>
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</tr>
<tr>
<td></td>
<td>========================================================================</td>
</tr>
<tr>
<td>Fortran</td>
<td>603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)</td>
</tr>
<tr>
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<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)</td>
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<td></td>
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<tr>
<td></td>
<td>========================================================================</td>
</tr>
<tr>
<td>Fortran, C</td>
<td>621.wrf_s(base, peak) 627.cam4_s(base, peak)</td>
</tr>
</tbody>
</table>
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(X11DSC+, Intel Xeon Gold 5220R)

SPECspeed®2017_fp_base = 137
SPECspeed®2017_fp_peak = 138

Compiler Version Notes (Continued)

| 628.pop2_s(base, peak)
----------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
----------------------------------------------

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
### Base Optimization Flags

C benchmarks:
- m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- ff infinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- mbranches-within-32B-boundaries

Fortran benchmarks:
- m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
- no-prec-div -qopt-prefetch -ff infinite-math-only
- qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
- mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
- ljemalloc

Benchmarks using both Fortran and C:
- m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-prefetch -ff infinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
- m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- qopt-prefetch -ff infinite-math-only -qopt-mem-layout-trans=4 -qopenmp
- DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

### Peak Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

### Peak Portability Flags

Same as Base Portability Flags
Supermicro
SuperStorage 6029P-E1CR24H (X11DSC+, Intel Xeon Gold 5220R)

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CPU2017 License: 001176
Test Sponsor: Supermicro
Test Date: Sep-2020
Hardware Availability: Feb-2020
Tested by: Supermicro
Software Availability: Apr-2020

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-CLX-revG.html
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SuperStorage 6029P-E1CR24H
(X11DSC+, Intel Xeon Gold 5220R)

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Hardware Availability: Feb-2020
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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-CLX-revG.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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