## SPEC CPU®2017 Integer Speed Result

**New H3C Technologies Co., Ltd.**

H3C UniServer R4900 G3 (Intel Xeon Gold 6234)

<table>
<thead>
<tr>
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<td>9066</td>
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<td>Apr-2020</td>
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### Threads

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<td><img src="image" alt="Threads" /></td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6234  
  - Max MHz: 4000  
  - Nominal: 3300  
  - Enabled: 16 cores, 2 chips  
  - Orderable: 1.2 chips  
  - Cache L1: 32 KB I + 32 KB D on chip per core  
  - L2: 1 MB I+D on chip per core  
  - L3: 24.75 MB I+D on chip per chip  
  - Other: None  
  - Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2933V-R)  
  - Storage: 2 x 600 GB SAS HDD,10k RPM,RAID 1  
  - Other: None

### Software

- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa)  
  - 4.18.0-193.el8.x86_64  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 2.00.33 released Aug-2019 BIOS  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
<td>263</td>
<td>6.76</td>
<td>263</td>
<td>6.75</td>
<td>261</td>
<td>6.79</td>
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<tr>
<td>602.gcc_s</td>
<td>16</td>
<td>391</td>
<td>10.2</td>
<td>390</td>
<td>10.2</td>
<td>393</td>
<td>10.1</td>
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<tr>
<td>605.mcf_s</td>
<td>16</td>
<td>255</td>
<td>18.5</td>
<td>254</td>
<td>18.6</td>
<td>255</td>
<td>18.5</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>205</td>
<td>7.94</td>
<td>196</td>
<td>8.31</td>
<td>197</td>
<td>8.28</td>
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<td>623.xalanchmk_s</td>
<td>16</td>
<td>105</td>
<td>13.5</td>
<td>105</td>
<td>13.5</td>
<td>105</td>
<td>13.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>16</td>
<td>110</td>
<td>16.1</td>
<td>109</td>
<td>16.1</td>
<td>109</td>
<td>16.2</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>16</td>
<td>244</td>
<td>5.88</td>
<td>245</td>
<td>5.86</td>
<td>244</td>
<td>5.88</td>
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<tr>
<td>641.leela_s</td>
<td>16</td>
<td>349</td>
<td>4.89</td>
<td>349</td>
<td>4.89</td>
<td>348</td>
<td>4.90</td>
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<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>174</td>
<td>16.9</td>
<td>174</td>
<td>16.9</td>
<td>174</td>
<td>16.9</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>16</td>
<td>297</td>
<td>20.8</td>
<td>297</td>
<td>20.8</td>
<td>297</td>
<td>20.8</td>
</tr>
</tbody>
</table>

- **SPECspeed**\textsuperscript{2017\_int\_base} = 10.9
- **SPECspeed**\textsuperscript{2017\_int\_peak} = 11.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux. The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

### Operating System Notes

Stack size set to unlimited using "ulimit \(-s\) unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP\_AFFINITY = "granularity=fine,scatter"
- LD\_LIBRARY\_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
- MALLOC\_CONF = "retain:true"
- OMP\_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6234)

**SPECspeed®2017_int_base = 10.9**  
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**General Notes (Continued)**

is mitigated in the system as tested and documented.  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
```bash  
sync; echo 3 > /proc/sys/vm/drop_caches  
```
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  

**Platform Notes**

**BIOS Settings:**
Set Hyper Threading to Disabled  
Set IMC Interleaving to 2-way Interleave  
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7ed1be6e46a485a0011  
running on localhost.localdomain Sun Sep 13 11:53:31 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6234 CPU @ 3.30GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 2 3 9 20 24 25 27
physical 1: cores 1 3 4 9 17 18 24 27
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 1
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
```

(Continued on next page)
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**Software Availability:** Apr-2020

### Platform Notes (Continued)

- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Gold 6234 CPU @ 3.30GHz
- **Stepping:** 7
- **CPU MHz:** 3014.342
- **CPU max MHz:** 4000.00
- **CPU min MHz:** 1200.0000
- **BogoMIPS:** 6600.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 25344K
- **NUMA node0 CPU(s):** 0-7
- **NUMA node1 CPU(s):** 8-15

**Flags:**

- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp lp0

The CPU family, model, and processor name are shown.

**/proc/cpuinfo cache data**

- cache size : 25344 KB

**WARNING:** a numactl 'node' might or might not correspond to a physical chip.

**Available options:**

- node 0 cpus: 0 1 2 3 4 5 6 7
- node 0 size: 96019 MB
- node 0 free: 95073 MB
- node 1 cpus: 8 9 10 11 12 13 14 15
- node 1 size: 96738 MB
- node 1 free: 96269 MB
- node distances:
  - node 0 1
  - 0: 10 21
  - 1: 21 10

**From /proc/meminfo**

- MemTotal: 197383300 kB

(Continued on next page)
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### Platform Notes (Continued)

- **HugePages_Total:** 0
- **Hugepagesize:** 2048 kB

From `/etc/*release* /etc/*version*`:

```plaintext
os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.2 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.2"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
    ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
    Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
    x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- **itlb_multihit:** KVM: Mitigation: Split huge pages
- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- **tsx_async_abort:** Mitigation: Clear CPU buffers; SMT disabled

run-level 3 Sep 13 11:52

SPEC is set to: /home/speccpu

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/mapper/rhel-home</td>
<td>xfs</td>
<td>503G</td>
<td>46G</td>
<td>457G</td>
<td>10%</td>
<td>/home</td>
</tr>
</tbody>
</table>

From `/sys/devices/virtual/dmi/id`

- **BIOS:** American Megatrends Inc. 2.00.33 08/22/2019
- **Vendor:** H3C
- **Product:** RS33M2C9S
- **Product Family:** Rack

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Platform Notes (Continued)
Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
6x Hynix HMA82GR7CJR8N-WM 16 GB 2 rank 2933
12x NO DIMM NO DIMM
6x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

Compiler Version Notes

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
   625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 600.perlbench_s(peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
   625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C | 600.perlbench_s(peak)
==============================================================================
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### Compiler Version Notes (Continued)

```plaintext
==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)  
        | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
Fortran | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

---

### Base Compiler Invocation

- **C benchmarks:**  
  - `icc`
- **C++ benchmarks:**  
  - `icpc`
- **Fortran benchmarks:**  
  - `ifort`

---

### Base Portability Flags

- `600.perlbench_s`: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- `602.gcc_s`: `-DSPEC_LP64`
- `605.mcf_s`: `-DSPEC_LP64`
- `620.omnetpp_s`: `-DSPEC_LP64`
- `623.xalancbmk_s`: `-DSPEC_LP64 -DSPEC_LINUX`
- `625.x264_s`: `-DSPEC_LP64`
- `631.deepsjeng_s`: `-DSPEC_LP64`
- `641.leela_s`: `-DSPEC_LP64`
- `648.exchange2_s`: `-DSPEC_LP64`
- `657.xz_s`: `-DSPEC_LP64`
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## Base Optimization Flags

**C benchmarks:**
- `m64 -qnextgen -std=c11`
- `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
- `-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops`
- `-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**C++ benchmarks:**
- `m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries`
- `-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse`
- `-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin -lqkmalloc`

**Fortran benchmarks:**
- `m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512`
- `-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte`
- `-mbranches-within-32B-boundaries`

## Peak Compiler Invocation

**C benchmarks:**
- `icc`

**C++ benchmarks:**
- `icpc`

**Fortran benchmarks:**
- `ifort`

## Peak Portability Flags

- `600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64`
- `605.mcf_s: -DSPEC_LP64`
- `620.omnetpp_s: -DSPEC_LP64`
- `623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX`
- `625.x264_s: -DSPEC_LP64`
- `631.deepsjeng_s: -DSPEC_LP64`
- `641.leela_s: -DSPEC_LP64`

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Peak Portability Flags (Continued)

648.exchange2_s: -DSPEC_LP64  
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)  
-xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold  
-Wl,-plugin-opt=-x86-brances-within-32B-boundaries  
-Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto  
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-brances-within-32B-boundaries  
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math  
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

(Continued on next page)
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<td>New H3C Technologies Co., Ltd.</td>
<td>Mar-2019</td>
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<td>Apr-2020</td>
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#### Peak Optimization Flags (Continued)

648.exchange2_s:basepeak = yes

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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