## SPEC CPU®2017 Floating Point Rate Result

**New H3C Technologies Co., Ltd.**

**H3C UniServer R4900 G3 (Intel Xeon Gold 5222)**

**SPECcpu®2017_fp_base = 82.8**

**SPECcpu®2017_fp_peak = 83.2**

<table>
<thead>
<tr>
<th>CPU2017 License: 9066</th>
<th>Test Date: Sep-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: New H3C Technologies Co., Ltd.</td>
<td>Hardware Availability: Mar-2019</td>
</tr>
<tr>
<td>Tested by: New H3C Technologies Co., Ltd.</td>
<td>Software Availability: Apr-2020</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECcpu®2017_fp_base</th>
<th>SPECcpu®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>50.0</td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>60.4</td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>76.0</td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>47.3</td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>92.6</td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>64.9</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>74.6</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>89.8</td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>92.6</td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>68.0</td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>48.3</td>
<td></td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Gold 5222
- **Max MHz:** 3900
- **Nominal:** 3800
- **Enabled:** 8 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D per chip core
- **L2:** 1 MB I+D per chip core
- **L3:** 16.5 MB I+D per chip
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933V-R)
- **Storage:** 2 x 600 GB SAS HDD, 10k RPM, RAID 1
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux;
  Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux
- **Parallel:** No
- **Firmware:** Version 2.00.33 released Aug-2019 BIOS
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>582</td>
<td>276</td>
<td>580</td>
<td>277</td>
<td>8</td>
<td>300</td>
<td>268</td>
<td>299</td>
<td>268</td>
<td>300</td>
<td>268</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>215</td>
<td>94.2</td>
<td>212</td>
<td>95.5</td>
<td>16</td>
<td>215</td>
<td>94.2</td>
<td>212</td>
<td>95.5</td>
<td>210</td>
<td>96.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>304</td>
<td>50.0</td>
<td>304</td>
<td>50.0</td>
<td>16</td>
<td>304</td>
<td>50.0</td>
<td>304</td>
<td>50.0</td>
<td>301</td>
<td>50.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>693</td>
<td>60.4</td>
<td>693</td>
<td>60.4</td>
<td>694</td>
<td>60.3</td>
<td>8</td>
<td>335</td>
<td>62.4</td>
<td>335</td>
<td>62.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>492</td>
<td>75.9</td>
<td>492</td>
<td>76.0</td>
<td>492</td>
<td>76.0</td>
<td>16</td>
<td>416</td>
<td>89.8</td>
<td>415</td>
<td>90.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>375</td>
<td>64.9</td>
<td>376</td>
<td>64.9</td>
<td>376</td>
<td>64.8</td>
<td>16</td>
<td>375</td>
<td>64.9</td>
<td>376</td>
<td>64.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>387</td>
<td>92.6</td>
<td>387</td>
<td>92.6</td>
<td>387</td>
<td>94.9</td>
<td>8</td>
<td>227</td>
<td>79.0</td>
<td>227</td>
<td>79.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>375</td>
<td>64.9</td>
<td>376</td>
<td>64.9</td>
<td>376</td>
<td>64.8</td>
<td>16</td>
<td>375</td>
<td>64.9</td>
<td>376</td>
<td>64.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>381</td>
<td>73.5</td>
<td>370</td>
<td>75.6</td>
<td>375</td>
<td>74.6</td>
<td>16</td>
<td>381</td>
<td>73.5</td>
<td>370</td>
<td>75.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>225</td>
<td>177</td>
<td>226</td>
<td>176</td>
<td>225</td>
<td>177</td>
<td>16</td>
<td>225</td>
<td>177</td>
<td>226</td>
<td>176</td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>236</td>
<td>114</td>
<td>235</td>
<td>115</td>
<td>235</td>
<td>114</td>
<td>16</td>
<td>236</td>
<td>114</td>
<td>235</td>
<td>115</td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>911</td>
<td>68.4</td>
<td>917</td>
<td>68.0</td>
<td>944</td>
<td>66.1</td>
<td>16</td>
<td>911</td>
<td>68.4</td>
<td>917</td>
<td>68.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>527</td>
<td>48.3</td>
<td>526</td>
<td>48.3</td>
<td>516</td>
<td>49.3</td>
<td>8</td>
<td>247</td>
<td>51.5</td>
<td>247</td>
<td>51.4</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base = 82.8**  
**SPECrate®2017_fp_peak = 83.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Compiler Notes**

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.  
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux  
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit'  
was used to generate numactl commands to bind each copy to a specific processor.  
For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:  
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"  
MALLOC_CONF = "retain:true"
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

SPECrate®2017_fp_base = 82.8
SPECrate®2017_fp_peak = 83.2

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
Set SNC to Enabled
Set XPT Prefetch to Enabled
Set IMC Interleaving to 1-way Interleave
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbb1e666e485a0011
running on localhost.localdomain Fri Sep 11 23:41:54 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 5222 CPU @ 3.80GHz
    2 "physical id"s (chips)
    16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 4
    siblings : 8
    physical 0: cores 0 5 9 13
    physical 1: cores 0 2 11 13

From lscpu:

(Continued on next page)
New H3C Technologies Co., Ltd. | SPEC CPU®2017 Floating Point Rate Result
H3C UniServer R4900 G3 (Intel Xeon Gold 5222) | SPECrate®2017_fp_base = 82.8
| SPECrate®2017_fp_peak = 83.2

CPU2017 License: 9066  Test Date: Sep-2020
Test Sponsor: New H3C Technologies Co., Ltd.  Hardware Availability: Mar-2019
Tested by: New H3C Technologies Co., Ltd.  Software Availability: Apr-2020

Platform Notes (Continued)

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5222 CPU @ 3.80GHz
Stepping: 7
CPU MHz: 3680.468
CPU max MHz: 3900.0000
CPU min MHz: 1200.0000
BogoMIPS: 7600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0,2,8,10
NUMA node1 CPU(s): 1,3,9,11
NUMA node2 CPU(s): 4,5,12,13
NUMA node3 CPU(s): 6,7,14,15
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtses64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mxp rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsavesopt xsaves qgetbv1 xsaveopt avx512_vnni

/proc/cpuinfo cache data

cache size : 16896 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 2 8 10

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

SPECrate®2017_fp_peak = 83.2
SPECrate®2017_fp_base = 82.8

Platform Notes (Continued)

node 0 size: 95079 MB
node 0 free: 83529 MB
node 1 cpus: 1 3 9 11
node 1 size: 96766 MB
node 1 free: 93354 MB
node 2 cpus: 4 5 12 13
node 2 size: 96766 MB
node 2 free: 93060 MB
node 3 cpus: 6 7 14 15
node 3 size: 96738 MB
node 3 free: 93572 MB
node distances:
  node 0 1 2 3
  0: 10 11 21 21
  1: 11 10 21 21
  2: 21 21 10 11
  3: 21 21 11 10

From /proc/meminfo
MemTotal: 394600176 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.2 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.2"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
  Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

  itlb_multihit:                 KVM: Mitigation: Split huge pages
  CVE-2018-3620 (L1 Terminal Fault): Not affected
  Microarchitectural Data Sampling: Not affected
  CVE-2017-5754 (Meltdown): Not affected

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

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CPU2017 License: 9066
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Test Date: Sep-2020
Hardware Availability: Mar-2019
Software Availability: Apr-2020

Platform Notes (Continued)

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
tsx_async_abort: Mitigation: Clear CPU buffers; SMT vulnerable

run-level 3 Sep 11 17:24 last=5

SPEC is set to: /home/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 504G 58G 446G 12% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. 2.00.33 08/22/2019
Vendor: H3C
Product: RS33M2C9S
Product Family: Rack

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
12x NO DIMM NO DIMM
1x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933
11x Samsung M393A4K40DB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C               | 519.ibm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak) |
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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==============================================================================
| C++             | 508.namd_r(base, peak) 510.parest_r(base, peak) |
==============================================================================

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1

(Continued on next page)
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Test Date: Sep-2020
Hardware Availability: Mar-2019
Tested by: New H3C Technologies Co., Ltd.
Software Availability: Apr-2020

Compiler Version Notes (Continued)

NextGen Build 20200304
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C++, C | 511.povray_r(base) 526.blender_r(base, peak)
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
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C++, C | 511.povray_r(base) 526.blender_r(base, peak)
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Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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C++, C | 511.povray_r(peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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(Continued on next page)
## Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>C++, C, Fortran</th>
<th>507.cactuBSSN_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
<tr>
<td>Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306</td>
<td></td>
</tr>
<tr>
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<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fortran</th>
<th>503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306</td>
<td></td>
</tr>
<tr>
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<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fortran, C</th>
<th>521.wrf_r(base) 527.cam4_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306</td>
<td></td>
</tr>
<tr>
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</tr>
<tr>
<td>Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1 NextGen Build 20200304</td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
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</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fortran, C</th>
<th>521.wrf_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306</td>
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<tr>
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<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306</td>
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New H3C Technologies Co., Ltd.  H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

| SPECrate®2017_fp_base = 82.8 | SPECrate®2017_fp_peak = 83.2 |

CPU2017 License: 9066  Test Date: Sep-2020
Test Sponsor: New H3C Technologies Co., Ltd.  Hardware Availability: Mar-2019
Tested by: New H3C Technologies Co., Ltd.  Software Availability: Apr-2020

Compiler Version Notes (Continued)

Fortran, C  | 521.wrf_r(base) 527.cam4_r(base, peak)

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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
SPEC CPU®2017 Floating Point Rate Result

New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

SPECrate®2017_fp_base = 82.8
SPECrate®2017_fp_peak = 83.2

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Hardware Availability: Mar-2019
Software Availability: Apr-2020

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -fmfpmath=sse
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
-m64 -Wl, -plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -fmfpmath=sse
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles

(Continued on next page)
New H3C Technologies Co., Ltd.  SPECrate®2017_fp_base = 82.8
H3C UniServer R4900 G3 (Intel Xeon Gold 5222)  SPECrate®2017_fp_peak = 83.2

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Sep-2020
Hardware Availability: Mar-2019
Software Availability: Apr-2020

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
-`align array32byte -auto -mbranches-within-32B-boundaries`
-`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using both C and C++:
-`-m64 -qnextgen -std=c11`
-`-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
-`-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse`
-`-funroll-loops -qopt-mem-layout-trans=4`
-`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using Fortran, C, and C++:
-`-m64 -qnextgen -std=c11`
-`-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs`
-`-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse`
-`-funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div`
-`-qopt-prefetch -ffinite-math-only`
-`-qopt-multiple-gather-scatter-by-shuffles` `-nostandard-realloc-lhs`
-`-align array32byte -auto -mbranches-within-32B-boundaries`
-`-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Peak Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`ifort icc`

Benchmarks using both C and C++:
`icpc icc`

Benchmarks using Fortran, C, and C++:
`icpc icc ifort`
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

SPEC CPU®2017 Floating Point Rate Result

SPECrate®2017_fp_base = 82.8
SPECrate®2017_fp_peak = 83.2

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Sep-2020
Hardware Availability: Mar-2019
Software Availability: Apr-2020

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes
544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes
510.parest_r: -m64 -gnextgen
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Fortran benchmarks:

503.bwaves_r: -m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

549.fotonik3d_r: basepeak = yes
554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:

521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-nostandard-realloc-lhs -align array32byte -auto

(Continued on next page)
New H3C Technologies Co., Ltd. H3C UniServer R4900 G3 (Intel Xeon Gold 5222)

**SPECrate®2017_fp_base = 82.8**

**SPECrate®2017_fp_peak = 83.2**

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Sep-2020  
**Hardware Availability:** Mar-2019  
**Software Availability:** Apr-2020

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### Peak Optimization Flags (Continued)

521.wrf_r (continued):
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

527.cam4_r: `basepeak = yes`

Benchmarks using both C and C++:

511.povray_r: `-prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3`  
- `-ipo -no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles`  
- `-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries`  
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

526.blender_r: `basepeak = yes`

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: `basepeak = yes`

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The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html)

You can also download the XML flags sources by saving the following links:  
- [http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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