SPEC CPU®2017 Floating Point Rate Result

New H3C Technologies Co., Ltd. H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPECrate®2017_fp_base = 253
SPECrate®2017_fp_peak = 269

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Hardware Availability: Mar-2020
Tested by: New H3C Technologies Co., Ltd.
Software Availability: Apr-2020

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<td>507.cactuBSSN_r</td>
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<td>554.roms_r</td>
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</table>

Hardware

CPU Name: Intel Xeon Gold 6230R
Max MHz: 4000
Nominal: 2100
Enabled: 52 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 35.75 MB I+D on chip per chip
Other: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2933V-R)
Storage: 2 x 600 GB SAS HDD, 10k RPM, RAID 1
Other: None

Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64
Compiler: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux;
Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux
Parallel: No
Firmware: Version 2.00.33 released Aug-2019 BIOS
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage
### Results Table

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<tr>
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<th>Ratio</th>
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**Compiler Notes**

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux. The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"
New H3C Technologies Co., Ltd. | SPEC®2017_fp_base = 253  
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R) | SPEC®2017_fp_peak = 269

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:
```
numactl --interleave=all runcpu <etc>
```


### Platform Notes

BIOS settings:
Set SNC to Enabled
Set IMC Interleaving to 1-way Interleave
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbe6e464a485a0011
running on localhost.localdomain Sat Sep 12 19:24:11 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) Gold 6230R CPU @ 2.10GHz
  2 "physical id"s (chips)
  104 "processors"
 cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 26
  siblings : 52
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
```

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPECRate®2017_fp_base = 253
SPECRate®2017_fp_peak = 269

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Hardware Availability: Mar-2020
Tested by: New H3C Technologies Co., Ltd.
Software Availability: Apr-2020

Platform Notes (Continued)

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 104
On-line CPU(s) list: 0-103
Thread(s) per core: 2
Core(s) per socket: 26
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6230R CPU @ 2.10GHz
Stepping: 7
CPU MHz: 3667.944
CPU max MHz: 4000.000
CPU min MHz: 1000.000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,7-9,13-15,20-22,52-55,59-61,65-67,72-74
NUMA node1 CPU(s): 4-6,10-12,16-19,23-25,56-58,62-64,68-71,75-77
NUMA node3 CPU(s): 30-32,36-38,42-44,45-49,51-53,58-60,65-67,72-74
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdelgb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtex64 monitor ds_cpl vmm vmx smx est tm2 ssse3 sdbbg fma cx16
xtrr pdcmd cdc dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdll_l3
invpcid_single intel_pinn ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmx
iflexpriority ept vpid fsgsbase tsc_adjust bni hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total
cqm_mbb_local dtherm ida arat pln pts pkup ospe avx512_vnni md_clear flush_lid
arch_capabilities

/proc/cpuinfo cache data
cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
## SPEC CPU®2017 Floating Point Rate Result

### New H3C Technologies Co., Ltd.

**H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)**

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
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<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
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**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Sep-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

### Platform Notes (Continued)

| node 0 cpus: | 0 1 2 3 7 8 9 13 14 15 20 21 22 52 53 54 55 59 60 61 65 66 67 72 73 74 |
| node 0 size: | 46690 MB |
| node 0 free: | 31664 MB |
| node 1 cpus: | 4 5 6 10 11 12 16 18 19 23 24 25 56 57 58 62 63 64 68 69 70 71 75 76 77 |
| node 1 size: | 48378 MB |
| node 1 free: | 37646 MB |
| node 2 cpus: | 26 27 28 29 33 34 35 39 40 41 46 47 48 78 79 80 81 85 86 87 91 92 93 98 99 |
| node 2 size: | 48378 MB |
| node 2 free: | 37221 MB |
| node 3 cpus: | 30 31 32 36 37 38 42 43 44 45 49 50 51 82 83 84 88 89 90 94 95 96 97 101 102 103 |
| node 3 size: | 48350 MB |
| node 3 free: | 37713 MB |

### From `/proc/meminfo`

- MemTotal: 196400116 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

### From `/etc/*release* /etc/*version*`

```
NAME="Red Hat Enterprise Linux"  
VERSION="8.2 (Ootpa)"  
ID="rhel"  
ID_LIKE="fedora"  
VERSION_ID="8.2"  
PLATFORM_ID="platform:el8"  
PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"  
ANSI_COLOR="0;31"  
redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)  
system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga
```

### uname -a:

```
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020  
x86_64 x86_64 x86_64 GNU/Linux
```

### Kernel self-reported vulnerability status:

```
itlb_multihit:  
KVM: Mitigation: Split huge pages
```

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 253
SPECrate®2017_fp_peak = 269

CPU2017 License: 9066
Test Date: Sep-2020
CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Tested by: New H3C Technologies Co., Ltd.
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Platform Notes (Continued)

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Symmetry variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Symmetry variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
tsx_async_abort:
Mitigation: Clear CPU buffers; SMT vulnerable

run-level 3 Sep 12 10:05

SPEC is set to: /home/spec/cpu

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 503G 81G 422G 16% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMI BIOS" standard.

Memory:
12x NO DIMM NO DIMM
12x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

Compiler Version Notes

C
519.ibm_r(base, peak) 538.imagick_r(base, peak)
544.nab_r(base, peak)

C++
508.namd_r(base, peak) 510.parest_r(base, peak)

(Continued on next page)
New H3C Technologies Co., Ltd. H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPECrater®2017_fp_base = 253
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Compiler Version Notes (Continued)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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C++, C | 511.povray_r(base) 526.blender_r(base, peak)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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C++, C | 511.povray_r(peak)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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C++, C | 511.povray_r(base) 526.blender_r(base, peak)

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Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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C++, C | 511.povray_r(peak)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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Intel(R) C Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
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Compiler Version Notes (Continued)
**Compiler Version Notes (Continued)**

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### C++, C, Fortran

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<td>507.cactuBSSN_r(base, peak)</td>
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**Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1**

NextGen Build 20200304

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**Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1**

NextGen Build 20200304

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**Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306**

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---

### Fortran

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**Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

### Fortran, C

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</tr>
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</table>

**Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306**

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**Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1**

NextGen Build 20200304

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### Fortran, C

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**Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306**

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New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPECraten®2017_fp_base = 253  
SPECraten®2017_fp_peak = 269

CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.  
Tested by: New H3C Technologies Co., Ltd.

Test Date: Sep-2020  
Hardware Availability: Mar-2020  
Software Availability: Apr-2020

Compiler Version Notes (Continued)

== Fortran, C == 521.wrf_r(base) 527.cam4_r(base, peak)

-----------------------------------------
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NextGen Build 20200304
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-----------------------------------------

== Fortran, C == 521.wrf_r(peak)

-----------------------------------------
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306
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-----------------------------------------

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

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**Hardware Availability:** Mar-2020

**Tested by:** New H3C Technologies Co., Ltd.  
**Software Availability:** Apr-2020

### Base Portability Flags

503.bwaves_r: -DSPEC_LP64  
507.cactuBSSN_r: -DSPEC_LP64  
508.namd_r: -DSPEC_LP64  
510.parest_r: -DSPEC_LP64  
511.povray_r: -DSPEC_LP64  
519.lbm_r: -DSPEC_LP64  
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char  
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG  
538.imagick_r: -DSPEC_LP64  
544.nab_r: -DSPEC_LP64  
549.fotonik3d_r: -DSPEC_LP64  
554.roms_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**

-m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**C++ benchmarks:**

-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries  
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**Fortran benchmarks:**

-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-fuse-ld=gold -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-multiple-gather-scatter-by-shuffles  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte  
-auto -mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**Benchmarks using both Fortran and C:**

-m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div  
-qopt-prefetch -ffinite-math-only  
-qopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs

(Continued on next page)
New H3C Technologies Co., Ltd. SPEC®2017 Floating Point Rate Result

H3C UniServer R4900 G3 (Intel Xeon Gold 6230R) SPECrate®2017_fp_base = 253

SPECrate®2017_fp_peak = 269

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Sep-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
-align array32byte -auto -mbranches-within-32B-boundaries
-ld=-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both C and C++:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-ld=-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-ld=-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
New H3C Technologies Co., Ltd.∥SPEC®2017 Floating Point Rate Result∥H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)

SPECrate®2017_fp_base = 253
SPECrate®2017_fp_peak = 269

CPU2017 License: 9066
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Tested by: New H3C Technologies Co., Ltd.

Test Date: Sep-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes
544.nab_r: basepeak = yes

C++ benchmarks:
508.namd_r: basepeak = yes
510.parest_r -m64 -gnextgen
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc64-5.0.1/lib
-1jemalloc

Fortran benchmarks:
503.bwaves_r -m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -1jemalloc

549.fotonik3d_r: basepeak = yes
554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:
521.wrf_r -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-nostandard-realloc-lhs -align array32byte -auto

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6230R)  

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## Peak Optimization Flags (Continued)

521.wrf_r (continued):  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

527.cam4_r: basepeak = yes

### Benchmarks using both C and C++:

511.povray_r:  
-prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3  
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-multiple-gather-scatter-by-shuffles  
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

526.blender_r: basepeak = yes

### Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at  
[http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html)

You can also download the XML flags sources by saving the following links:  
[http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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