Lenovo Global Technology
ThinkSystem SR250
(3.40 GHz, Intel Xeon E-2224)

| Test Sponsor: | Lenovo Global Technology |
| Test Date:    | Jul-2020 |
| Hardware Availability: | Mar-2020 |
| Tested by:    | Lenovo Global Technology |
| Software Availability: | Apr-2020 |

**CPU2017 License:** 9017

**Tested by:** Lenovo Global Technology

**Software**

- **OS:** SUSE Linux Enterprise Server 15 SP1 (x86_64)
- **Kernel:** 4.12.14-195-default
- **Compiler:**
  - C/C++: Version 19.1.1.217 of Intel C/C++
  - Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Lenovo BIOS Version ISE115D 2.10 released Apr-2020
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage

**Hardware**

- **CPU Name:** Intel Xeon E-2224
- **Max MHz:** 4600
- **Nominal:** 3400
- **Enabled:** 4 cores, 1 chip
- **Orderable:** 1 chip
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 256 KB I+D on chip per core
- **L3:** 8 MB I+D on chip per chip
- **Other:** None
- **Memory:** 128 GB (4 x 32 GB 2Rx4 PC4-2666V-E)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None

<table>
<thead>
<tr>
<th>Thread Count</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
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<tr>
<td>4</td>
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<tr>
<td>8</td>
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<td>25.9</td>
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<td>16</td>
<td>23.2</td>
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<tr>
<td>32</td>
<td>21.0</td>
<td>21.1</td>
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</table>

**Threaded Benchmark Results:**

- **Threads:**
  - 603.bwaves_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 607.cactuBSSN_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 619.lbm_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 621.wrf_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 627.cam4_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 628.pop2_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 638.imagick_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 644.nab_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 649.fotonik3d_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
  - 654.roms_s: 4 threads, SPECspeed®2017_fp_peak = 27.3
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>649.fotonik3d_s</td>
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<td>18.1</td>
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<tr>
<td>654.roms_s</td>
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<td>1016</td>
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<td>1016</td>
<td>15.5</td>
<td>1014</td>
<td>15.5</td>
</tr>
</tbody>
</table>

### SPECspeed®2017_fp_base = 27.0
### SPECspeed®2017_fp_peak = 27.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- `KMP_AFFINITY = "granularity=fine,compact"
- `LD_LIBRARY_PATH = "/home/cpu2017-1.1.0-ic19.1.1/lib/intel64:/home/cpu2017-1.1.0-ic19.1.1/jce5.0.1-64"
- `MALLOC_CONF = "retain:true"
- `OMP_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0.

Transparent Huge Pages enabled by default.

Prior to runcpu invocation:
- Filesystem page cache synced and cleared with:
  - `sync; echo 3> /proc/sys/vm/drop_caches`

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jul-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

General Notes (Continued)

is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance

Sysinfo program /home/cpu2017-1.1.0-ic19.1.1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7eddb1e6e46a485a0011
running on linux-jecn Thu Jul  2 14:47:47 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) E-2224 CPU @ 3.40GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 39 bits physical, 48 bits virtual
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Xeon(R) E-2224 CPU @ 3.40GHz
Stepping: 10
CPU MHz: 3400.000
CPU max MHz: 4600.0000

(Continued on next page)
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Platform Notes (Continued)

CPU min MHz: 800.0000
BogoMIPS: 6816.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 8192K
NUMA node0 CPU(s): 0-3
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb invpcid_single pti ssbd ibrs ibpb tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms cmov rdtscp aarch64 cpuid_fault

From /proc/cpuinfo cache data
   cache size : 8192 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 1 nodes (0)
   node 0 cpus: 0 1 2 3
   node 0 size: 128867 MB
   node 0 free: 128378 MB
   node distances:
     node 0 0
     0: 10

From /proc/meminfo
   MemTotal: 131960244 kB
   HugePages_Total: 0
   Hugepagesize: 2048 kB

From /usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 15 SP1

From /etc/*release* /etc/*version*
   os-release:
     NAME="SLES"
     VERSION="15-SP1"
     VERSION_ID="15.1"
     PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
     ID="sles"

(Continued on next page)
Platform Notes (Continued)

```
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp1"
```

```
uname -a:
   Linux linux-jecn 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion; VMX: conditional
   cache flushes, SMT disabled
Microarchitectural Data Sampling: Mitigation: Clear CPU buffers; SMT disabled
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
   via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted
   Speculation, IBPB: conditional, IBRS_FW, STIBP: disabled, RSB filling
```

```
run-level 3 Jul 2 14:47
```

```
SPEC is set to: /home/cpu2017-1.1.0-ic19.1.1
   Filesystem  Type Size Used Avail Use% Mounted on 
   /dev/sda2  xfs  446G  82G  364G  19% /
```

From /sys/devices/virtual/dmi/id
```
BIOS:  Lenovo -[ISE115D-2.10]- 04/24/2020
Vendor: Lenovo
Product: ThinkSystem SR250 -[7Y51CTOOWW]-
Product Family: ThinkSystem
Serial: 1234567890
```

```
Additional information from dmidecode follows. WARNING: Use caution when you interpret
   this section. The 'dmidecode' program reads system data which is "intended to allow
   hardware to be accurately determined", but the intent may not be met, as there are
   frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
      4x SK Hynix HMAA4GU7A4R8N-VK 32767 MB 2 rank 2666
```

(End of data from sysinfo program)
**SPEC CPU®2017 Floating Point Speed Result**

**Lenovo Global Technology**

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<th>Lenovo Global Technology</th>
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<tr>
<td>SPECspeed®2017_fp_peak = 27.3</td>
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**Compiler Version Notes**

<table>
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<tr>
<th>C</th>
<th>619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)</th>
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<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,</td>
<td></td>
</tr>
<tr>
<td>Version 19.1.1.217 Build 20200306</td>
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<tr>
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<table>
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<tr>
<th>C++, C, Fortran</th>
<th>607.cactuBSSN_s(base, peak)</th>
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<th>603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)</th>
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SPEC CPU®2017 Floating Point Speed Result

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SPECspeed®2017_fp_base = 27.0
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Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp

(Continued on next page)
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---

### Base Optimization Flags (Continued)

**Benchmarks using both Fortran and C (continued):**  
-DSPEC_OPENMP  
-mbranches-within-32B-boundaries  
-nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**Benchmarks using Fortran, C, and C++:**  
-m64  
-std=c11  
-Wl,-z,muldefs  
-xCORE-AVX2  
-ipo  
-O3  
-no-prec-div  
-qopt-prefetch  
-ffinite-math-only  
-qopt-mem-layout-trans=4  
-qopenmp  
-DSPEC_OPENMP  
-mbranches-within-32B-boundaries  
-nostandard-realloc-lhs  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

---

### Peak Compiler Invocation

**C benchmarks:**  
icc

**Fortran benchmarks:**  
ifort

**Benchmarks using both Fortran and C:**  
ifort icc

**Benchmarks using Fortran, C, and C++:**  
icpc icc ifort

---

### Peak Portability Flags

Same as Base Portability Flags

---

### Peak Optimization Flags

**C benchmarks:**  
619.lbm_s: basepeak = yes  
638.imagick_s: basepeak = yes  
644.nab_s: -m64  
-std=c11  
-Wl,-z,muldefs  
-xCORE-AVX2  
-ipo  
-O3  
-no-prec-div  
-qopt-prefetch  
-ffinite-math-only  
-qopt-mem-layout-trans=4  
-qopenmp  
-DSPEC_OPENMP  
-mbranches-within-32B-boundaries

(Continued on next page)
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SPECspeed®2017_fp_base = 27.0
SPECspeed®2017_fp_peak = 27.3

Peak Optimization Flags (Continued)

644.nab_s (continued):
- /usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX2
-03 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -gopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
- /usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX2 -03 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
- /usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-J.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-J.xml
**Lenovo Global Technology**

ThinkSystem SR250  
(3.40 GHz, Intel Xeon E-2224)

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<th>SPECspeed(^{\text{TM}}) 2017 fp_base</th>
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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jul-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

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