## Lenovo Global Technology

**ThinkSystem ST250**  
(3.60 GHz, Intel Xeon E-2246G)

**SPECspeed®2017_fp_base = 32.4**  
**SPECspeed®2017_fp_peak = 32.8**

### Hardware

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_fp_base (32.4)</th>
<th>SPECspeed®2017_fp_peak (32.8)</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>6</td>
<td>55.0</td>
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<tr>
<td>607.cactuBSSN_s</td>
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<td>619.lbm_s</td>
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<td>621.wrf_s</td>
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<td>627.cam4_s</td>
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<td>628.pop2_s</td>
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<td>638.imagick_s</td>
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<td>644.nab_s</td>
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<td>649.fotonik3d_s</td>
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<td>59.6</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>6</td>
<td>16.8</td>
</tr>
</tbody>
</table>

**CPU Name:** Intel Xeon E-2246G  
**Max MHz:** 4800  
**Nominal:** 3600  
**Enabled:** 6 cores, 1 chip  
**Orderable:** 1 chip  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 256 KB I+D on chip per core  
**L3:** 12 MB I+D on chip per chip  
**Other:** None  
**Memory:** 128 GB (4 x 32 GB 2Rx4 PC4-2666V-E)  
**Storage:** 1 x 960 GB SATA SSD  
**Other:** None

### Software

**OS:** SUSE Linux Enterprise Server 12 SP5 (x86_64)  
**Kernel:** 4.12.14-120-default  
**Compiler:**  
- C/C++: Version 19.1.1.217 of Intel  
- Fortran: Version 19.1.1.217 of Intel Fortran  
**Parallel:** Yes  
**Firmware:** Lenovo BIOS Version ISE115D 2.10 released Apr-2020  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 64-bit  
**Other:** jemalloc memory allocator V5.0.1  
**Power Management:** BIOS set to prefer performance at the cost of additional power usage
Lenovo Global Technology
ThinkSystem ST250
(3.60 GHz, Intel Xeon E-2246G)

SPECspeed®2017_fp_base = 32.4
SPECspeed®2017_fp_peak = 32.8

Results Table

<table>
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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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</tbody>
</table>

SPECspeed®2017_fp_base = 32.4
SPECspeed®2017_fp_peak = 32.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/home/cpu2017-1.1.0-ic19.1.1/lib/intel64:/home/cpu2017-1.1.0-ic19.1.1/j
e5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) 

(Continued on next page)
General Notes (Continued)

is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
C1 Enhanced Mode set to Enable
Hyper-Threading set to Disable

Sysinfo program /home/cpu2017-1.1.0-ic19.1.1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed1b1e6e46a485a0011
running on linux-tzna Tue Jun 16 17:33:33 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) E-2246G CPU @ 3.60GHz
  1 "physical id"s (chips)
  6 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 6
  siblings : 6
  physical 0: cores 0 1 2 3 4 5

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  Address sizes: 39 bits physical, 48 bits virtual
  CPU(s): 6
  On-line CPU(s) list: 0-5
  Thread(s) per core: 1
  Core(s) per socket: 6
  Socket(s): 1
  NUMA node(s): 1
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 158
  Model name: Intel(R) Xeon(R) E-2246G CPU @ 3.60GHz
  Stepping: 10

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<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>= 32.4</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>= 32.8</td>
</tr>
</tbody>
</table>

---

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jun-2020
Hardware Availability: Mar-2020
Tested by: Lenovo Global Technology
Software Availability: Apr-2020

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**Platform Notes (Continued)**

- CPU MHz: 3600.000
- CPU max MHz: 4800.0000
- CPU min MHz: 800.0000
- BogoMIPS: 7200.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 256K
- L3 cache: 12288K
- NUMA node0 CPU(s): 0-5
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single pti ssbd ibrs ibp bts stibp tpr_shadow vmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt intel_pt xsaveopt xsavec xgetbv1 xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp md_clear flush_l1d

/proc/cpuinfo cache data
- cache size : 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
- available: 1 nodes (0)
- node 0 cpus: 0 1 2 3 4 5
- node 0 size: 128865 MB
- node 0 free: 128361 MB
- node distances:
- node 0
- 0: 10

From /proc/meminfo
- MemTotal: 131958660 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
- SuSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 5
  - # This file is deprecated and will be removed in a future service pack or release.
  - # Please check /etc/os-release for details about this release.
  - os-release:

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CPU2017 License: 9017
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SPECspeed®2017_fp_base = 32.4
SPECspeed®2017_fp_peak = 32.8

Test Date: Jun-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Platform Notes (Continued)

NAME="SLES"
VERSION="12-SP5"
VERSION_ID="12.5"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP5"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp5"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

itlb_multihit: KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion; VMX: conditional cache flushes, SMT disabled
Microarchitectural Data Sampling: Mitigation: Clear CPU buffers; SMT disabled
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB: conditional, IBRS_FW, RSB filling
tsx_async_abort: Mitigation: Clear CPU buffers; SMT disabled

run-level 3 Jun 16 17:31

SPEC is set to: /home/cpu2017-1.1.0-ic19.1.1
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb3 xfs 893G 64G 829G 8% /

From /sys/devices/virtual/dmi/id
BIOS: Lenovo -[ISE115D-2.10]- 04/24/2020
Vendor: Lenovo
Product: ThinkSystem ST250 -[7Y45CT00WW]-
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
4x SK Hynix HMAA4GU7AJR8N-VK 32767 MB 2 rank 2666

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Lenovo Global Technology
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SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017_fp_base = 32.4
SPECspeed®2017_fp_peak = 32.8

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jun-2020
Tested by: Lenovo Global Technology
Hardware Availability: Mar-2020
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Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak) |
|   |                                                                         |
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)                                          |
|               |                                                                 |
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak) |
|        |                                                                         |
Intel, Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak) |
|           |                                                                         |
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
(Continued on next page)
**SPECCPU®2017 Floating Point Speed Result**

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**Lenovo Global Technology**

ThinkSystem ST250
(3.60 GHz, Intel Xeon E-2246G)

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<td>Software Availability:</td>
<td>Apr-2020</td>
</tr>
</tbody>
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**Compiler Version Notes (Continued)**

**Base Compiler Invocation**

C benchmarks:
```shell
test C benchmarks:
  icc
```

Fortran benchmarks:
```shell
test Fortran benchmarks:
  ifort
```

Benchmarks using both Fortran and C:
```shell
test Fortran and C:
  ifort icc
```

Benchmarks using Fortran, C, and C++:
```shell
test Fortran, C, and C++:
  icpc icc ifort
```

---

**Base Portability Flags**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>-DSPEC_LP64</td>
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<td>607.cactuBSSN_s</td>
<td>-DSPEC_LP64</td>
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<tr>
<td>619.lbm_s</td>
<td>-DSPEC_LP64</td>
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<tr>
<td>621.wrf_s</td>
<td>-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian</td>
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<td>-DSPEC_LP64 -DSPEC_CASE_FLAG</td>
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<td>628.pop2_s</td>
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</tr>
<tr>
<td>654.roms_s</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

---

**Base Optimization Flags**

C benchmarks:
```shell
test C benchmarks:
  -m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
  -mbranches-within-32B-boundaries
```

Fortran benchmarks:
```shell
test Fortran benchmarks:
  -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
  -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
  -nostandard-realloc-lhs -mbranches-within-32B-boundaries
```

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**Lenovo Global Technology**

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</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

---

**Base Optimization Flags (Continued)**

Fortran benchmarks (continued):

```
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

---

**Peak Compiler Invocation**

**C benchmarks:**

```
icc
```

**Fortran benchmarks:**

```
ifort
```

Benchmarks using both Fortran and C:

```
ifort icc
```

Benchmarks using Fortran, C, and C++:

```
icpc icc ifort
```

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

**C benchmarks:**

```
619.lbm_s: basepeak = yes
```
Lenovo Global Technology
ThinkSystem ST250
(3.60 GHz, Intel Xeon E-2246G)

SPECspeed®2017_fp_base = 32.4
SPECspeed®2017_fp_peak = 32.8

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Peak Optimization Flags (Continued)

638.imagick_s: basepeak = yes

644.nab_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX2
-O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-ipo -xCORE-AVX2 -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-J.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-J.xml
# SPEC CPU®2017 Floating Point Speed Result

**Lenovo Global Technology**  
ThinkSystem ST250  
(3.60 GHz, Intel Xeon E-2246G)  

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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