### SPEC CPU®2017 Floating Point Speed Result

New H3C Technologies Co., Ltd.  
H3C UniServer R6700 G3 (Intel Xeon Gold 6130)

- **CPU2017 License**: 9066
- **Test Sponsor**: New H3C Technologies Co., Ltd.
- **Tested by**: New H3C Technologies Co., Ltd.
- **Test Date**: Jun-2020
- **Hardware Availability**: Mar-2019
- **Software Availability**: May-2019

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>167</td>
<td>167</td>
</tr>
</tbody>
</table>

#### Software

- **OS**: Red Hat Enterprise Linux Server release 7.6 (Maipo) 3.10.0-957.el7.x86_64
- **Compiler**: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
- **Parallel**: Yes
- **Firmware**: Version 2.00.39 released Mar-2020 BIOS
- **File System**: xfs
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 64-bit
- **Peak Pointers**: 64-bit
- **Power Management**: BIOS set to prefer performance at the cost of additional power usage

#### Hardware

- **CPU Name**: Intel Xeon Gold 6130
- **Max MHz**: 3700
- **Nominal**: 2100
- **Enabled**: 64 cores, 4 chips
- **Orderable**: 1,2,3,4 chips
- **Cache L1**: 32 KB I + 32 KB D on chip per core
- **L2**: 1 MB I+D on chip per core
- **L3**: 22 MB I+D on chip per chip
- **Other**: None
- **Memory**: 384 GB (24 x 16 GB 2Rx8 PC4-2933V-R, running at 2666)
- **Storage**: 1 x 800 GB SATA SSD
- **Other**: None

#### Benchmark Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
<td>175</td>
<td>175</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
<td>144</td>
<td>143</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>64</td>
<td>125</td>
<td>123</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>64</td>
<td>115</td>
<td>116</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>64</td>
<td>52.3</td>
<td>53.3</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>64</td>
<td>162</td>
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</tr>
<tr>
<td>638.imagick_s</td>
<td>64</td>
<td>109</td>
<td>110</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>64</td>
<td>110</td>
<td>116</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>64</td>
<td>276</td>
<td>276</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>64</td>
<td>216</td>
<td>216</td>
</tr>
</tbody>
</table>

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Legend:

- **threads**: 167
- **SPECspeed®2017_fp_base**: 167
- **SPECspeed®2017_fp_peak**: 167
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Results Table

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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
<td>70.5</td>
<td>837</td>
<td>72.1</td>
<td>818</td>
<td>70.6</td>
<td>836</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
<td>94.9</td>
<td>176</td>
<td>95.5</td>
<td>175</td>
<td>95.7</td>
<td>174</td>
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<tr>
<td>619.lbm_s</td>
<td>64</td>
<td>36.6</td>
<td>143</td>
<td>36.2</td>
<td>145</td>
<td>36.3</td>
<td>144</td>
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<tr>
<td>621.wrf_s</td>
<td>64</td>
<td>106</td>
<td>125</td>
<td>105</td>
<td>126</td>
<td>106</td>
<td>125</td>
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<tr>
<td>627.cam4_s</td>
<td>64</td>
<td>76.7</td>
<td>116</td>
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<td>115</td>
<td>76.8</td>
<td>115</td>
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<tr>
<td>628.pop2_s</td>
<td>64</td>
<td>231</td>
<td>51.4</td>
<td>225</td>
<td>52.8</td>
<td>227</td>
<td>52.3</td>
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<tr>
<td>638.imagick_s</td>
<td>64</td>
<td>89.4</td>
<td>161</td>
<td>88.7</td>
<td>163</td>
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<td>162</td>
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<tr>
<td>644.nab_s</td>
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<tr>
<td>649.fotonik3d_s</td>
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<td>83.3</td>
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<tr>
<td>654.roms_s</td>
<td>64</td>
<td>71.7</td>
<td>220</td>
<td>71.4</td>
<td>221</td>
<td>71.8</td>
<td>219</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
New H3C Technologies Co., Ltd.

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**SPECspeed®2017_fp_base = 167**

**SPECspeed®2017_fp_peak = 167**

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**Platform Notes**

BIOS settings:
- Set Hyper Threading to Disabled
- Set IMC Interleaving to 2-way Interleave

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbl1e6e46a485a0011
running on localhost.localdomain Wed Jun 10 10:02:38 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
  4 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
```

From lscpu:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 1478.356
CPU max MHz: 3700.0000
CPU min MHz: 1000.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
```

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Platform Notes (Continued)

| L2 cache: | 1024K |
| L3 cache: | 22528K |
| NUMA node0 CPU(s): | 0–15 |
| NUMA node1 CPU(s): | 16–31 |
| NUMA node2 CPU(s): | 32–47 |
| NUMA node3 CPU(s): | 48–63 |
| Flags:       | fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsdp lm constant_tsc art arch_perfmon pebs bts rep_good nop1 xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch epb cat_13 cd pflags intel_pt ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 bmi2 avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts hwp hwp_act_window hwp-epp hwp-pkg_req pku ospke spec_ctrl intel_stibp flush_lld |

/proc/cpuinfo cache data
  cache size: 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0–3)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  node 0 size: 96928 MB
  node 0 free: 93753 MB
  node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
  node 1 size: 98304 MB
  node 1 free: 93441 MB
  node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
  node 2 size: 98304 MB
  node 2 free: 94459 MB
  node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
  node 3 size: 98304 MB
  node 3 free: 92885 MB
  node distances:
    node 0 1 2 3
    0: 10 21 21 21
    1: 21 10 21 21
    2: 21 21 10 21
    3: 21 21 21 10

From /proc/meminfo
  MemTotal: 394633424 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

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Platform Notes (Continued)

From /etc/*release* /etc/*version*

os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.6 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VARIANT="Server"
  VARIANT_ID="server"
  VERSION_ID="7.6"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
  Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion; VMX: SMT disabled, L1i conditional cache flushes
Microarchitectural Data Sampling: No status reported
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)
run-level 3 Jun 10 05:04 last=5

SPEC is set to: /home/speccpu
Filesystem Type Size Used Avail Use% Mounted on
  /dev/mapper/rhel-home xfs 690G 131G 559G 19% /home

From /sys/devices/virtual/dmi/id
  BIOS: American Megatrends Inc. 2.00.39 03/24/2020
  Vendor: New H3C Technologies Co., Ltd.
  Product: UniServer R6700 G3
  Serial: 210200A01SH18B000020

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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Platform Notes (Continued)

Memory:
24x Hynix HMA82GR7CJR8N-WM 16 GB 2 rank 2933, configured at 2666
24x NO DIMM NO DIMM

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
| 644.nab_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
| 654.roms_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
| 628.pop2_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416

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Compiler Version Notes (Continued)

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel (R) C Intel (R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

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</tbody>
</table>

### Base Optimization Flags (Continued)

- Fortran benchmarks:
  -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
  -nostandard-realloc-lhs

- Benchmarks using both Fortran and C:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
  -nostandard-realloc-lhs

- Benchmarks using Fortran, C, and C++:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
  -nostandard-realloc-lhs

### Peak Compiler Invocation

- C benchmarks:
  icc -m64 -std=c11

- Fortran benchmarks:
  ifort -m64

- Benchmarks using both Fortran and C:
  ifort -m64 icc -m64 -std=c11

- Benchmarks using Fortran, C, and C++:
  icpc -m64 icc -m64 -std=c11 ifort -m64

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

- C benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

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</table>

Peak Optimization Flags (Continued)

Fortran benchmarks:

603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP  
-DSPEC_OPENMP -02 -xCORE-AVX512 -qopt-prefetch -ipo -O3  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4  
-qopenmp -nostandard-realloc-lhs

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4  
-qopenmp -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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