## Dell Inc.

### PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz)

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jun-2020  
**Hardware Availability:** Feb-2020  
**Software Availability:** Apr-2020

<table>
<thead>
<tr>
<th>Thread</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>101</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>52.5</td>
</tr>
<tr>
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<td>621.wrf_s</td>
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<td>627.cam4_s</td>
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<tr>
<td>628.pop2_s</td>
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<td>638.imagick_s</td>
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<td>644.nab_s</td>
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<td>52.8</td>
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<tr>
<td>649.fotonik3d_s</td>
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<td>125</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>16</td>
<td>73.0</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6208U  
  - **Max MHz:** 3900  
  - **Nominal:** 2900  
  - **Enabled:** 16 cores, 1 chip  
  - **Orderable:** 1 chips  
  - **Cache L1:** 32 KB I + 32 KB D on chip per core  
  - **L2:** 1 MB I+D on chip per core  
  - **L3:** 22 MB I+D on chip per chip  
  - **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2933V-R, running at 2933)  
  - **Storage:** 1 x 1.92 TB SATA SSD  
  - **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux 8.1  
  - **kernel:** 4.18.0-147.el8.x86_64  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 2.7.7 released May-2020  
- **File System:** tmpfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None  

- **jemalloc memory allocator V5.0.1**

### Power Management:

- BIOS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.

PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz)

SPECspeed®2017_fp_base = 80.9

SPECspeed®2017_fp_peak = 81.5

Results Table

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SPECspeed®2017_fp_base = 80.9
SPECspeed®2017_fp_peak = 81.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-ic19.1u1/lib/intel64:/mnt/ramdisk/cpu2017-ic19.1u1/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9–7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
Dell Inc.
PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz)

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CPU2017 License: 55
Test Sponsor: Dell Inc.
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General Notes (Continued)

Benchmark run from a 225 GB ramdisk created with the cmd; "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
Sub NUMA Cluster disabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub set to standard
Logical Processor disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
UPI Prefetch disabled
LLC Prefetch disabled
Dead Line LLC Alloc enabled
Directory AtoS disabled

Sysinfo program /mnt/ramdisk/cpu2017-ic19.1u1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed1e6e46a485a0011
running on rhel-8-1-sut Fri Jun 5 14:12:29 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz
 1  "physical id"'s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lsCPU:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
Platform Notes (Continued)

Byte Order:                  Little Endian
CPU(s):                     16
On-line CPU(s) list:        0-15
Thread(s) per core:         1
Core(s) per socket:         16
Socket(s):                  1
NUMA node(s):               1
Vendor ID:                  GenuineIntel
CPU family:                 6
Model:                      85
Model name:                 Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz
Stepping:                   7
CPU MHz:                    1781.485
CPU max MHz:                3900.0000
CPU min MHz:                1200.0000
BogoMIPS:                   5800.00
Virtualization:             VT-x
L1d cache:                  32K
L1i cache:                  32K
L2 cache:                   1024K
L3 cache:                   22528K
NUMA node0 CPU(s):          0-15
Flags:                      fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                            pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                            lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
                            aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                            xtpr pdcm pcd cda sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
                            avx f16c rdrand lahf_lm abtm ibks dpm tsc_cuckoo clflushopt clflush dts mcm
                            cmov pdcm_pteồ dcfms nonstop_tsc cpuid_fault epb cat_l3 cdp_l3
                            invpcid_single intel_puin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
                            flexpriority ephemeral tsc_adjust bm1 hle avx2 smep bmi2 erms invvpid rtm
                            cmq mpx rdta_avx512f_avx512dq_rsdce adx smap clflushopt_clwb intel_pt_avx512cd
                            avx512bw_avx512vl_xsaveopt_xsavec_xgetbv1_xsaveavx512_vnni md_clear flush_l1d
                            arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 1 nodes (0)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
    node 0 size: 192048 MB
    node 0 free: 175610 MB
    node distances:
        node 0
    0: 10

(Continued on next page)
Dell Inc.

PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz)

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Specspeed®2017_fp_base = 80.9
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Platform Notes (Continued)

From /proc/meminfo
MemTotal: 196657468 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.1 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.1"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

uname -a:
Linux rhel-8-1-sut 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Jun 5 09:43 last=5

SPEC is set to: /mnt/ramdisk/cpu2017-ic19.1u1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 150G 11G 140G 7% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
BIOS: Dell Inc. 2.7.7 05/04/2020
Vendor: Dell Inc.
Product: PowerEdge R640
Product Family: PowerEdge

(Continued on next page)
Dell Inc. PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz) SPECspeed®2017_fp_base = 80.9
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Platform Notes (Continued)

Serial: FPFXCH2

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
6x 002C069D002C 18ASF2G72PDZ-2G9E1 16 GB 2 rank 2933
4x 00AD00B300AD HMA82GR7CJR8N-WM 16 GB 2 rank 2933
2x 00AD063200AD HMA82GR7CJR8N-WM 16 GB 2 rank 2933
12x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

=====================================================================================================
<table>
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<tr>
<th></th>
<th>619.lbm_s(base, peak) 638.imagick_s(base, peak)</th>
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=====================================================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====================================================================================================

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

=====================================================================================================

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====================================================================================================

Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) |
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
Dell Inc. (Intel Xeon Gold 6208U, 2.90 GHz)

**SPEC CPU®2017 Floating Point Speed Result**

**SPECspeed®2017_fp_base = 80.9**

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**Compiler Version Notes (Continued)**

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**Base Compiler Invocation**

- **C benchmarks:**
  - `icc`

- **Fortran benchmarks:**
  - `ifort`

- **Benchmarks using both Fortran and C:**
  - `ifort icc`

- **Benchmarks using Fortran, C, and C++:**
  - `icpc icc ifort`

---

**Base Portability Flags**

- `603.bwaves_s: -DSPEC_LP64`
- `607.cactuBSSN_s: -DSPEC_LP64`
- `619.lbm_s: -DSPEC_LP64`
- `621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
- `627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG`
- `628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
- `-assume byterecl`
- `638.imagick_s: -DSPEC_LP64`
- `644.nab_s: -DSPEC_LP64`
- `649.fotonik3d_s: -DSPEC_LP64`
- `654.roms_s: -DSPEC_LP64`
Dell Inc.  
PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz)  

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### Base Optimization Flags

C benchmarks:
- `-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
- `-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP`
- `-mbranches-within-32B-boundaries`

Fortran benchmarks:
- `-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs`
- `-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib`
- `-ljemalloc`

Benchmarks using both Fortran and C:
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp`
- `-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp`
- `-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

### Peak Compiler Invocation

C benchmarks:
- `icc`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

### Peak Portability Flags

Same as Base Portability Flags
Dell Inc. PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz)

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### Peak Optimization Flags

**C benchmarks:**

619.lbm_s.basepeak = yes

638.imagick_s.basepeak = yes

644.nab_s: -m64 -std=c11 -Wl,-z,muldefs -xcORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**Fortran benchmarks:**

603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
-O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

**Benchmarks using both Fortran and C:**

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

**Benchmarks using Fortran, C, and C++:**

607.cactuBSSN_s: basepeak = yes

---

The flags files that were used to format this result can be browsed at

*http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html*

*http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE10.html*
### SPEC CPU®2017 Floating Point Speed Result

**Dell Inc.**

PowerEdge R640 (Intel Xeon Gold 6208U, 2.90 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak</th>
<th>SPECspeed®2017_fp_base</th>
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</thead>
<tbody>
<tr>
<td>81.5</td>
<td>80.9</td>
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</tbody>
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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jun-2020  
**Hardware Availability:** Feb-2020  
**Software Availability:** Apr-2020

You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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