# SPEC CPU®2017 Floating Point Speed Result

**New H3C Technologies Co., Ltd.**

**H3C UniServer R4900 G3 (Intel Xeon Gold 6154)**

| SPECspeed®2017_fp_base | 128 |
| SPECspeed®2017_fp_peak | 129 |

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Apr-2020  
**Hardware Availability:** Jul-2017  
**Software Availability:** May-2019

## Hardware

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>36</td>
<td>466</td>
<td>466</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>36</td>
<td>148</td>
<td>148</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>36</td>
<td>86.5</td>
<td>86.5</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>36</td>
<td>132</td>
<td>132</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>36</td>
<td>95.9</td>
<td>95.9</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>36</td>
<td>95.5</td>
<td>95.5</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>36</td>
<td>68.0</td>
<td>68.0</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>36</td>
<td>69.1</td>
<td>69.1</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>36</td>
<td>126</td>
<td>126</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>36</td>
<td>239</td>
<td>239</td>
</tr>
</tbody>
</table>

## Software

- **OS:**  
  Red Hat Enterprise Linux Server release 7.6 (Maipo)  
  3.10.0-957.el7.x86_64

- **Compiler:**  
  C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;  
  Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux

- **Parallel:** Yes

- **Firmware:**  
  Version 2.00.38 released Feb-2020 BIOS

- **File System:** xfs

- **System State:** Run level 3 (multi-user)

- **Base Pointers:** 64-bit

- **Peak Pointers:** 64-bit

- **Other:** None

- **Power Management:** BIOS set to prefer performance at the cost of additional power usage

---

New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6154)  
SPECspeed®2017_fp_base = 128  
SPECspeed®2017_fp_peak = 129

**CPU Name:** Intel Xeon Gold 6154  
**Max MHz:** 3700  
**Nominal:** 3000  
**Enabled:** 36 cores, 2 chips  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 24.75 MB I+D on chip per chip  
**Other:** None  
**Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)  
**Storage:** 1 x 6 TB SATA HDD 7200RPM  
**Other:** None

**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Base</td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>603.bwaves_s</td>
<td>36</td>
<td>127</td>
<td>466</td>
<td>127</td>
<td>465</td>
<td>126</td>
<td>468</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>36</td>
<td>113</td>
<td>148</td>
<td>113</td>
<td>147</td>
<td>110</td>
<td>151</td>
</tr>
<tr>
<td>619.llvm_s</td>
<td>36</td>
<td>60.4</td>
<td>86.7</td>
<td>60.8</td>
<td>86.1</td>
<td>60.6</td>
<td>86.8</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>36</td>
<td>99.7</td>
<td>133</td>
<td>100</td>
<td>132</td>
<td>100</td>
<td>132</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>36</td>
<td>92.4</td>
<td>95.9</td>
<td>92.9</td>
<td>95.4</td>
<td>92.4</td>
<td>95.9</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>36</td>
<td>177</td>
<td>66.9</td>
<td>174</td>
<td>68.0</td>
<td>173</td>
<td>68.6</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>36</td>
<td>115</td>
<td>126</td>
<td>115</td>
<td>126</td>
<td>115</td>
<td>126</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>36</td>
<td>76.5</td>
<td>228</td>
<td>76.4</td>
<td>229</td>
<td>76.4</td>
<td>229</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>36</td>
<td>120</td>
<td>75.9</td>
<td>119</td>
<td>76.9</td>
<td>117</td>
<td>77.6</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>36</td>
<td>146</td>
<td>108</td>
<td>146</td>
<td>108</td>
<td>150</td>
<td>105</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 128
SPECspeed®2017_fp_peak = 129

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/home/speccpu/lib/intel64"
- OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesyste page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesyste page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Set Hyper-Threading to Disabled
Set Stale AtoS to Enabled
Set LLC Deadline Alloc to Disabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7eddb1e6e46a485a0011
running on localhost.localdomain Wed Apr 29 11:45:08 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6154 CPU @ 3.00GHz
  2 "physical id"s (chips)
  36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
siblings : 18
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 36
On-line CPU(s) list: 0-35
Thread(s) per core: 1
Core(s) per socket: 18
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6154 CPU @ 3.00GHz
Stepping: 4
CPU MHz: 2044.006
CPU max MHz: 3700.000

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6154)

| SPECspeed®2017_fp_base = 128 |
| SPECspeed®2017_fp_peak = 129 |

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Apr-2020
Hardware Availability: Jul-2017
Software Availability: May-2019

| SPEC CPU®2017 Floating Point Speed Result |

### Platform Notes (Continued)

- **CPU min MHz:** 1200.0000
- **BogoMIPS:** 6000.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 25344K
- **NUMA node0 CPU(s):** 0-17
- **NUMA node1 CPU(s):** 18-35
- **Flags:**
  - fpu
  - vme
  - de
  - pse
  - tsc
  - msr
  - pae
  - mce
  - cx8
  - apic
  - sep
  - mtrr
  - pge
  - mca
  - cmov
  - pat
  - pse36
  - clflush
  - dts
  - acpi
  - mmx
  - fxsr
  - sse
  - sse2
  - ss
  - ht
  - tm
  - pbe
  - syscall
  - nx
  - pdpe1gb
  - rdtscp
  - lm
  - constant_tsc
  - arch_perfmon
  - pebs
  - bts
  - rep_good
  - nopl
  - xtopology
  - nonstop_tsc
  - aperfmperf
  - eagerfpu
  - pni
  - pclmulqdq
  - dtes64
  - monitor
  - ds_cpl
  - tm
  - pbe
  - syscall
  - nx
  - pdpe1gb
  - rdtscp
  - lm
  - constant_tsc
  - arch_perfmon
  - pebs
  - bts
  - rep_good
  - nopl
  - xtopology
  - nonstop_tsc
  - aperfmperf
  - eagerfpu
  - pni
  - pclmulqdq
  - dtes64
  - monitor
  - ds_cpl
  - tm
  - pbe
  - syscall
  - nx
  - pdpe1gb
  - rdtscp
  - lm
  - constant_tsc
  - arch_perfmon
  - pebs
  - bts
  - rep_good
  - nopl
  - xtopology
  - nonstop_tsc
  - aperfmperf
  - eagerfpu
  - pni
  - pclmulqdq
  - dtes64
  - monitor
  - ds_cpl
  - tm
  - pbe
  - syscall
  - nx
  - pdpe1gb
  - rdtscp
  - lm
  - constant_tsc
  - arch_perfmon
  - pebs
  - bts
  - rep_good
  - nopl
  - xtopology
  - nonstop_tsc
  - aperfmperf
  - eagerfpu
  - pni
  - pclmulqdq

From numactl --hardware

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17
node 0 size: 195224 MB
node 0 free: 185569 MB
node 1 cpus: 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35
node 1 size: 196608 MB
node 1 free: 176633 MB

From /proc/meminfo

```
MemTotal: 394623476 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

From /etc/*release* /etc/*version*

```
NAME="Red Hat Enterprise Linux Server"
VERSION="7.6 (Maipo)"
```

(Continued on next page)
Platform Notes (Continued)

ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Mitigation: PTE Inversion; VMX: SMT disabled, L1D conditional cache flushes
Microarchitectural Data Sampling: No status reported
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Apr 29 05:37 last=5

SPEC is set to: /home/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 5.5T 13G 5.4T 1% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. 2.00.38 02/28/2020
Vendor: New H3C Technologies Co., Ltd.
Product: UniServer R4900 G3
Serial: 210235A3QUH188000033

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
12x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666
12x NO DIMM NO DIMM

(End of data from sysinfo program)
**New H3C Technologies Co., Ltd.**

H3C UniServer R4900 G3 (Intel Xeon Gold 6154)  

SPEC CPU®2017 Floating Point Speed Result  

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>128</td>
<td>129</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9066  
**Test Date:** Apr-2020  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Hardware Availability:** Jul-2017  
**Software Availability:** May-2019

### Compiler Version Notes

<table>
<thead>
<tr>
<th>Language</th>
<th>Applications</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)</td>
</tr>
</tbody>
</table>
|          | Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416  
          | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
| C++, C, Fortran | 607.cactuBSSN_s(base, peak) |
|          | Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416  
          | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
|          | Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416  
          | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
|          | Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416  
          | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
| Fortran  | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak) |
|          | Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416  
          | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
| Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak) |
|          | Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416  
          | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
|          | Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416  
          | Copyright (C) 1985-2019 Intel Corporation. All rights reserved. |
New H3C Technologies Co., Ltd. | SPEC CPU®2017 Floating Point Speed Result
H3C UniServer R4900 G3 (Intel Xeon Gold 6154)

**SPECspeed®2017_fp_base = 128**

**SPECspeed®2017_fp_peak = 129**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9066</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>New H3C Technologies Co., Ltd.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Apr-2020</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2019</td>
</tr>
</tbody>
</table>

---

**Base Compiler Invocation**

C benchmarks:
```bash
icc -m64 -std=c11
```

Fortran benchmarks:
```bash
ifort -m64
```

Benchmarks using both Fortran and C:
```bash
ifort -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```bash
icpc -m64 icc -m64 -std=c11 ifort -m64
```

---

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:
```bash
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
```

Fortran benchmarks:
```bash
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:
```bash
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs
```

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6154)

SPECspeed®2017_fp_base = 128
SPECspeed®2017_fp_peak = 129

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Apr-2020
Hardware Availability: Jul-2017
Software Availability: May-2019

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4
-qopenmp -nostandard-realloc-lhs
649.fotonik3d_s: Same as 603.bwaves_s
654.roms_s: -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6154)

| SPECspeed®2017_fp_base = 128 |
| SPECspeed®2017_fp_peak = 129 |

CPU2017 License: 9066  
Test Sponsor: New H3C Technologies Co., Ltd.  
Tested by: New H3C Technologies Co., Ltd.

Peak Optimization Flags (Continued)

654.roms_s (continued):
-qopenmp -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -02 -xCORE-AVX512
-qopt-prefetch -ipo -03 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevE.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-04-29 11:45:07-0400.