**SPEC CPU®2017 Integer Rate Result**

**Fujitsu**

PRIMERGY RX2520 M5, Intel Xeon Gold 5218R, 2.10 GHz

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>19</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Fujitsu</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Fujitsu</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Mar-2020</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>May-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2019</td>
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**SPECrate®2017_int_base = 198**

**SPECrate®2017_int_peak = 206**

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<tr>
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<td>505.mcf_r</td>
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<td>520.omnetpp_r</td>
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<td>523.xalancbmk_r</td>
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<td>525.x264_r</td>
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</tr>
<tr>
<td>557.xz_r</td>
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<td>133</td>
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</tbody>
</table>

**Hardware**

- CPU Name: Intel Xeon Gold 5218R
- Max MHz: 4000
- Nominal: 2100
- Enabled: 40 cores, 2 chips, 2 threads/core
- Orderable: 1.2 chips
- Cache L1: 32 KB I + 32 KB D on chip per core
- Cache L2: 1 MB I+D on chip per core
- Cache L3: 27.5 MB I+D on chip per chip
- Other: None
- Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2666)
- Storage: 1 x SATA M.2 SSD, 480 GB
- Other: None

**Software**

- OS: SUSE Linux Enterprise Server 15
- Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
  Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
- Parallel: No
- Firmware: Fujitsu BIOS Version V5.0.0.14 R1.18.0 for D3386-B1x released Apr-2020
- File System: ext4
- System State: Run level 3 (multi-user)
- Base Pointers: 64-bit
- Peak Pointers: 32/64-bit
- Other: jemalloc: jemalloc memory allocator V5.0.1
- Power Management: BIOS set to prefer performance at the cost of additional power usage
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CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

SPECrate®2017_int_base = 198
SPECrate®2017_int_peak = 206

Results Table

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<td>645</td>
<td>134</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with: nohz_full=1-79

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/home/Benchmark/cpu2017-1.1.0/lib/intel64:/home/Benchmark/cpu2017-1.1.0/lib/ia32:/home/Benchmark/cpu2017-1.1.0/je5.0.1-32"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Fujitsu
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SPECrate®2017_int_base = 198
SPECrate®2017_int_peak = 206

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Mar-2020
Hardware Availability: May-2019
Software Availability: May-2019

General Notes (Continued)
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5
jemalloc: sources available via jemalloc.net
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
DCU Streamer Prefetcher = Disabled
DCU Ip Prefetcher = Disabled
Stale AtoS = Enabled
Patrol Scrub = Disabled
WR CRC feature Control = Disabled
Fan Control = Full

Sysinfo program /home/Benchmark/cpu2017-1.1.0/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7eddb1e6e46a485a0011
running on sles15 Thu Mar 12 19:57:57 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
  2 "physical id"s (chips)
  80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 40
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80

(Continued on next page)
Platform Notes (Continued)

On-line CPU(s) list: 0–79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5218R CPU @ 2.10GHz
Stepping: 7
CPU MHz: 2100.000
CPU max MHz: 4000.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0–2,5,6–10–12,15,16,40–42,45,46,50–52,55,56
NUMA node1 CPU(s): 3,4,7–9,13,14,17–19,43,44,47–49,53,54,57–59
NUMA node2 CPU(s): 20–22,25,26,30–32,35,36,60–62,65,66,70–72,75,76
NUMA node3 CPU(s): 23,24,27–29,33,34,37–39,63,64,67–69,73,74,77–79
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant-tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop-tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrr pdcm pcd cda sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abtm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_pinn ssbd mba ibrs ibpb stibp ibrs Enhanced tpr_shadow vnmli
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3ms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsaves xsavec qsm_mbb lsc qmm_unit lsc qmm_total
qmm_mbb_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku
ospke avx512_vnni flush_l1d arch_capabilities

/proc/cpuinfo cache data
 cache size : 28160 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
 available: 4 nodes (0–3)
 node 0 cpus: 0 1 2 3 6 10 11 12 15 16 40 41 42 45 46 50 51 52 55 56
 node 0 size: 95249 MB
 node 0 free: 91667 MB
 node 1 cpus: 3 4 7 8 9 13 14 17 18 19 43 44 47 48 49 53 54 57 58 59
 node 1 size: 96764 MB

(Continued on next page)
Platform Notes (Continued)

node 1 free: 96476 MB
node 2 cpus: 20 21 22 25 26 30 31 32 35 60 61 62 65 66 70 71 72 75 76
node 2 size: 96764 MB
node 2 free: 96503 MB
node 3 cpus: 23 24 27 28 33 34 37 38 39 63 64 67 68 69 73 74 77 78 79
node 3 size: 96762 MB
node 3 free: 96493 MB
node distances:
  node 0  1  2  3
  0:  10  11  21  21
  1:  11  10  21  21
  2:  21  21  10  11
  3:  21  21  11  10

From /proc/meminfo
  MemTotal:       394793548 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15"
    VERSION_ID="15"
    PRETTY_NAME="SUSE Linux Enterprise Server 15"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:15"

  uname -a:
    Linux sles15 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019 (dd6077c)
    x86_64 x86_64 x86_64 GNU/Linux

  Kernel self-reported vulnerability status:

   CVE-2018-3620 (L1 Terminal Fault): Not affected
   Microarchitectural Data Sampling: No status reported
   CVE-2017-5754 (Meltdown): Not affected
   CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
   CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
   CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Mar 12 19:57
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Tested by: Fujitsu  Software Availability: May-2019

Platform Notes (Continued)

SPEC is set to: /home/Benchmark/cpu2017-1.1.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 188G 33G 146G 19% /

From /sys/devices/virtual/dmi/id
BIOS: FUJITSU // American Megatrends Inc. V5.0.0.14 R1.18.0 for D3386-B1x
02/10/2020
Vendor: FUJITSU
Product: PRIMERGY RX2520 M5
Product Family: SERVER
Serial: YMNRXXXX17

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
12x Micron 36ASF4G72PZ-2G9E2 32 GB 2 rank 2933, configured at 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
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C       | 502.gcc_r(peak)
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(Continued on next page)
Compiler Version Notes (Continued)

------------------------------------------------------------------------------
| C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) |
|         | 525.x264_r(base, peak) 557.xz_r(base, peak) |
------------------------------------------------------------------------------

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C++     | 523.xalancbmk_r(peak) |
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) |
|         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C++     | 523.xalancbmk_r(peak) |
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) |
|         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| Fortran | 548.exchange2_r(base, peak) |
------------------------------------------------------------------------------

(Continued on next page)
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Copyright 2017-2020 Standard Performance Evaluation Corporation

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Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4

(Continued on next page)
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Base Optimization Flags (Continued)

C++ benchmarks (continued):
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11


C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -fno-strict-overflow -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: basepeak = yes


C++ benchmarks:


523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: basepeak = yes

541.leelu_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes
Fujitsu
PRIMERGY RX2520 M5, Intel Xeon Gold 5218R, 2.10 GHz

SPECrater®2017_int_base = 198
SPECrater®2017_int_peak = 206

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Test Sponsor: Fujitsu
Tested by: Fujitsu

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Software Availability: May-2019

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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