## SPEC CPU®2017 Integer Speed Result

**Hewlett Packard Enterprise**
(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.90 GHz, Intel Xeon Gold 6226R)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 9.64</th>
<th>SPECspeed®2017_int_peak = 9.75</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: HPE</td>
<td>Software Availability: Jun-2019</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Hardware Availability: Feb-2020</td>
</tr>
<tr>
<td>CPU2017 License: 3</td>
<td>Test Date: Feb-2020</td>
</tr>
</tbody>
</table>

### Hardware

**CPU Name:** Intel Xeon Gold 6226R  
**Max MHz:** 3900  
**Nominal:** 2900  
**Enabled:** 32 cores, 2 chips  
**Orderable:** 1, 2 chip(s)  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 22 MB I+D on chip per chip  
**Other:** None  
**Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
**Storage:** 1 x 400 GB SAS SSD  
**Other:** None  

### Software

**OS:** SUSE Linux Enterprise Server 15 SP1 (x86_64)  
**Kernel:** 4.12.14-195-default  
**Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux  
**Parallel:** Yes  
**Firmware:** HPE BIOS Version U30 2.22 (11/13/2019) released Feb-2020  
**File System:** btrfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 64-bit  
**Other:** jemalloc memory allocator V5.0.1  
**Power Management:** BIOS set to prefer performance at the cost of additional power usage

### Software Performance

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base (9.64)</th>
<th>SPECspeed®2017_int_peak (9.75)</th>
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</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>32</td>
<td>6.43</td>
<td>7.51</td>
</tr>
<tr>
<td>gcc</td>
<td>32</td>
<td>8.83</td>
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<td>mcf</td>
<td>32</td>
<td>11.8</td>
<td>12.7</td>
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<tr>
<td>omnetpp</td>
<td>32</td>
<td>7.50</td>
<td>8.31</td>
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<tr>
<td>xalancbmk</td>
<td>32</td>
<td>11.9</td>
<td>12.7</td>
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<tr>
<td>x264</td>
<td>32</td>
<td>5.32</td>
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<tr>
<td>deepsjeng</td>
<td>32</td>
<td>4.57</td>
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<tr>
<td>leela</td>
<td>32</td>
<td>4.57</td>
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<tr>
<td>exchange2</td>
<td>32</td>
<td>15.5</td>
<td>16.3</td>
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<tr>
<td>xz</td>
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<td>21.5</td>
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## Hewlett Packard Enterprise

**(Test Sponsor: HPE)**

ProLiant DL380 Gen10  
*(2.90 GHz, Intel Xeon Gold 6226R)*

---

**SPECspeed®2017_int_base = 9.64**

**SPECspeed®2017_int_peak = 9.75**

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**CPU2017 License:** 3  
**Test Date:** Feb-2020  
**Test Sponsor:** HPE  
**Hardware Availability:** Feb-2020  
**Tested by:** HPE  
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## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
<td><strong>Base</strong></td>
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<td>6.43</td>
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<td>223</td>
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<td>124</td>
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<td>14.2</td>
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<table>
<thead>
<tr>
<th><strong>Peak</strong></th>
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<td>222</td>
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**SPECspeed®2017_int_base = 9.64**  
**SPECspeed®2017_int_peak = 9.75**

---

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesyte page cache synced and cleared with:  
```
sync; echo 3 > /proc/sys/vm/drop_caches
```

---

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

---

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
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SPECspeed®2017_int_base = 9.64
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General Notes (Continued)
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to General Peak Frequency Compute
Workload Profile set to Custom
Energy/Performance Bias set to Balanced Power
Minimum Processor Idle Power Core C-State set to C1E State
Numa Group Size Optimization set to Flat
XPT Prefetcher set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbble6e46a485a0011
running on linux-3rlx Thu Feb 27 05:17:33 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 32
On-line CPU(s) list: 0-31

(Continued on next page)
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SPECspeed®2017_int_base = 9.64
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CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz
Stepping: 7
CPU MHz: 2900.000
BogoMIPS: 5800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_pinn ssbd mba ibrs ibpb ibrsopl ibrd ibrs_enhanced tpr_shadow vmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdtd_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaeopt xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbb_total
cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

/proc/cpuinfo cache data
 cache size : 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
 available: 2 nodes (0-1)
 node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
 node 0 size: 193127 MB
 node 0 free: 190715 MB
 node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
 node 1 size: 193502 MB
 node 1 free: 193157 MB
 node distances:
 node 0 1
 0: 10 21
 1: 21 10

(Continued on next page)
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Test Sponsor: HPE
Tested by: HPE

| Test Date: Feb-2020 |
| Hardware Availability: Feb-2020 |
| Software Availability: Jun-2019 |

Platform Notes (Continued)

From /proc/meminfo
   MemTotal: 395909740 kB
   HugePages_Total: 0
   Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15-SP1"
      VERSION_ID="15.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
   Linux linux-3rlx 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
run-level 3 Feb 27 05:15

SPEC is set to: /home/cpu2017
   Filesystem Type Size Used Avail Use% Mounted on
   /dev/sda2 btrfs 369G 155G 214G 42% /home

From /sys/devices/virtual/dmi/id
   BIOS: HPE U30 11/13/2019
   Vendor: HPE
   Product: ProLiant DL380 Gen10
   Product Family: ProLiant
   Serial: 2M294204YV

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow

(Continued on next page)
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Platform Notes (Continued)

hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
12x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 648.exchange2_s(base, peak)
==============================================================================
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

(Continued on next page)
### Base Compiler Invocation (Continued)

Fortran benchmarks:

```fortran
ifort -m64
```

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
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</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>-DSPEC_LP64, -DSPEC_LINUX_X64</td>
</tr>
<tr>
<td>gcc</td>
<td>-DSPEC_LP64</td>
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<td>mcf</td>
<td>-DSPEC_LP64</td>
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<td>-DSPEC_LP64</td>
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<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>xz</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

### Base Optimization Flags

**C benchmarks**:

```
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

**C++ benchmarks**:

```
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
```

**Fortran benchmarks**:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs
```

### Peak Compiler Invocation

**C benchmarks**:

```
icc -m64 -std=c11
```

**C++ benchmarks**:

```
icpc -m64
```
Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4

(Continued on next page)
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(Test Sponsor: HPE)
ProLiant DL380 Gen10
(2.90 GHz, Intel Xeon Gold 6226R)

SPECspeed\textsuperscript{\textregistered}2017\textsubscript{int}\_peak = 9.75
SPECspeed\textsuperscript{\textregistered}2017\textsubscript{int}\_base = 9.64

CPU\textsubscript{2017} License: 3
Test Sponsor: HPE
Tested by: HPE

Peak Optimization Flags (Continued)

620.omnetpp\_s (continued):
-DSPEC\_SUPPRESS\_OPENMP
-L/usr/local/IntelCompiler19/compilers\_and\_libraries\_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk\_s: -Wl,-z,muldefs -xCORE\textbar AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers\_and\_libraries\_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng\_s: Same as 623.xalancbmk\_s
641.leela\_s: Same as 623.xalancbmk\_s

Fortran benchmarks:
-xCORE\textbar AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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