### SPEC CPU®2017 Integer Speed Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55R/55RL  
(2.60 GHz, Intel Xeon Gold 6240)

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Feb-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Sep-2019</td>
</tr>
<tr>
<td>Tested by: Netweb</td>
<td>Software Availability: Aug-2019</td>
</tr>
</tbody>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Gold 6240  
- **Max MHz:** 3900  
- **Nominal:** 2600  
- **Enabled:** 36 cores, 2 chips, 2 threads/core  
- **Orderable:** 1, 2 (chip)  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 24.75 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 480 GB SSD  
- **Other:** None

#### Software

- **OS:** CentOS Linux release 7.7.1908 (Core)  
- **Compiler:** C/C++: Version 19.0.4.243 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.243 of Intel Fortran Compiler Build 20190416 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version V8.101 released Aug-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** Default

---

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<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base (10.1)</th>
<th>SPECspeed®2017_int_peak (10.3)</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s 72</td>
<td>6.8</td>
<td>7.95</td>
</tr>
<tr>
<td>602.gcc_s 72</td>
<td>9.88</td>
<td>10.0</td>
</tr>
<tr>
<td>605.mcf_s 72</td>
<td>12.4</td>
<td>12.6</td>
</tr>
<tr>
<td>620.omnetpp_s 72</td>
<td>7.69</td>
<td>7.83</td>
</tr>
<tr>
<td>623.xalanchmk_s 72</td>
<td>12.4</td>
<td>12.3</td>
</tr>
<tr>
<td>625.x264_s 72</td>
<td>14.0</td>
<td>14.0</td>
</tr>
<tr>
<td>631.deepsjeng_s 72</td>
<td>5.45</td>
<td>5.45</td>
</tr>
<tr>
<td>641.leela_s 72</td>
<td>4.75</td>
<td>4.74</td>
</tr>
<tr>
<td>648.exchange2_s 72</td>
<td>16.2</td>
<td>16.2</td>
</tr>
<tr>
<td>657.xz_s 72</td>
<td>23.3</td>
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</tr>
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**Threads**

- 1 to 24

**SPECspeed®2017_int_base = 10.1**

**SPECspeed®2017_int_peak = 10.3**
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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</tr>
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<tr>
<td>600.perlbench_s</td>
<td>72</td>
<td>261</td>
<td>6.80</td>
<td>258</td>
<td>6.89</td>
<td>261</td>
<td>6.81</td>
<td>224</td>
<td>7.94</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>72</td>
<td>405</td>
<td>9.84</td>
<td>401</td>
<td>9.94</td>
<td>403</td>
<td>9.88</td>
<td>397</td>
<td>10.0</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>72</td>
<td>380</td>
<td>12.4</td>
<td>377</td>
<td>12.5</td>
<td>380</td>
<td>12.4</td>
<td>376</td>
<td>12.5</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>72</td>
<td>214</td>
<td>7.64</td>
<td>212</td>
<td>7.69</td>
<td>210</td>
<td>7.76</td>
<td>208</td>
<td>7.83</td>
</tr>
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<td>623.xalancmk_s</td>
<td>72</td>
<td>115</td>
<td>12.4</td>
<td>115</td>
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<td>263</td>
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<td>14.0</td>
<td>126</td>
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<td>263</td>
<td>5.45</td>
<td>263</td>
<td>5.45</td>
<td>264</td>
<td>5.44</td>
<td>360</td>
<td>4.74</td>
</tr>
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<td>641.leela_s</td>
<td>72</td>
<td>359</td>
<td>4.75</td>
<td>359</td>
<td>4.75</td>
<td>359</td>
<td>4.75</td>
<td>360</td>
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</tr>
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<td>183</td>
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<td>16.2</td>
<td>182</td>
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<td>265</td>
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<td>265</td>
<td>23.3</td>
<td>260</td>
<td>23.7</td>
</tr>
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<td>SPECspeed®2017_int_base = 10.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has learned that this result, which used an evaluation compiler, was submitted contrary to the compiler license terms. Intel has granted a one-time waiver for this result.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

(Continued on next page)
SPECCPU®2017 Integer Speed Result

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SPECspeed®2017_int_base = 10.1
SPECspeed®2017_int_peak = 10.3

General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ebd1e6e46a485a0011
running on NODE2 Sat Feb 15 07:42:42 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name: Intel(R) Xeon(R) Gold 6240 CPU @ 2.60GHz
  2 "physical id"s (chips)
  72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 18
siblings: 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 72
On-line CPU(s) list: 0-71
Thread(s) per core: 2
Core(s) per socket: 18
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6240 CPU @ 2.60GHz

(Continued on next page)
Platform Notes (Continued)

Stepping:              7
CPU MHz:               999.914
CPU max MHz:           3900.0000
CPU min MHz:           1000.0000
BogoMIPS:              5200.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              25344K
NUMA node0 CPU(s):     0-17,36-53
NUMA node1 CPU(s):     18-35,54-71
Flags:                 fpu vme de pse ts mcef pmr pge mca cmov
                       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                       rdtsdp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                       nonstop_tsc aperffmonf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl
                       sse dt tm pbe syscall nx pdpe1gb rdtscl pwp bmi mce cx8 apic sep mtrr
                       pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe
                       syscall nx pdpe1gb rdtsdp lm constant_tsc art arch_perfmon pebs bts rep_good
                       nopl xtopology nonstop_tsc aperffmonf eagerfpu pni pclmulqdq dtes64 monitor
dx tsc_deadline_timer aes xsave avx fl64 rdrand lahf_lm abm 3dnowprefetch epb cat_13
                       cdp_13 intel_pni intel_pt ssbd mba ibrs ibpb stibp ibsib Enhanced tpr_shadow
                       vnmi flexpriority ept vpid fsgsb m tsc_adjust bmi1 hle avx2 smep bmi2 erms
                       invpcid rtm cqm mp xdp kdt_a avx512f avx512dq rdseed adx smap clflushopt
                       clwb avx512cd avx512bw avx512vl xsaveopt xsaveopt xsaveopt xgetbv1
                       cqm_llc cqm_occupllc cqm_mbm_total cqm_mbm_local dtherm ida arat pln
                       pts hwp hwp_act_window hwp epp hwp_pkg_req pku ospke avx512_vnni md_clear
                       spec_ctrl intel_stibp flush_lld arch_capabilities

!/proc/cpuinfo cache data
  cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 36 37 38 39 40 41 42 43
  44 45 46 47 48 49 50 51 52 53
  node 0 size: 195228 MB
  node 0 free: 165834 MB
  node 1 cpus: 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37
  38 39 40 41 42 43 44 45
  46 61 62 63 64 65 66 67 68 69 70 71
  node 1 size: 196608 MB
  node 1 free: 169112 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 394858864 KB
  HugePages_Total: 0

(Continued on next page)
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SPECspeed®2017_int_base = 10.1
SPECspeed®2017_int_peak = 10.3

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Platform Notes (Continued)

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
centos-release: CentOS Linux release 7.7.1908 (Core)
centos-release-upstream: Derived from Red Hat Enterprise Linux 7.7 (Source)
os-release:
  NAME="CentOS Linux"
  VERSION="7 (Core)"
  ID="centos"
  ID_LIKE="rhel fedora"
  VERSION_ID="7"
  PRETTY_NAME="CentOS Linux 7 (Core)"
  ANSI_COLOR="0;31"
  CPE_NAME=cpe:/o:centos:centos:7
redhat-release: CentOS Linux release 7.7.1908 (Core)
system-release: CentOS Linux release 7.7.1908 (Core)
system-release-cpe: cpe:/o:centos:centos:7

uname -a:
  Linux NODE2 3.10.0-1062.el7.x86_64 #1 SMP Wed Aug 7 18:08:02 UTC 2019 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

run-level 3 Feb 13 07:54

SPEC is set to: /home/cpu2017
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/mapper/centos-home xfs 392G 148G 245G 38% /home

From /sys/devices/virtual/dmi/id
  BIOS: American Megatrends Inc. V8.101 08/02/2019
  Vendor: Tyrone Systems
  Product: DIT400TR-55R
  Serial: empty

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow
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### Platform Notes (Continued)

Hardware to be accurately determined®, but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

<table>
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<tr>
<th>Compiler</th>
<th>Benchmarks</th>
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<td>C</td>
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**Intel(R) C** Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.243 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

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**Intel(R) C++** Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.243 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
icpc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

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<td>648.exchange2_s(base, peak)</td>
</tr>
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**Intel(R) Fortran** Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.243 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
ifort: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

### Base Compiler Invocation

**C benchmarks:**

```bash
icc -m64 -std=c11
```

**C++ benchmarks:**

```bash
icpc -m64
```

(Continued on next page)
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- Test Date: Feb-2020
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- Software Availability: Aug-2019

**Base Compiler Invocation (Continued)**

Fortran benchmarks:
ifort -m64

**Base Portability Flags**

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<tr>
<th>Benchmark</th>
<th>Flags</th>
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</tr>
</tbody>
</table>

**Base Optimization Flags**

C benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-ipo -O3 -no-prec-div -qopt-mem-layout-transform=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-ipo -O3 -no-prec-div -qopt-mem-layout-transform=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-transform=4
-nostandard-realloc-lhs

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11
Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55R/55RL  
(2.60 GHz, Intel Xeon Gold 6240)

**SPECspeed®2017_int_base = 10.1**  
**SPECspeed®2017_int_peak = 10.3**

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Feb-2020</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Sep-2019</td>
</tr>
<tr>
<td>Tested by: Netweb</td>
<td>Software Availability: Aug-2019</td>
</tr>
</tbody>
</table>

**Peak Optimization Flags (Continued)**

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-DSPEC_SUPPRESS_OPENMP  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64  
-lqkmalloc

623.xalancbmk_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64  
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at  
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.html

You can also download the XML flags sources by saving the following links:  
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-02-15 07:42:42-0500.  
Originally published on 2020-03-17.