



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Supermicro

A+ Server 2023US-TR4  
(H11DSU-iN , AMD EPYC 7452)

**SPECSpeed®2017\_fp\_base = 158**

**SPECSpeed®2017\_fp\_peak = 163**

CPU2017 License: 001176

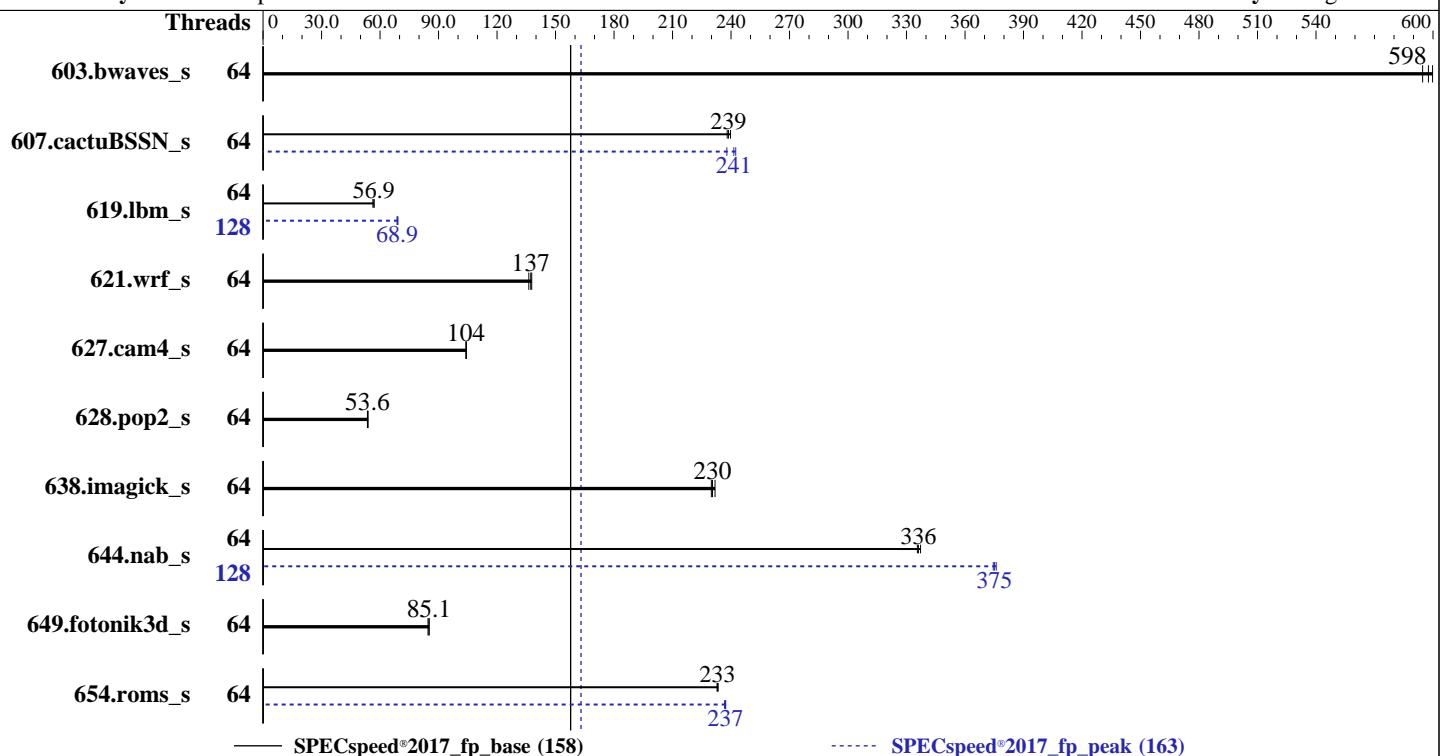
Test Date: Jan-2020

Test Sponsor: Supermicro

Hardware Availability: Aug-2019

Tested by: Supermicro

Software Availability: Aug-2019



Hardware		Software	
CPU Name:	AMD EPYC 7452	OS:	Ubuntu 19.04
Max MHz:	3350	Compiler:	Kernel 5.0.0-25-generic
Nominal:	2350	Parallel:	C/C++/Fortran: Version 2.0.0 of AOCC
Enabled:	64 cores, 2 chips, 2 threads/core	Firmware:	Yes
Orderable:	1,2 chips	File System:	Version 2.0b released Nov-2019
Cache L1:	32 KB I + 32 KB D on chip per core	System State:	ext4
L2:	512 KB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	128 MB I+D on chip per chip, 16 MB shared / 4 cores	Peak Pointers:	64-bit
Other:	None	Other:	64-bit
Memory:	1 TB (16 x 64 GB 4DRx4 PC4-3200AA-L)	Power Management:	jemalloc: jemalloc memory allocator library v5.1.0
Storage:	1 x 200 GB SATA III SSD		BIOS set to prefer performance at the cost of additional power usage.
Other:	None		



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	98.4	600	<b>98.7</b>	<b>598</b>	99.2	595	64	98.4	600	<b>98.7</b>	<b>598</b>	99.2	595
607.cactuBSSN_s	64	70.0	238	69.5	240	<b>69.8</b>	<b>239</b>	64	<b>69.0</b>	<b>241</b>	68.8	242	70.1	238
619.lbm_s	64	<b>92.1</b>	<b>56.9</b>	92.0	56.9	93.0	56.3	128	76.2	68.7	<b>76.1</b>	<b>68.9</b>	75.7	69.2
621.wrf_s	64	<b>96.3</b>	<b>137</b>	96.0	138	97.0	136	64	<b>96.3</b>	<b>137</b>	96.0	138	97.0	136
627.cam4_s	64	<b>85.1</b>	<b>104</b>	85.2	104	85.0	104	64	<b>85.1</b>	<b>104</b>	85.2	104	85.0	104
628.pop2_s	64	220	53.9	<b>222</b>	<b>53.6</b>	222	53.5	64	220	53.9	<b>222</b>	<b>53.6</b>	222	53.5
638.imagick_s	64	62.2	232	62.7	230	<b>62.6</b>	<b>230</b>	64	62.2	232	62.7	230	<b>62.6</b>	<b>230</b>
644.nab_s	64	51.8	337	<b>52.0</b>	<b>336</b>	52.1	336	128	46.5	376	<b>46.6</b>	<b>375</b>	46.7	374
649.fotonik3d_s	64	107	85.2	108	84.6	<b>107</b>	<b>85.1</b>	64	107	85.2	108	84.6	<b>107</b>	<b>85.1</b>
654.roms_s	64	67.5	233	<b>67.6</b>	<b>233</b>	67.6	233	64	<b>66.4</b>	<b>237</b>	66.4	237	66.5	237

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory sync then drop\_caches=3 to reset caches before invoking runcpu

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-127"
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_speed_aocc200_rome_C_lib/64;/home/cpu2017/amd_speed_a
    occ200_rome_C_lib/32:"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "128"
```

Environment variables set by runcpu during the 607.cactubSSN\_s peak run:

```
GOMP_CPU_AFFINITY = "0-63"
```

Environment variables set by runcpu during the 619.lbm\_s peak run:

```
GOMP_CPU_AFFINITY = "0 64 1 65 2 66 3 67 4 68 5 69 6 70 7 71 8 72 9 73 10 74
    11 75 12 76 13 77 14 78 15 79 16 80 17 81 18 82 19 83 20 84 21 85 22 86
    23 87 24 88 25 89 26 90 27 91 28 92 29 93 30 94 31 95 32 96 33 97 34 98
    35 99 36 100 37 101 38 102 39 103 40 104 41 105 42 106 43 107 44 108 45
    109 46 110 47 111 48 112 49 113 50 114 51 115 52 116 53 117 54 118 55
    119 56 120 57 121 58 122 59 123 60 124 61 125 62 126 63 127"
```

Environment variables set by runcpu during the 644.nab\_s peak run:

```
GOMP_CPU_AFFINITY = "0 64 1 65 2 66 3 67 4 68 5 69 6 70 7 71 8 72 9 73 10 74
    11 75 12 76 13 77 14 78 15 79 16 80 17 81 18 82 19 83 20 84 21 85 22 86
    23 87 24 88 25 89 26 90 27 91 28 92 29 93 30 94 31 95 32 96 33 97 34 98
    35 99 36 100 37 101 38 102 39 103 40 104 41 105 42 106 43 107 44 108 45
    109 46 110 47 111 48 112 49 113 50 114 51 115 52 116 53 117 54 118 55
    119 56 120 57 121 58 122 59 123 60 124 61 125 62 126 63 127"
```

Environment variables set by runcpu during the 654.roms\_s peak run:

```
GOMP_CPU_AFFINITY = "0-63"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto

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## General Notes (Continued)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

## Platform Notes

BIOS Settings:

Determinism Control = Manual

Determinism Slider = Power

cTDP Control = Manual

cTDP = 180

Package Power Limit Control = Manual

Package Power Limit = 180

IOMMU = Enabled

APBDIS = 1

NUMA Nodes Per Socket = NPS4

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011  
running on h11dsu-01 Thu Jan 9 18:28:41 2020

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : AMD EPYC 7452 32-Core Processor
  2 "physical id"s (chips)
  128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 32
  siblings : 64
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
Address sizes:         43 bits physical, 48 bits virtual
CPU(s):                128
On-line CPU(s) list:  0-127
Thread(s) per core:   2
```

(Continued on next page)



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## Platform Notes (Continued)

Core(s) per socket: 32  
Socket(s): 2  
NUMA node(s): 8  
Vendor ID: AuthenticAMD  
CPU family: 23  
Model: 49  
Model name: AMD EPYC 7452 32-Core Processor  
Stepping: 0  
CPU MHz: 2011.978  
CPU max MHz: 2350.0000  
CPU min MHz: 1500.0000  
BogoMIPS: 4700.04  
Virtualization: AMD-V  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 512K  
L3 cache: 16384K  
NUMA node0 CPU(s): 0-7,64-71  
NUMA node1 CPU(s): 8-15,72-79  
NUMA node2 CPU(s): 16-23,80-87  
NUMA node3 CPU(s): 24-31,88-95  
NUMA node4 CPU(s): 32-39,96-103  
NUMA node5 CPU(s): 40-47,104-111  
NUMA node6 CPU(s): 48-55,112-119  
NUMA node7 CPU(s): 56-63,120-127  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpe1gb rdtscp lm constant\_tsc rep\_good nopl xtTopology nonstop\_tsc cpuid extd\_apicid aperfmpf perf\_pni pclmulqdq monitor ssse3 fma cx16 sse4\_1 sse4\_2 movbe popcnt aes xsave avx f16c rdrand lahf\_lm cmp\_legacy svm extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpext perfctr\_llc mwaitx cpb cat\_13 cdp\_13 hw\_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall fsgsbase bmil avx2 smep bmi2 cqmq rdt\_a rdseed adx smap clflushopt clwb sha\_ni xsaveopt xsavec xgetbv1 xsaves cqmq\_llc cqmq\_occup\_llc cqmq\_mbm\_total cqmq\_mbm\_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm\_lock nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter pfthreshold avic v\_vmsave\_vmload vgif umip rdpid overflow\_recov succor smca

/proc/cpuinfo cache data  
cache size : 512 KB

From numactl --hardware    WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)  
node 0 cpus: 0 1 2 3 4 5 6 7 64 65 66 67 68 69 70 71  
node 0 size: 128891 MB  
node 0 free: 128422 MB

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## Platform Notes (Continued)

```
node 1 cpus: 8 9 10 11 12 13 14 15 72 73 74 75 76 77 78 79
node 1 size: 129016 MB
node 1 free: 128645 MB
node 2 cpus: 16 17 18 19 20 21 22 23 80 81 82 83 84 85 86 87
node 2 size: 129016 MB
node 2 free: 128597 MB
node 3 cpus: 24 25 26 27 28 29 30 31 88 89 90 91 92 93 94 95
node 3 size: 129004 MB
node 3 free: 128260 MB
node 4 cpus: 32 33 34 35 36 37 38 39 96 97 98 99 100 101 102 103
node 4 size: 129016 MB
node 4 free: 128577 MB
node 5 cpus: 40 41 42 43 44 45 46 47 104 105 106 107 108 109 110 111
node 5 size: 128993 MB
node 5 free: 128620 MB
node 6 cpus: 48 49 50 51 52 53 54 55 112 113 114 115 116 117 118 119
node 6 size: 129016 MB
node 6 free: 128647 MB
node 7 cpus: 56 57 58 59 60 61 62 63 120 121 122 123 124 125 126 127
node 7 size: 129015 MB
node 7 free: 128640 MB
node distances:
node   0   1   2   3   4   5   6   7
  0: 10 12 12 12 32 32 32 32
  1: 12 10 12 12 32 32 32 32
  2: 12 12 10 12 32 32 32 32
  3: 12 12 12 10 32 32 32 32
  4: 32 32 32 32 10 12 12 12
  5: 32 32 32 32 12 10 12 12
  6: 32 32 32 32 12 12 10 12
  7: 32 32 32 32 12 12 12 10
```

From /proc/meminfo

```
MemTotal:      1056739724 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
debian_version: buster/sid
os-release:
  NAME="Ubuntu"
  VERSION="19.04 (Disco Dingo)"
  ID=ubuntu
  ID_LIKE=debian
  PRETTY_NAME="Ubuntu 19.04"
  VERSION_ID="19.04"
  HOME_URL="https://www.ubuntu.com/"
```

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## Platform Notes (Continued)

SUPPORT\_URL="https://help.ubuntu.com/"

uname -a:

```
Linux h11dsu-01 5.0.0-25-generic #26-Ubuntu SMP Thu Aug 1 12:04:58 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Full AMD retrpoline, IBPB: conditional, IBRS_FW, STIBP: conditional, RSB filling

run-level 3 Jan 8 13:56

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	ext4	183G	29G	145G	17%	/

From /sys/devices/virtual/dmi/id

BIOS:	American Megatrends Inc.	2.0b	11/15/2019
Vendor:	Supermicro		
Product:	Super Server		
Serial:	0123456789		

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x NO DIMM Unknown
16x Samsung M386A8K40DM2-CWE 64 kB 4 rank 3200

(End of data from sysinfo program)

## Compiler Version Notes

=====

C	619.lbm_s(base, peak) 638.imagick_s(base, peak)
	644.nab_s(base, peak)

=====

(Continued on next page)



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## Compiler Version Notes (Continued)

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
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AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
| 628.pop2\_s(base, peak)

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
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## Compiler Version Notes (Continued)

```
InstalledDir: /sppo/dev/compilers/aoxx-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
    AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aoxx-compiler-2.0.0/bin
```

---

## Base Compiler Invocation

C benchmarks:  
clang

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

Benchmarks using Fortran, C, and C++:  
clang++ clang flang

## Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:  
-fno -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math

(Continued on next page)



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## Base Optimization Flags (Continued)

C benchmarks (continued):

```
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

Fortran benchmarks:

```
-ftlo -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -fopenmp=libomp
-lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-ftlo -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -fopenmp=libomp
-lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -ftlo -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -freemap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lpthread -ldl -lmvec
-lamdlibm -ljemalloc -lflang
```



# SPEC CPU®2017 Floating Point Speed Result

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## Supermicro

A+ Server 2023US-TR4  
(H11DSU-iN , AMD EPYC 7452)

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

SPECSpeed®2017\_fp\_base = 158

SPECSpeed®2017\_fp\_peak = 163

Test Date: Jan-2020

Hardware Availability: Aug-2019

Software Availability: Aug-2019

## Base Other Flags

C benchmarks:

-Wno-return-type -DUSE\_OPENMP

Fortran benchmarks:

-DUSE\_OPENMP -Wno-return-type

Benchmarks using both Fortran and C:

-DUSE\_OPENMP -Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-return-type -DUSE\_OPENMP

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -fllto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
```

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## Peak Optimization Flags (Continued)

619.lbm\_s (continued):

```
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-lmvec -lamdlibm -fopenmp=libomp -lomp -lpthread -ldl
-ljemalloc -lflang
```

638.imagick\_s: basepeak = yes

644.nab\_s: Same as 619.lbm\_s

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

```
654.roms_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang
```

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

627.cam4\_s: basepeak = yes

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
```

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Test Date: Jan-2020

Hardware Availability: Aug-2019

Software Availability: Aug-2019

## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch -mllvm -loop-unswitch-threshold=200000
-O3 -funroll-loops -Mrecursive -Kieee -fno-finite-math-only
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lpthread -ldl -lmvec
-lamdlibm -ljemalloc -lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-return-type -DUSE_OPENMP
```

Fortran benchmarks:

```
-DUSE_OPENMP -Wno-return-type
```

Benchmarks using both Fortran and C:

```
-DUSE_OPENMP -Wno-return-type
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -DUSE_OPENMP
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Rome-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-B1.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Rome-revB.xml>

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