SPEC has determined that this result does not comply with the SPEC OSG Guidelines for General Availability and the SPEC CPU 2017 run and reporting rules. Specifically, at this time, the submitter is not able to make a public statement of intent to ship this particular configuration.

<table>
<thead>
<tr>
<th>Threads</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td></td>
</tr>
<tr>
<td>644.nab_s</td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware
- **CPU Name**: redacted
- **Max MHz**: 0
- **Nominal**: 0
- **Enabled**: 0 cores, 1 chip, 0 threads/core
- **Orderable**: 1 chip
- **Cache L1**: redacted
- **L2**: redacted
- **L3**: redacted
- **Other**: None
- **Memory**: 128 GB (8 x 16 GB 2Rx8 PC4-3200AA-R, running at 3200)

### Software
- **OS**: SUSE Linux Enterprise Server 15 SP1 kernel 4.12.14-195-default
- **Compiler**: C/C++/Fortran: Version 2.0.0 of AOCC
- **Parallel**: Yes
- **Firmware**: Version 1.2.12 released Dec-2019
- **File System**: xfs
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 64-bit
- **Peak Pointers**: 64-bit
- **Other**: jemalloc: jemalloc memory allocator library v5.1.0
- **Power Management**: BIOS set to prefer performance at the cost of additional power usage.
## SPEC CPU®2017 Floating Point Speed Result

Dell Inc.  
PowerEdge R6515 (redacted)  

**SPEC CPU®2017 Floating Point Speed Result**  
Copyright 2017-2020 Standard Performance Evaluation Corporation

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jan-2020  
**Hardware Availability:** Apr-2020  
**Software Availability:** Aug-2019

SPEC has determined that this result does not comply with the SPEC OSG Guidelines for General Availability and the SPEC CPU 2017 run and reporting rules. Specifically, at this time, the submitter is not able to make a public statement of intent to ship this particular configuration.

### Hardware (Continued)

**Storage:** 1 x 960 GB SATA SSD  
**Other:** None

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>NC</td>
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<td>607.cactuBSSN_s</td>
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<td>NC</td>
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<td>NC</td>
<td>NC</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base =  
SPECspeed®2017_fp_peak =

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
http://developer.amd.com/amd-aocc/

### Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.
Dell Inc.  
PowerEdge R6515 (redacted)  

<table>
<thead>
<tr>
<th>SPECspeed®2017fp_peak =</th>
<th>SPECspeed®2017fp_base =</th>
</tr>
</thead>
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CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  
Test Date: Jan-2020  
Hardware Availability: Apr-2020  
Software Availability: Aug-2019

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Operating System Notes

'ulimit –s unlimited' was used to set environment stack size
'ulimit –l 2097152' was used to set environment locked pages in memory limit
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-15"
LD_LIBRARY_PATH = /lib64,amd_speed_aocc200_rome_C_lib/64;/root/cpu2017-1.1.0/amd_speed_aocc200_rome_C_lib/32:
MAIL_C_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "16"

Environment variables set by runcpu during the 603.bwaves_s peak run:
GOMP_CPU_AFFINITY = "0-7"

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:
GOMP_CPU_AFFINITY = "0-7"

(Continued on next page)
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### Environment Variable Notes (Continued)

Environment variables set by runcpu during the 627.cam4_s peak run:

```gomp_cpu_affinity = "0-7"```

Environment variables set by runcpu during the 628.pop2_s peak run:

```gomp_cpu_affinity = "0-7"```

Environment variables set by runcpu during the 638.imagick_s peak run:

```gomp_cpu_affinity = "0-7"```

Environment variables set by runcpu during the 644.nab_s peak run:

```gomp_cpu_affinity = "0 8 9 2 10 3 11 4 12 5 13 6 14 7 15"```

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

```gomp_cpu_affinity = "0-7"```

Environment variables set by runcpu during the 654.roms_s peak run:

```gomp_cpu_affinity = "0-7"```

### General Notes

### Non-Compliant

Bylines were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.
PowerEdge R6515 (redacted)

SPECspeed®2017_fp_base = SPECspeed®2017_fp_peak =

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
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OSG Guidelines for General Availability and the SPEC CPU 2017 run
and reporting rules. Specifically, at this time, the submitter is
not able to make a public statement of intent to ship this
particular configuration.

Platform Notes

BIOS settings:
NUMA Nodes Per Socket set to 4
CCX as NUMA Domain set to Enabled
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost Enabled
Cstates set to Enabled
Memory Patrol Scrub Disabled
Memory Refresh Rate set to 1x
PCI ASPM L1 Link Power Management Disabled
Determinism Slider set to Power Determinism
Efficiency Optimized Mode Disabled
Memory Interleaving set to Disabled
Memory Freq set to 3200
Fan Speed = Maximum

Sysinfo program /root/cpu2017-1.1.0/bin/sysinfo
Revision: 4f8635f 2019-08-21 295195f888a3d7ed81e6e46a485a0011
running on Linux g3ob Sun Dec 29 05:46:59 2020

(Continued on next page)
SPEC has determined that this result does not comply with the SPEC OSG Guidelines for General Availability and the SPEC CPU 2017 run and reporting rules. Specifically, at this time, the submitter is not able to make a public statement of intent to ship this particular configuration.
<table>
<thead>
<tr>
<th>Platform Notes (Continued)</th>
</tr>
</thead>
<tbody>
<tr>
<td>run-level 3 Nov 25 11:37 last=5</td>
</tr>
</tbody>
</table>

SPEC is set to: /root/cpu2017-11.1.0

--mount all rw,uid=1000,gid=1000,mode=0755 /

/dev/sda2 xfs 440G 36G 405G 9% /

From /sys/devices/virtual/dmi/id

BIOS: Dell Inc. 1.2 12/12/2019
Vendor: Dell Inc.
Product: PowerEdge R6515
Product Family: PowerEdge

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x 80A80B380AD HMA82GR7CRCR8N-XN 16 GB 2 rank 3200
8x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 619.1bm_s(base, peak) 638.imagick_s(base, peak)
| 644.nab_s(base, peak)
==============================================================================
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

(Continued on next page)
SPECSPEED®2017_fp_base =
SPECSPEED®2017_fp_peak =

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Compiler Version Notes (Continued)

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins)
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins)
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins)
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
628.pop2_s(base, peak)

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins)
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
628.pop2_s(base, peak)

(Continued on next page)
## SPEC CPU®2017 Floating Point Speed Result

**Dell Inc.**  
**PowerEdge R6515 (redacted)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak =</th>
<th>SPECspeed®2017_fp_base =</th>
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</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jan-2020  
**Hardware Availability:** Apr-2020  
**Software Availability:** Aug-2019

---

**Compiler Version Notes (Continued)**

AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)  
Target: x86_64-unknown-linux-gnu  
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AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

---

**Base Compiler Invocation**

**C benchmarks:**  
clang  

**Fortran benchmarks:**  
flang  

**Both Fortran and C:**  
flang clang

**Benchmarks using Fortran, C, and C++:**  
clang++ clang flang

---

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64  
607.cactuBSSN_s: -DSPEC_LP64  
619.lbm_s: -DSPEC_LP64

(Continued on next page)
SPEC has determined that this result does not comply with the SPEC OSG Guidelines for General Availability and the SPEC CPU 2017 run and reporting rules. Specifically, at this time, the submitter is not able to make a public statement of intent to ship this particular configuration.

**Base Portability Flags (Continued)**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
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<tbody>
<tr>
<td>621.wrf_s</td>
<td>-DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64</td>
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<tr>
<td>627.cam4_s</td>
<td>-DSPEC_CASE_FLAG -DSPEC_LP64</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>-DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>-DSPEC_LP64</td>
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<tr>
<td>644.nab_s</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

**Base Optimization Flags**

**C benchmarks:**

- `-flto -Wl,-mlvm -Wl,-function-specialize`
- `-Wl,-mlvm -Wl,-region-vectorize -Wl,-mlvm -Wl,-vector-library=LIBMVEC`
- `-Wl,-mlvm -Wl,-reduce-array-computations=3 -O3 -ffast-math`
- `-march=znver2 -fstrict-layout=3 -mlvm -unroll-threshold=50`
- `--specialization -z muldefs -DSPEC_OPENMP -fopenmp`
- `fopenmp=libomp -lomp -lpthread -ldl -lmvec -lmdllibm -ljemalloc -lflang`

**Fortran benchmarks:**

- `-flto -Wl,-mlvm -Wl,-function-specialize`
- `-Wl,-mlvm -Wl,-region-vectorize -Wl,-mlvm -Wl,-vector-library=LIBMVEC`
- `-Wl,-mlvm -Wl,-reduce-array-computations=3 -O3 -march=znver2`
- `-funroll-loops -Mrecursive -mlvm -vector-library=LIBMVEC -z muldefs`
- `-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -fopenmp=libomp`
- `-lomp -lpthread -ldl -lmvec -lmdllibm -ljemalloc -lflang`

**Benchmarks using both Fortran and C:**

- `-flto -Wl,-mlvm -Wl,-function-specialize`

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.  
PowerEdge R6515 (redacted)  

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak =</th>
<th>SPECspeed®2017_base =</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dell Inc.</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>PowerEdge R6515</td>
<td>PowerEdge R6515</td>
</tr>
</tbody>
</table>

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### Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
- `-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC`
- `-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math`
- `-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50`
- `-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist`
- `-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp`
- `-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000`
- `-fivy-function-specialization -fivy-loops -Mrecursive -z muldefs`
- `-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -fopenmp=libomp`
- `-lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc -lflang`

Benchmarks using Fortran, C, and C++:
- `-std=c++98 -fno-math搂 -Wl,-mllvm -Wl,-function-specialize`
- `-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC`
- `-Wl,-mllvm -Wl,-reduce-array-computations=3`
- `-Wl,-mllvm -Wl,-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays`
- `-mllvm -function-specialize -mllvm -enable-gvn-hoist`
- `-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp`
- `-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000`
- `-fivy-function-specialization -mllvm -loop-unswitch-threshold=200000`
- `-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch`
- `-fivy-loop -Mrecursive -z muldefs -Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc -lflang`

### Base Other Flags

C benchmarks:
- `-Wno-return-type`

(Continued on next page)
SPECSpeed®2017_fp_peak = **Non-Compliant**

SPECSpeed®2017_fp_base = **Non-Compliant**

Dell Inc.
PowerEdge R6515 (redacted)

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
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### Base Other Flags (Continued)

- Fortran benchmarks: `-Wno-return-type`
- Benchmarks using both Fortran and C: `-Wno-return-type`
- Benchmarks using Fortran, C, and C++: `-Wno-return-type`

### Peak Compiler Invocation

- C benchmarks: `clang`
- Fortran benchmarks: `flang`

### Peak Portability Flags

Same as Base Portability Flags
## Dell Inc. PowerEdge R6515 (redacted)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak</th>
<th>SPECspeed®2017_fp_base</th>
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**CPU2017 License:** 55
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### Peak Optimization Flags

#### C benchmarks:

- `619.lbm_s`: `basepeak = yes`
- `644.nab_s`: Same as 638.imagick_s

#### Fortran benchmarks:


(Continued on next page)
## SPEC CPU®2017 Floating Point Speed Result

**Dell Inc.**  
**PowerEdge R6515 (redacted)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak =</th>
<th>SPECspeed®2017_fp_base =</th>
</tr>
</thead>
</table>

### CPU2017 License: 55

Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  
Test Date: Jan-2020  
Hardware Availability: Apr-2020  
Software Availability: Aug-2019

SPEC has determined that this result does not comply with the SPEC OSG Guidelines for General Availability and the SPEC CPU 2017 run and reporting rules. Specifically, at this time, the submitter is not able to make a public statement of intent to ship this particular configuration.

### Peak Optimization Flags (Continued)

649.fotonik3d_s: Same as 603.bwaves_s


Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes


628.pop2_s: Same as 627.cam4_s

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.
PowerEdge R6515 (redacted)

SPECspspeéd®2017_fp_base =
SPECspspeéd®2017_fp_peak =

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jan-2020
Hardware Availability: Apr-2020
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SPECK has determined that this result does not comply with the SPEC OSG Guidelines for General Availability and the SPEC CPU 2017 run and reporting rules. Specifically, at this time, the submitter is not able to make a public statement of intent to ship this particular configuration.

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

Peak Other Flags

C benchmarks:
-Wno-return-type

Fortran benchmarks:
-Wno-return-type

Benchmarks using both Fortran and C:
-Wno-return-type

Benchmarks using Fortran, C, and C++:
-Wno-return-type
Dell Inc.
PowerEdge R6515 (redacted)

<table>
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**CPU2017 License:** 55
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The flags files that were used to format this result can be browsed at


You can also download the XML flag sources by saving the following links:


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