



# SPEC CPU®2017 Floating Point Rate Result

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## ASUSTeK Computer Inc.

ASUS RS700A-E9V2(KNPP-D32-R) Server System  
3.00 GHz, AMD EPYC 7302

SPECrate®2017\_fp\_base = 290

SPECrate®2017\_fp\_peak = 294

CPU2017 License: 9016

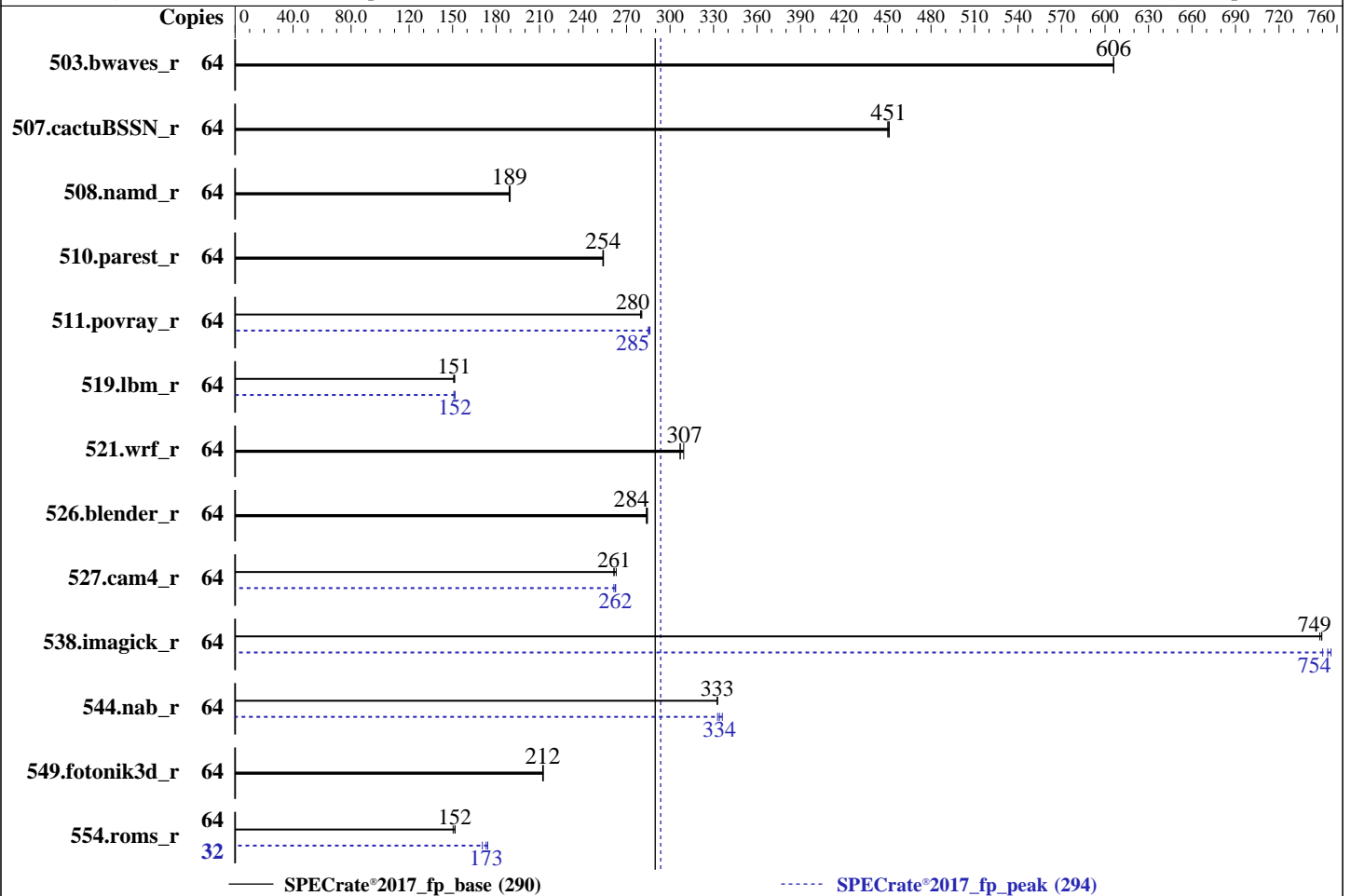
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Nov-2019

Hardware Availability: Nov-2019

Software Availability: Sep-2019



### Hardware

CPU Name: AMD EPYC 7302  
 Max MHz: 3300  
 Nominal: 3000  
 Enabled: 32 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 128 MB I+D on chip per chip,  
 16 MB shared / 2 cores  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
 Storage: 1 x 1 TB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP1 (x86\_64)  
 Kernel 4.12.14-195-default  
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 0202 released Oct-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.2.0  
 Power Management: Prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	1059	606	<b>1059</b>	<b>606</b>	1059	606	64	1059	606	<b>1059</b>	<b>606</b>	1059	606
507.cactuBSSN_r	64	180	451	180	450	<b>180</b>	<b>451</b>	64	180	451	180	450	<b>180</b>	<b>451</b>
508.namd_r	64	<b>321</b>	<b>189</b>	320	190	322	189	64	<b>321</b>	<b>189</b>	320	190	322	189
510.parest_r	64	659	254	<b>659</b>	<b>254</b>	660	254	64	659	254	<b>659</b>	<b>254</b>	660	254
511.povray_r	64	533	281	<b>534</b>	<b>280</b>	534	280	64	524	285	<b>524</b>	<b>285</b>	522	286
519.lbm_r	64	<b>446</b>	<b>151</b>	445	151	448	151	64	<b>445</b>	<b>152</b>	445	152	447	151
521.wrf_r	64	467	307	<b>467</b>	<b>307</b>	463	309	64	467	307	<b>467</b>	<b>307</b>	463	309
526.blender_r	64	343	284	344	284	<b>343</b>	<b>284</b>	64	343	284	344	284	<b>343</b>	<b>284</b>
527.cam4_r	64	426	263	428	261	<b>428</b>	<b>261</b>	64	<b>427</b>	<b>262</b>	426	262	428	261
538.imagick_r	64	<b>212</b>	<b>749</b>	212	750	213	748	64	211	756	<b>211</b>	<b>754</b>	212	750
544.nab_r	64	324	332	<b>324</b>	<b>333</b>	324	333	64	324	333	<b>322</b>	<b>334</b>	321	336
549.fotonik3d_r	64	1176	212	<b>1175</b>	<b>212</b>	1173	213	64	1176	212	<b>1175</b>	<b>212</b>	1173	213
554.roms_r	64	670	152	676	150	<b>671</b>	<b>152</b>	32	292	174	<b>294</b>	<b>173</b>	298	171

SPECrate®2017\_fp\_base = **290**

SPECrate®2017\_fp\_peak = **294**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu

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## Operating System Notes (Continued)

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)  
OS set to performance mode via cpupower frequency-set -g performance.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH =  
"/spec2017c1/amd\_rate\_aocc200\_rome\_C\_lib/64:/spec2017c1/amd\_rate\_aocc200\_rome\_C\_lib/32:"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto  
jemalloc 5.2.0 is available here:  
<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

## Platform Notes

BIOS Configuration:  
Power phase shedding = Disabled  
SVM Mode = Disabled  
SR-IOV support = Disabled  
DRAM Scrub time = Disabled  
NUMA nodes per socket = NPS4  
Determinism Slider = Power

Sysinfo program /spec2017c1/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on linux-fkvs Sat Nov 23 17:00:57 2019

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### Platform Notes (Continued)

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 7302 16-Core Processor

2 "physical id"s (chips)

64 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 16

siblings : 32

physical 0: cores 0 1 4 5 8 9 12 13 16 17 20 21 24 25 28 29

physical 1: cores 0 1 4 5 8 9 12 13 16 17 20 21 24 25 28 29

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

Address sizes: 43 bits physical, 48 bits virtual

CPU(s): 64

On-line CPU(s) list: 0-63

Thread(s) per core: 2

Core(s) per socket: 16

Socket(s): 2

NUMA node(s): 8

Vendor ID: AuthenticAMD

CPU family: 23

Model: 49

Model name: AMD EPYC 7302 16-Core Processor

Stepping: 0

CPU MHz: 3000.000

CPU max MHz: 3000.0000

CPU min MHz: 1500.0000

BogoMIPS: 6060.12

Virtualization: AMD-V

L1d cache: 32K

L1i cache: 32K

L2 cache: 512K

L3 cache: 16384K

NUMA node0 CPU(s): 0-3,32-35

NUMA node1 CPU(s): 4-7,36-39

NUMA node2 CPU(s): 8-11,40-43

NUMA node3 CPU(s): 12-15,44-47

NUMA node4 CPU(s): 16-19,48-51

NUMA node5 CPU(s): 20-23,52-55

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### Platform Notes (Continued)

NUMA node6 CPU(s): 24-27,56-59

NUMA node7 CPU(s): 28-31,60-63

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpe1gb rdtscp lm  
constant\_tsc rep\_good nopl xtopology nonstop\_tsc cpuid extd\_apicid aperfmperf pni  
pclmulqdq monitor ssse3 fma cx16 sse4\_1 sse4\_2 movbe popcnt aes xsave avx f16c  
rdrand lahf\_lm cmp\_legacy svm extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch  
osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpext perfctr\_l2 mwaitx cpb  
cat\_l3 cdp\_l3 hw\_pstate sme ssbd sev ibrs ibpb stibp vmmcall fsgsbase bml avx2 smep  
bmi2 cqm rdt\_a rdseed adx smap clflushopt clwb sha\_ni xsaveopt xsavec xgetbv1 xsaves  
cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local clzero irperf xsaveerptr arat npt  
lbrv svm\_lock nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v\_vmsave\_vmload vgif umip rdpid overflow\_recov succor smca

```
/proc/cpuinfo cache data
cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3 32 33 34 35
node 0 size: 128834 MB
node 0 free: 128624 MB
node 1 cpus: 4 5 6 7 36 37 38 39
node 1 size: 129017 MB
node 1 free: 128804 MB
node 2 cpus: 8 9 10 11 40 41 42 43
node 2 size: 129017 MB
node 2 free: 128817 MB
node 3 cpus: 12 13 14 15 44 45 46 47
node 3 size: 129005 MB
node 3 free: 128781 MB
node 4 cpus: 16 17 18 19 48 49 50 51
node 4 size: 129017 MB
node 4 free: 128829 MB
node 5 cpus: 20 21 22 23 52 53 54 55
node 5 size: 129017 MB
node 5 free: 128824 MB
node 6 cpus: 24 25 26 27 56 57 58 59
node 6 size: 129017 MB
node 6 free: 128821 MB
node 7 cpus: 28 29 30 31 60 61 62 63
node 7 size: 128985 MB
node 7 free: 128795 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
```

(Continued on next page)



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### Platform Notes (Continued)

1:	12	10	12	12	32	32	32	32
2:	12	12	10	12	32	32	32	32
3:	12	12	12	10	32	32	32	32
4:	32	32	32	32	10	12	12	12
5:	32	32	32	32	12	10	12	12
6:	32	32	32	32	12	12	10	12
7:	32	32	32	32	12	12	12	10

From /proc/meminfo

```
MemTotal:      1056677960 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="SLES"
VERSION="15-SP1"
VERSION_ID="15.1"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp1"
```

uname -a:

```
Linux linux-fkvs 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-3620 (L1 Terminal Fault):      Not affected
Microarchitectural Data Sampling:      Not affected
CVE-2017-5754 (Meltdown):              Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):      Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):      Mitigation: Full AMD retpoline, IBPB:
conditional, IBRS_FW, STIBP: conditional, RSB
filling
```

run-level 3 Nov 22 17:44

SPEC is set to: /spec2017c1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	929G	34G	896G	4%	/

From /sys/devices/virtual/dmi/id

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## Platform Notes (Continued)

BIOS: American Megatrends Inc. 0202 10/30/2019  
Vendor: ASUSTeK COMPUTER INC.  
Product: KNPP-D32-R Series  
Product Family: Server  
Serial: System Serial Number

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
16x Samsung M393A8G40AB2-CWE 64 kB 2 rank 3200  
16x Unknown Unknown

(End of data from sysinfo program)

## Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak)  
| 544.nab\_r(base, peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
=====

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
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InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
=====

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
=====

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=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

-----  
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Target: x86\_64-unknown-linux-gnu  
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InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak)  
| 554.roms\_r(base, peak)

-----  
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Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
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=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

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Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64  
510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
526.blender\_r: -funsigned-char -D\_\_BOOL\_DEFINED -DSPEC\_LP64  
527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64



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## Base Optimization Flags

### C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang
```

### C++ benchmarks:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang
```

### Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

### Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

### Benchmarks using both C and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -freemap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
```

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# SPEC CPU®2017 Floating Point Rate Result

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**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
3.00 GHz, AMD EPYC 7302

SPECrate®2017\_fp\_base = 290  
SPECrate®2017\_fp\_peak = 294

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



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## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver2
-mno-sse4a -fstruct-layout=5 -mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:

508.namd\_r: basepeak = yes

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: basepeak = yes

```
554.roms_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

```
527.cam4_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
```

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## Peak Optimization Flags (Continued)

527.cam4\_r (continued):

```
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -O3 -funroll-loops
-Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Rome-V1.0-revC.html>  
<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.2019-09-17.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Rome-V1.0-revC.xml>  
<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.2019-09-17.xml>



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