



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

SPECrate®2017\_fp\_base = 539

SPECrate®2017\_fp\_peak = 589

CPU2017 License: 9016

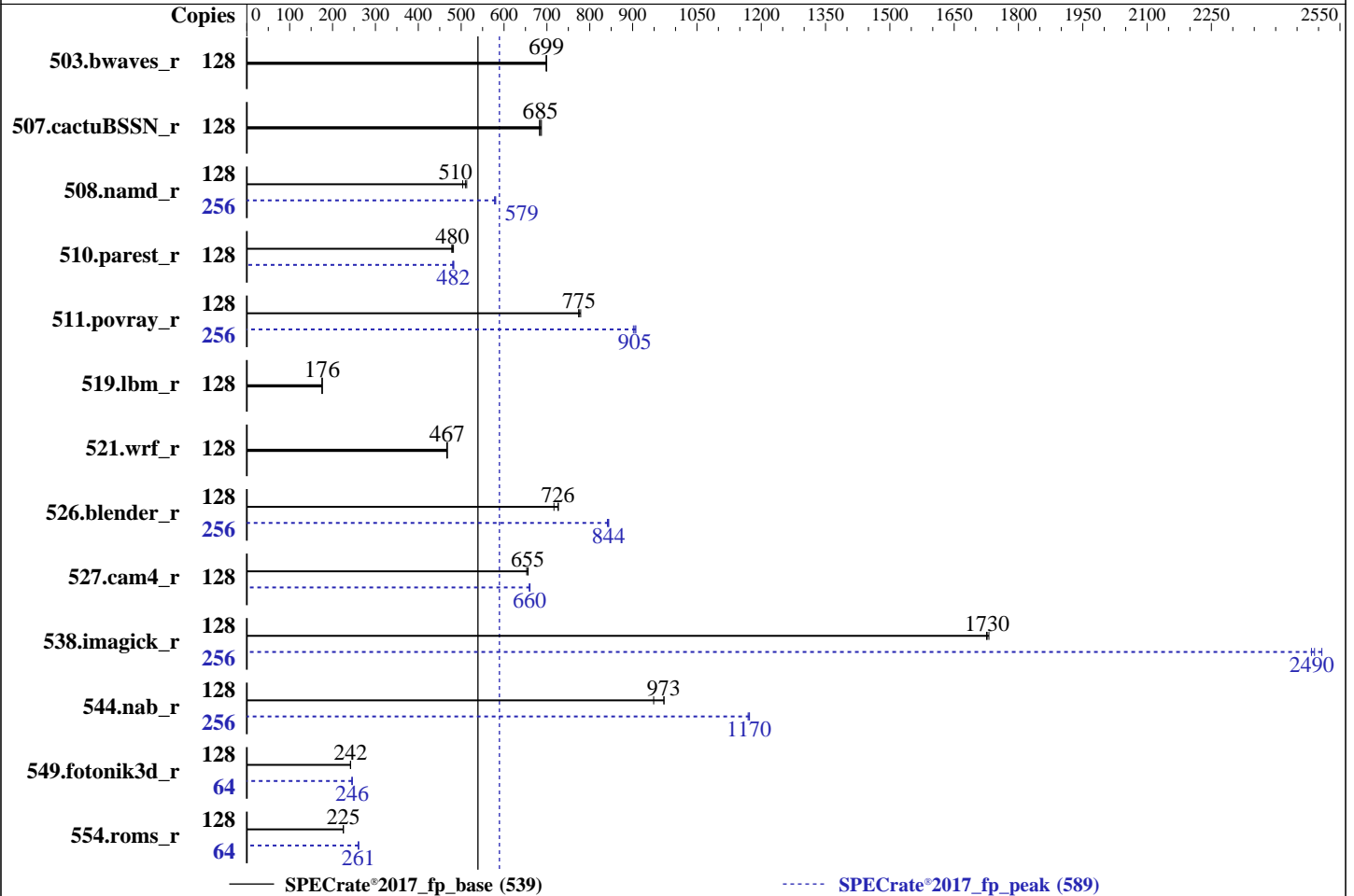
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Nov-2019

Hardware Availability: Nov-2019

Software Availability: Sep-2019



### Hardware

CPU Name: AMD EPYC 7742  
 Max MHz: 3400  
 Nominal: 2250  
 Enabled: 128 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
 16 MB shared / 4 cores  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
 Storage: 1 x 1 TB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP1 (x86\_64)  
 Kernel 4.12.14-195-default  
 Compiler: C/C++/Fortran: Version 2.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 0202 released Oct-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.2.0  
 Power Management: --



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

SPECrate®2017\_fp\_base = 539  
SPECrate®2017\_fp\_peak = 589

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	128	<b><u>1836</u></b>	<b><u>699</u></b>	1835	699	1838	698	128	<b><u>1836</u></b>	<b><u>699</u></b>	1835	699	1838	698
507.cactuBSSN_r	128	<b><u>237</u></b>	<b><u>685</u></b>	237	683	236	687	128	<b><u>237</u></b>	<b><u>685</u></b>	237	683	236	687
508.namd_r	128	242	503	237	513	<b><u>239</u></b>	<b><u>510</u></b>	256	421	578	<b><u>420</u></b>	<b><u>579</u></b>	419	581
510.parest_r	128	<b><u>698</u></b>	<b><u>480</u></b>	700	478	695	482	128	<b><u>695</u></b>	<b><u>482</u></b>	697	480	694	483
511.povray_r	128	386	774	<b><u>386</u></b>	<b><u>775</u></b>	384	779	256	663	902	659	908	<b><u>660</u></b>	<b><u>905</u></b>
519.lbm_r	128	768	176	768	176	<b><u>768</u></b>	<b><u>176</u></b>	128	768	176	768	176	<b><u>768</u></b>	<b><u>176</u></b>
521.wrf_r	128	<b><u>613</u></b>	<b><u>467</u></b>	613	468	615	466	128	<b><u>613</u></b>	<b><u>467</u></b>	613	468	615	466
526.blender_r	128	272	716	<b><u>269</u></b>	<b><u>726</u></b>	268	727	256	462	844	464	841	<b><u>462</u></b>	<b><u>844</u></b>
527.cam4_r	128	343	653	<b><u>342</u></b>	<b><u>655</u></b>	341	656	128	<b><u>339</u></b>	<b><u>660</u></b>	340	659	339	661
538.imagick_r	128	184	1730	<b><u>184</u></b>	<b><u>1730</u></b>	184	1730	256	<b><u>256</u></b>	<b><u>2490</u></b>	256	2480	254	2510
544.nab_r	128	227	949	<b><u>221</u></b>	<b><u>973</u></b>	221	973	256	367	1170	368	1170	<b><u>368</u></b>	<b><u>1170</u></b>
549.fotonik3d_r	128	2062	242	<b><u>2062</u></b>	<b><u>242</u></b>	2061	242	64	1015	246	1017	245	<b><u>1016</u></b>	<b><u>246</u></b>
554.roms_r	128	<b><u>903</u></b>	<b><u>225</u></b>	903	225	905	225	64	391	260	<b><u>390</u></b>	<b><u>261</u></b>	390	261

SPECrate®2017\_fp\_base = **539**

SPECrate®2017\_fp\_peak = **589**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory  
sync then drop\_caches=3 to reset caches before invoking runcpu

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**

ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

SPECrate®2017\_fp\_base = 539

SPECrate®2017\_fp\_peak = 589

**CPU2017 License:** 9016

**Test Sponsor:** ASUSTeK Computer Inc.

**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019

**Hardware Availability:** Nov-2019

**Software Availability:** Sep-2019

## Operating System Notes (Continued)

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH =

"/spec2017cl/amd\_rate\_aocc200\_rome\_C\_lib/64;/spec2017cl/amd\_rate\_aocc200\_rome\_C\_lib/32:"

MALLOC\_CONF = "retain:true"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto  
jemalloc 5.2.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2>

## Platform Notes

BIOS Configuration:

Power phase shedding = Disabled

SVM Mode = Disabled

SR-IOV support = Disabled

DRAM Scrub time = Disabled

NUMA nodes per socket = NPS4

Determinism Slider = Power

Sysinfo program /spec2017cl/bin/sysinfo

Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011

running on linux-fkvs Wed Nov 6 04:13:44 2019

SUT (System Under Test) info as seen by some common utilities.

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Platform Notes (Continued)

For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : AMD EPYC 7742 64-Core Processor
 2 "physical id"s (chips)
256 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 64
siblings  : 128
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55 56 57 58 59 60 61 62 63
```

```
From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
Address sizes:          43 bits physical, 48 bits virtual
CPU(s):                 256
On-line CPU(s) list:   0-255
Thread(s) per core:    2
Core(s) per socket:    64
Socket(s):              2
NUMA node(s):          8
Vendor ID:              AuthenticAMD
CPU family:             23
Model:                  49
Model name:             AMD EPYC 7742 64-Core Processor
Stepping:               0
CPU MHz:                2250.000
CPU max MHz:           2250.0000
CPU min MHz:           1500.0000
BogoMIPS:               4545.16
Virtualization:        AMD-V
L1d cache:              32K
L1i cache:              32K
L2 cache:               512K
L3 cache:               16384K
NUMA node0 CPU(s):     0-15,128-143
NUMA node1 CPU(s):     16-31,144-159
NUMA node2 CPU(s):     32-47,160-175
NUMA node3 CPU(s):     48-63,176-191
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Platform Notes (Continued)

```
NUMA node4 CPU(s): 64-79,192-207
NUMA node5 CPU(s): 80-95,208-223
NUMA node6 CPU(s): 96-111,224-239
NUMA node7 CPU(s): 112-127,240-255
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx
f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse
3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_l2 mwaitx cpb cat_l3 cdp_l3 hw_pstate sme ssbd sev ibrs ibpb stibp vmmcall
fsgsbase bmi1 avx2 smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
clzero irperf xsaveerptr arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean
flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip
rdpid overflow_recov succor smca
```

```
/proc/cpuinfo cache data
cache size : 512 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 8 nodes (0-7)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 128 129 130 131 132 133 134 135 136
137 138 139 140 141 142 143
node 0 size: 128819 MB
node 0 free: 128331 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 144 145 146 147 148 149
150 151 152 153 154 155 156 157 158 159
node 1 size: 129016 MB
node 1 free: 128604 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 160 161 162 163 164 165
166 167 168 169 170 171 172 173 174 175
node 2 size: 128987 MB
node 2 free: 128572 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 176 177 178 179 180 181
182 183 184 185 186 187 188 189 190 191
node 3 size: 129004 MB
node 3 free: 128542 MB
node 4 cpus: 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 192 193 194 195 196 197
198 199 200 201 202 203 204 205 206 207
node 4 size: 129016 MB
node 4 free: 128637 MB
node 5 cpus: 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 208 209 210 211 212 213
214 215 216 217 218 219 220 221 222 223
node 5 size: 129016 MB
node 5 free: 128623 MB
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Platform Notes (Continued)

node 6 cpus: 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 224 225 226  
227 228 229 230 231 232 233 234 235 236 237 238 239

node 6 size: 129016 MB

node 6 free: 128639 MB

node 7 cpus: 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 240 241  
242 243 244 245 246 247 248 249 250 251 252 253 254 255

node 7 size: 129014 MB

node 7 free: 128638 MB

node distances:

node	0	1	2	3	4	5	6	7
0:	10	12	12	12	32	32	32	32
1:	12	10	12	12	32	32	32	32
2:	12	12	10	12	32	32	32	32
3:	12	12	12	10	32	32	32	32
4:	32	32	32	32	10	12	12	12
5:	32	32	32	32	12	10	12	12
6:	32	32	32	32	12	12	10	12
7:	32	32	32	32	12	12	12	10

From /proc/meminfo

MemTotal: 1056659436 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:  
NAME="SLES"  
VERSION="15-SP1"  
VERSION\_ID="15.1"  
PRETTY\_NAME="SUSE Linux Enterprise Server 15 SP1"  
ID="sles"  
ID\_LIKE="suse"  
ANSI\_COLOR="0;32"  
CPE\_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:

Linux linux-fkvs 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)  
x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected  
Microarchitectural Data Sampling: Not affected  
CVE-2017-5754 (Meltdown): Not affected  
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp  
CVE-2017-5753 (Spectre variant 1): Mitigation: \_\_user pointer sanitization

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional, IBRS\_FW, STIBP: conditional, RSB filling

run-level 3 Nov 5 15:05

SPEC is set to: /spec2017c1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	929G	20G	909G	3%	/

```

From /sys/devices/virtual/dmi/id
  BIOS:      American Megatrends Inc. 0202 10/30/2019
  Vendor:    ASUSTeK COMPUTER INC.
  Product:   KNPP-D32-R Series
  Product Family: Server
  Serial:    System Serial Number

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
  16x Samsung M393A8G40AB2-CWE 64 kB 2 rank 3200
  16x Unknown Unknown

```

(End of data from sysinfo program)

## Compiler Version Notes

```

=====
C          | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
          | 544.nab_r(base, peak)
-----

```

```

AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0-Build#191) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
-----

```

```

=====
C++       | 508.namd_r(base, peak) 510.parest_r(base, peak)
-----

```

```

AOCCLLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCCLLVM.2.0.0-Build#191) (based on LLVM AOCCLLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Compiler Version Notes (Continued)

Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

-----  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

-----  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak)  
| 554.roms\_r(base, peak)

-----  
AOCCLLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCCLLVM.2.0.0.B191.2019\_07\_19) (based on LLVM AOCCLLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

SPECrate®2017\_fp\_base = 539  
SPECrate®2017\_fp\_peak = 589

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Compiler Version Notes (Continued)

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin  
-----

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

Benchmarks using both C and C++:  
clang++ clang

Benchmarks using Fortran, C, and C++:  
clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
507.cactuBSSN\_r: -DSPEC\_LP64  
508.namd\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

SPECrate®2017\_fp\_base = 539  
SPECrate®2017\_fp\_peak = 589

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Base Portability Flags (Continued)

```
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

### C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang
```

### C++ benchmarks:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang
```

### Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

### Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

SPECrate®2017\_fp\_base = 539

SPECrate®2017\_fp\_peak = 589

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Nov-2019

Hardware Availability: Nov-2019

Software Availability: Sep-2019

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -freemap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -freemap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang
```

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Peak Compiler Invocation (Continued)

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

Benchmarks using both C and C++:  
clang++ clang

Benchmarks using Fortran, C, and C++:  
clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

```
538.imagick_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang
```

544.nab\_r: Same as 538.imagick\_r

C++ benchmarks:

```
508.namd_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016  
**Test Sponsor:** ASUSTeK Computer Inc.  
**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Sep-2019

## Peak Optimization Flags (Continued)

508.namd\_r (continued):

```
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang
```

510.parest\_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -Ofast -march=znver2
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang
```

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

549.fotonik3d\_r: -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver2 -funroll-loops -Mrecursive
-mllvm -vector-library=LIBMVEC -Kieee
-fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang
```

554.roms\_r: -flto -Wl,-mllvm -Wl,-function-specialize

```
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**

ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

SPECrate®2017\_fp\_base = 539

SPECrate®2017\_fp\_peak = 589

**CPU2017 License:** 9016

**Test Sponsor:** ASUSTeK Computer Inc.

**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019

**Hardware Availability:** Nov-2019

**Software Availability:** Sep-2019

## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

```
527.cam4_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -O3 -funroll-loops
-Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang
```

```
526.blender_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**ASUSTeK Computer Inc.**  
ASUS RS700A-E9V2(KNPP-D32-R) Server System  
2.25 GHz, AMD EPYC 7742

**SPECrate®2017\_fp\_base = 539**  
**SPECrate®2017\_fp\_peak = 589**

**CPU2017 License:** 9016

**Test Sponsor:** ASUSTeK Computer Inc.

**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Nov-2019

**Hardware Availability:** Nov-2019

**Software Availability:** Sep-2019

## Peak Optimization Flags (Continued)

526.blender\_r (continued):

```
-mllvm -vector-library=LIBMVEC  
-mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000  
-flv-function-specialization -mllvm -unroll-threshold=100  
-mllvm -enable-partial-unswitch  
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm  
-ljemalloc -lflang
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Rome-V1.0-revA.2019-11-12.html>

<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.2019-09-17.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Rome-V1.0-revA.2019-11-12.xml>

<http://www.spec.org/cpu2017/flags/aocc200-flags-A1.2019-09-17.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.0 on 2019-11-05 15:13:43-0500.

Report generated on 2019-12-10 14:59:24 by CPU2017 PDF formatter v6255.

Originally published on 2019-12-10.