SPEC CPU®2017 Floating Point Speed Result

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Tyrone Systems

(Test Sponsor: Netweb Pte Ltd)

DS400TR-54/R

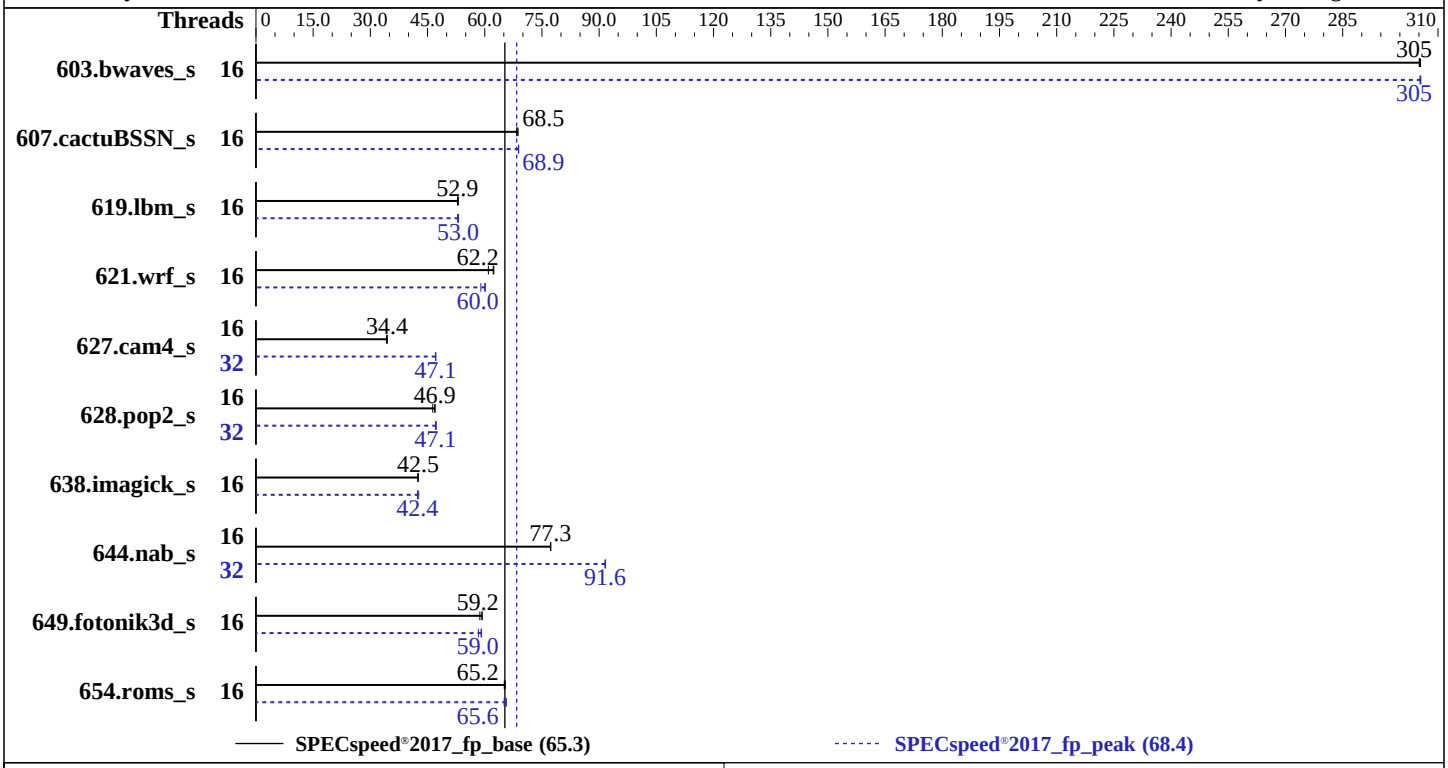
(2.10 GHz,Intel Xeon Silver 4110)

SPECspeed®2017_fp_base = 65.3

SPECspeed®2017_fp_peak = 68.4

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Nov-2019
Hardware Availability: Sep-2019
Software Availability: Aug-2019



Hardware

CPU Name:	Intel Xeon Silver 4110
Max MHz:	3000
Nominal:	2100
Enabled:	16 cores, 2 chips, 2 threads/core
Orderable:	1, 2 (chip)s
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	11 MB I+D on chip per chip
Other:	None
Memory:	384 GB (12 x 32 GB 2Rx4 PC4-2933P-R, running at 2400)
Storage:	1 x 480 GB SSD
Other:	None

Software

OS:	CentOS Linux release 7.7.1908 (Core) 3.10.0-1062.el7.x86_64
Compiler:	C/C++: Version 19.0.4.243 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.243 of Intel Fortran Compiler Build 20190416 for Linux
Parallel:	Yes
Firmware:	Version 3.1a released Jun-2019
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	None

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	193	305	**193**	**305**	193	305	16	**193**	**305**	193	306	193	305
607.cactuBSSN_s	16	**244**	**68.5**	243	68.7	244	68.4	16	242	68.9	**242**	**68.9**	242	68.9
619.lbm_s	16	98.7	53.1	99.1	52.9	**99.1**	**52.9**	16	**98.8**	**53.0**	99.0	52.9	98.6	53.1
621.wrf_s	16	**213**	**62.2**	217	61.0	212	62.4	16	220	60.2	224	59.0	**220**	**60.0**
627.cam4_s	16	259	34.3	257	34.4	**258**	**34.4**	32	**188**	**47.1**	188	47.1	188	47.1
628.pop2_s	16	253	47.0	**253**	**46.9**	256	46.4	32	**252**	**47.1**	251	47.3	252	47.1
638.imagick_s	16	339	42.5	340	42.5	**339**	**42.5**	16	338	42.7	**340**	**42.4**	341	42.4
644.nab_s	16	**226**	**77.3**	226	77.2	226	77.3	32	**191**	**91.6**	191	91.6	191	91.7
649.fotonik3d_s	16	**154**	**59.2**	155	58.7	154	59.3	16	154	59.2	**154**	**59.0**	156	58.3
654.roms_s	16	242	65.1	241	65.4	**242**	**65.2**	16	**240**	**65.6**	240	65.7	242	65.1

SPECspeed®2017_fp_base = 65.3

SPECspeed®2017_fp_peak = 68.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has learned that this result, which used an evaluation compiler, was submitted contrary to the compiler license terms.
Intel has granted a one-time waiver for this result.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on NODE3 Mon Nov  4 19:14:15 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
      2  "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7
      physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
     Architecture:          x86_64
     CPU op-mode(s):        32-bit, 64-bit
     Byte Order:            Little Endian
     CPU(s):                32
     On-line CPU(s) list:   0-31
     Thread(s) per core:    2
     Core(s) per socket:    8
     Socket(s):             2
     NUMA node(s):          2
     Vendor ID:             GenuineIntel
     CPU family:            6
     Model:                 85
     Model name:            Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
     Stepping:              4
     CPU MHz:               800.061
     CPU max MHz:           3000.0000
     CPU min MHz:           800.0000
     BogoMIPS:              4200.00
```

(Continued on next page)

Platform Notes (Continued)

```
      Virtualization:        VT-x
      L1d cache:             32K
      L1i cache:             32K
      L2 cache:              1024K
      L3 cache:              11264K
      NUMA node0 CPU(s):     0-7,16-23
      NUMA node1 CPU(s):     8-15,24-31
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
      aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
      fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
      xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_ppin
      intel_pt ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase
      tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
      rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
      cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku ospke
      md_clear spec_ctrl intel_stibp flush_l1d

  /proc/cpuinfo cache data
     cache size : 11264 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
    node 0 size: 195239 MB
    node 0 free: 172425 MB
    node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
    node 1 size: 196608 MB
    node 1 free: 176861 MB
    node distances:
    node   0   1
      0:  10  21
      1:  21  10

  From /proc/meminfo
     MemTotal:       394875832 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     centos-release: CentOS Linux release 7.7.1908 (Core)
     centos-release-upstream: Derived from Red Hat Enterprise Linux 7.7 (Source)
     os-release:
        NAME="CentOS Linux"
        VERSION="7 (Core)"
```

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Platform Notes (Continued)

```
       ID="centos"
       ID_LIKE="rhel fedora"
       VERSION_ID="7"
       PRETTY_NAME="CentOS Linux 7 (Core)"
       ANSI_COLOR="0;31"
       CPE_NAME="cpe:/o:centos:centos:7"
   redhat-release: CentOS Linux release 7.7.1908 (Core)
   system-release: CentOS Linux release 7.7.1908 (Core)
   system-release-cpe: cpe:/o:centos:centos:7

uname -a:
   Linux NODE3 3.10.0-1062.el7.x86_64 #1 SMP Wed Aug 7 18:08:02 UTC 2019 x86_64 x86_64
   x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):        Mitigation: PTE Inversion; VMX: conditional
                                          cache flushes, SMT vulnerable
Microarchitectural Data Sampling:         Mitigation: Clear CPU buffers; SMT vulnerable
CVE-2017-5754 (Meltdown):                 Mitigation: PTI
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                          via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):        Mitigation: Load fences, __user pointer
                                          sanitization
CVE-2017-5715 (Spectre variant 2):        Mitigation: Full retpoline, IBPB

run-level 3 Nov 2 19:04

SPEC is set to: /home/cpu2017
   Filesystem              Type  Size  Used Avail Use% Mounted on
   /dev/mapper/centos-home xfs   392G  123G  270G  32% /home

From /sys/devices/virtual/dmi/id
    BIOS:    American Megatrends Inc. 3.1a 06/11/2019
    Vendor:  Tyrone Systems
    Product: X11DAi-N
    Serial:  123456789

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
                | 644.nab_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
icpc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
ifort: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
------------------------------------------------------------------------------

==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
                | 654.roms_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
ifort: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
                | 628.pop2_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
ifort: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.243 Build 20190416
```

(Continued on next page)

Compiler Version Notes (Continued)

```
Copyright (C) 1985-2019 Intel Corporation.  All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc -m64 -std=c11`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`ifort -m64 icc -m64 -std=c11`

Benchmarks using Fortran, C, and C++:
`icpc -m64 icc -m64 -std=c11 ifort -m64`

Base Portability Flags

603.bwaves_s: `-DSPEC_LP64`
607.cactuBSSN_s: `-DSPEC_LP64`
619.lbm_s: `-DSPEC_LP64`
621.wrf_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
627.cam4_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
628.pop2_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl`
638.imagick_s: `-DSPEC_LP64`
644.nab_s: `-DSPEC_LP64`
649.fotonik3d_s: `-DSPEC_LP64`
654.roms_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP`

Fortran benchmarks:
`-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp`

(Continued on next page)

Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:

(Continued on next page)

Peak Optimization Flags (Continued)

603.bwaves_s: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs`

649.fotonik3d_s: `Same as 603.bwaves_s`

654.roms_s: `-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs`

Benchmarks using both Fortran and C:

621.wrf_s: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`

627.cam4_s: `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`

628.pop2_s: `Same as 621.wrf_s`

Benchmarks using Fortran, C, and C++:
`-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-15.html
http://www.spec.org/cpu2017/flags/Default-Platform-Flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-15.xml
http://www.spec.org/cpu2017/flags/Default-Platform-Flags.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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