



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Fujitsu

PRIMEQUEST 3800E2, Intel Xeon Gold 6244,  
3.60GHz

SPECrate®2017\_fp\_base = 297

SPECrate®2017\_fp\_peak = Not Run

CPU2017 License: 19

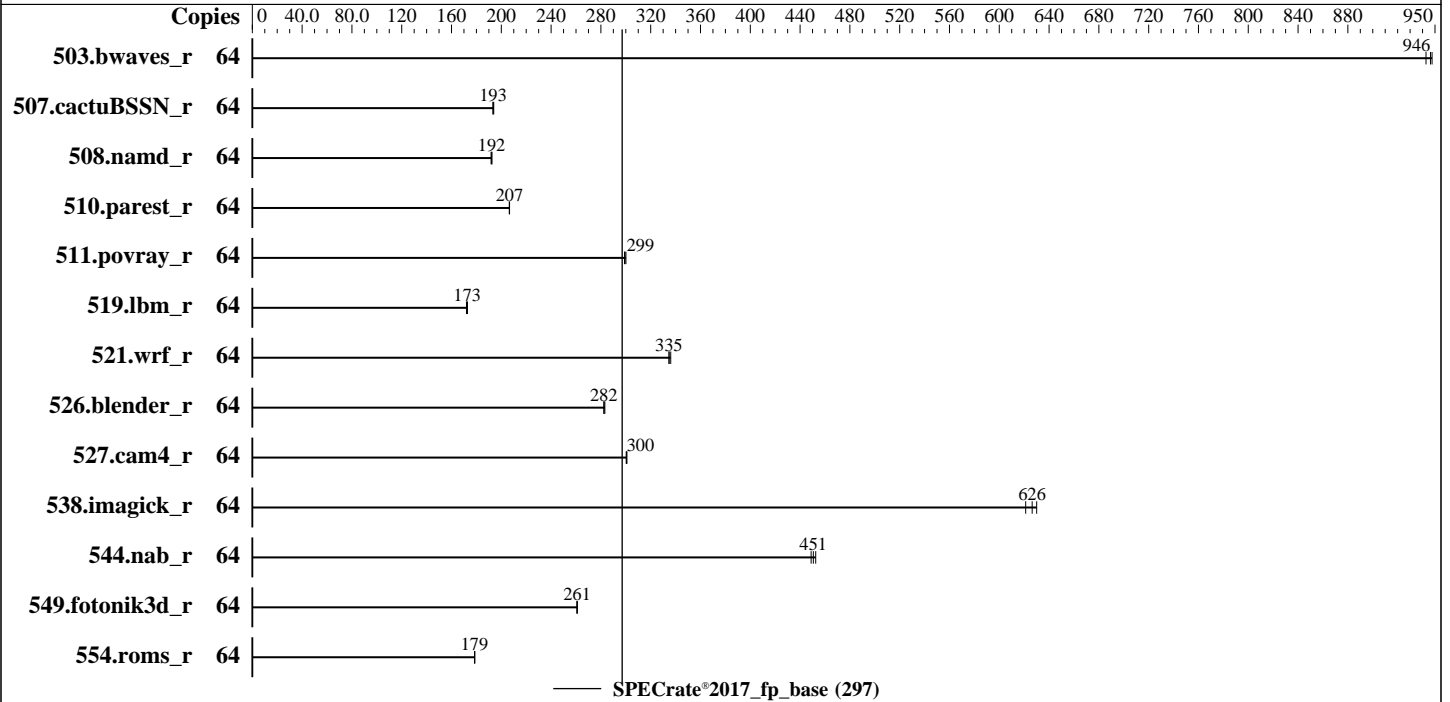
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Jun-2019

Hardware Availability: Apr-2019

Software Availability: Feb-2019



### Hardware

CPU Name: Intel Xeon Gold 6244  
 Max MHz: 4400  
 Nominal: 3600  
 Enabled: 32 cores, 4 chips, 2 threads/core  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 24.75 MB I+D on chip per chip  
 Other: None  
 Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2933Y-R)  
 Storage: 1 x SAS HDD, 600GB, 10.5K RPM, SAS HDD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15  
 4.12.14-25.28-default  
 Compiler: C/C++: Version 19.0.0.117 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 19.0.0.117 of Intel Fortran  
 Compiler for Linux  
 Parallel: No  
 Firmware: PRIMEQUEST 3800E2 Unified Firmware  
 Version PB19043, Released Jun-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: --



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	64	677	948	<b><u>678</u></b>	<b><u>946</u></b>	681	943							
507.cactuBSSN_r	64	418	194	<b><u>419</u></b>	<b><u>193</u></b>	419	193							
508.namd_r	64	316	192	317	192	<b><u>316</u></b>	<b><u>192</u></b>							
510.parest_r	64	810	207	<b><u>810</u></b>	<b><u>207</u></b>	811	206							
511.povray_r	64	500	299	498	300	<b><u>499</u></b>	<b><u>299</u></b>							
519.lbm_r	64	390	173	<b><u>391</u></b>	<b><u>173</u></b>	392	172							
521.wrf_r	64	<b><u>428</u></b>	<b><u>335</u></b>	427	336	428	335							
526.blender_r	64	345	282	344	283	<b><u>345</u></b>	<b><u>282</u></b>							
527.cam4_r	64	<b><u>373</u></b>	<b><u>300</u></b>	373	300	372	301							
538.imagick_r	64	<b><u>254</u></b>	<b><u>626</u></b>	256	621	253	630							
544.nab_r	64	238	453	240	449	<b><u>239</u></b>	<b><u>451</u></b>							
549.fotonik3d_r	64	956	261	<b><u>956</u></b>	<b><u>261</u></b>	956	261							
554.roms_r	64	569	179	<b><u>570</u></b>	<b><u>179</u></b>	570	178							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Kernel Boot Parameter set with : nohz\_full=1-63

## General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/home/Benchmark/speccpu2017-fp/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

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### General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

BIOS configuration:  
DCU Streamer Prefetcher = Disabled  
DDR4 Write Data CRC Protection = Disabled  
HWPM Support = Native Mode with no legacy  
LLC Dead Line Alloc = Disabled  
Sub Numa Clustering = Enabled  
Uncore Frequency Scaling = Disabled  
UPI Link L0p = Disabled  
Fan Control = Full  
Sysinfo program /home/Benchmark/speccpu2017-fp/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-8r5c Wed Jun 19 17:17:58 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz  
4 "physical id"s (chips)  
64 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 16  
physical 0: cores 1 2 3 4 9 17 18 27  
physical 1: cores 2 3 17 18 19 20 24 25  
physical 2: cores 3 4 8 17 18 24 25 27  
physical 3: cores 2 4 9 18 19 24 25 27

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 64  
On-line CPU(s) list: 0-63  
Thread(s) per core: 2

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### Platform Notes (Continued)

```

Core(s) per socket: 8
Socket(s): 4
NUMA node(s): 8
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
Stepping: 6
CPU MHz: 3600.000
CPU max MHz: 4400.0000
CPU min MHz: 1200.0000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,1,4,5,32,33,36,37
NUMA node1 CPU(s): 2,3,6,7,34,35,38,39
NUMA node2 CPU(s): 8,10,14,15,40,42,46,47
NUMA node3 CPU(s): 9,11-13,41,43-45
NUMA node4 CPU(s): 16,17,20,23,48,49,52,55
NUMA node5 CPU(s): 18,19,21,22,50,51,53,54
NUMA node6 CPU(s): 24,26,29,30,56,58,61,62
NUMA node7 CPU(s): 25,27,28,31,57,59,60,63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku
ospke avx512_vnni flush_l1d arch_capabilities

```

```
/proc/cpuinfo cache data
cache size : 25344 KB
```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 8 nodes (0-7)
node 0 cpus: 0 1 4 5 32 33 36 37
node 0 size: 192263 MB
node 0 free: 191874 MB

```

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### Platform Notes (Continued)

```

node 1 cpus: 2 3 6 7 34 35 38 39
node 1 size: 193523 MB
node 1 free: 193370 MB
node 2 cpus: 8 10 14 15 40 42 46 47
node 2 size: 193494 MB
node 2 free: 193341 MB
node 3 cpus: 9 11 12 13 41 43 44 45
node 3 size: 193523 MB
node 3 free: 193329 MB
node 4 cpus: 16 17 20 23 48 49 52 55
node 4 size: 193523 MB
node 4 free: 193361 MB
node 5 cpus: 18 19 21 22 50 51 53 54
node 5 size: 193523 MB
node 5 free: 193370 MB
node 6 cpus: 24 26 29 30 56 58 61 62
node 6 size: 193523 MB
node 6 free: 193349 MB
node 7 cpus: 25 27 28 31 57 59 60 63
node 7 size: 193320 MB
node 7 free: 192965 MB
node distances:
node  0  1  2  3  4  5  6  7
 0:  10 11 20 20 20 20 28 28
 1:  11 10 20 20 20 20 28 28
 2:  20 20 10 11 28 28 20 20
 3:  20 20 11 10 28 28 20 20
 4:  20 20 28 28 10 11 20 20
 5:  20 20 28 28 11 10 20 20
 6:  28 28 20 20 20 20 10 11
 7:  28 28 20 20 20 20 11 10

```

From /proc/meminfo

```

MemTotal:      1583818816 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

```

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### Platform Notes (Continued)

```
uname -a:
Linux linux-8r5c 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019 (dd6077c)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown):          Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB
filling
```

```
run-level 3 Jun 19 17:05
```

```
SPEC is set to: /home/Benchmark/speccpu2017-fp
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   142G   35G  107G  25% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS FUJITSU V1.0.0.0 R1.16.0 for D3858-B1x          04/24/2019
```

```
Memory:
35x Micron 36ASF4G72PZ-2G9E2 32 GB 2 rank 2933
13x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933
```

(End of data from sysinfo program)

### Compiler Version Notes

```
=====  
C          | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)  
=====
```

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====
```

```
=====  
C++       | 508.namd_r(base) 510.parest_r(base)  
=====
```

```
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====
```

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### Compiler Version Notes (Continued)

=====  
C++, C | 511.povray\_r(base) 526.blender\_r(base)  
=====

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base)  
=====

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

=====  
Fortran | 503.bwaves\_r(base) 549.fotonik3d\_r(base) 554.roms\_r(base)  
=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
=====

=====  
Fortran, C | 521.wrf\_r(base) 527.cam4\_r(base)  
=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.0.117 Build 20180804  
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## Base Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64  
507.cactuBSSN_r: -DSPEC_LP64  
508.namd_r: -DSPEC_LP64  
510.parest_r: -DSPEC_LP64  
511.povray_r: -DSPEC_LP64  
519.lbm_r: -DSPEC_LP64  
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char  
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG  
538.imagick_r: -DSPEC_LP64  
544.nab_r: -DSPEC_LP64  
549.fotonik3d_r: -DSPEC_LP64  
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
```

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## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
```

```
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

```
-align array32byte
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
```

```
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

```
-align array32byte
```

Benchmarks using both C and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
```

```
-qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
```

```
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

```
-align array32byte
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic19.0-official-linux64.2019-01-15.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic19.0-official-linux64.2019-01-15.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevE.xml>

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Tested with SPEC CPU®2017 v1.0.5 on 2019-06-19 04:17:57-0400.

Report generated on 2019-11-12 15:01:32 by CPU2017 PDF formatter v6255.

Originally published on 2019-11-12.