Supermicro
SuperWorkstation 5039A-i (X11SRA, Intel Xeon W-2295)

SPEC CPU®2017 Integer Speed Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

**SPECspeed®2017_int_base = 11.9**

**SPECspeed®2017_int_peak = 12.2**

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
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<tbody>
<tr>
<td>OS: SUSE Linux Enterprise Server 12 SP4 (x86_64)</td>
<td>CPU Name: Intel Xeon W-2295</td>
</tr>
<tr>
<td>Kernel 4.12.14-95.29-default</td>
<td>Max MHz: 4600</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.4.227 of Intel Fortran Compiler for Linux</td>
<td>Nominal: 3000</td>
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<tr>
<td>Parallel: Yes</td>
<td>Enabled: 18 cores, 1 chip, 2 threads/core</td>
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<tr>
<td>Firmware: Version 2.0 released Sep-2019</td>
<td>Orderable: 1 chip</td>
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<tr>
<td>File System: xfs</td>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>L3: 24.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
<td>Other: None</td>
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<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
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**Test Date: Sep-2019**
**Hardware Availability: Oct-2019**

**CPU2017 License: 001176**
**Test Sponsor: Supermicro**
**Tested by: Supermicro**
**Software Availability: Aug-2019**
**Hardware Availability: Oct-2019**

Test Date: Sep-2019

**Tested by:** Supermicro

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### Supermicro

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<th>Seconds</th>
<th>Ratio</th>
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<td>36</td>
<td>217</td>
<td>8.19</td>
<td>215</td>
<td>8.24</td>
<td>214</td>
<td>8.28</td>
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<td>185</td>
<td>9.62</td>
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<td>602.gcc_s</td>
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<td>9.53</td>
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<td>620.omnetpp_s</td>
<td>36</td>
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<td>623.xalanchmk_s</td>
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<td>36</td>
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<td>625.x264_s</td>
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<td>631.deepsjeng_s</td>
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<td>5.86</td>
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<tr>
<td>648.exchange2_s</td>
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<td>143</td>
<td>20.5</td>
<td>143</td>
<td>20.5</td>
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<td>36</td>
<td>251</td>
<td>24.6</td>
<td>251</td>
<td>24.6</td>
<td>251</td>
</tr>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync
```

```
/proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

(Continued on next page)
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SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 12.2

CPU2017 License: 001176
Test Date: Sep-2019
Test Sponsor: Supermicro
Tested by: Supermicro

Hardware Availability: Oct-2019
Software Availability: Aug-2019

General Notes (Continued)

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbb1e6e46a485a0011
running on linux Sun Sep 22 11:44:36 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) W-2295 CPU @ 3.00GHz
  1 "physical id"s (chips)
  36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
siblings : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:
  Architecture:          x86_64
  CPU op-mode(s):        32-bit, 64-bit
  Byte Order:            Little Endian
  CPU(s):                36
  On-line CPU(s) list:   0-35
  Thread(s) per core:    2
  Core(s) per socket:    18
  Socket(s):             1
  NUMA node(s):          1
  Vendor ID:             GenuineIntel
  CPU family:            6
  Model:                 85
  Model name:            Intel(R) Xeon(R) W-2295 CPU @ 3.00GHz
  Stepping:              7
  CPU MHz:               3000.000
  CPU max MHz:           4800.0000
  CPU min MHz:           1200.0000
  BogoMIPS:              6000.00
  Virtualization:        VT-x
  L1d cache:             32K
  L1i cache:             32K
  L2 cache:              1024K

(Continued on next page)
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SPECspeed®2017_int_base = 11.9
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Platform Notes (Continued)

L3 cache: 25344K
NUMA node0 CPU(s): 0-35
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpedbg rdtsq
rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrnr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdg_l3
invpcid_single intel_pni ssbd mba ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bni1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occup_l1t cqm_mbm_total

cqm_mbm_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req
avx512_vnni md_clear flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip:
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
28 29 30 31 32 33 34 35
node 0 size: 128367 MB
node 0 free: 97751 MB
node distances:
node 0
0: 10

From /proc/meminfo
MemTotal: 131448716 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP4

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 4
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP4"

(Continued on next page)
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Platform Notes (Continued)

VERSION_ID="12.4"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
Linux linux 4.12.14-95.29-default #1 SMP Thu Aug 1 15:34:33 UTC 2019 (47e48a4) x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Sep 21 08:17

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 1.8T 103G 1.7T 6% /home

From /sys/devices/virtual/dmi/id
BIOS: American Megatrends Inc. 2.0 09/20/2019
Vendor: Supermicro
Product: Super Server
Serial: 0123456789

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
4x Hynix HMA84GR7CJR4N-XN 32 GB 2 rank 3200, configured at 2934
4x NO DIMM NO DIMM

(End of data from sysinfo program)
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Compiler Version Notes
==============================================================================
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
       | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
       | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

Fortran | 648.exchange2_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

Base Compiler Invocation
C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64

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### Base Portability Flags (Continued)

- 623.xalanbmk_s: -DSPEC_LP64 -DSPEC_LINUX
- 625.x264_s: -DSPEC_LP64
- 631.deepsjeng_s: -DSPEC_LP64
- 641.leela_s: -DSPEC_LP64
- 648.exchange2_s: -DSPEC_LP64
- 657.xz_s: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- -L/usr/local/je5.0.1-64/lib -ljemalloc

**C++ benchmarks:**
- -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
- -qopt-mem-layout-trans=4
- -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
- -lqkmalloc

**Fortran benchmarks:**
- -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs

### Peak Compiler Invocation

**C benchmarks:**
- icc -m64 -std=c11

**C++ benchmarks:**
- icpc -m64

**Fortran benchmarks:**
- ifort -m64

### Peak Portability Flags

Same as Base Portability Flags
Supermicro

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Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Supermicro
SuperWorkstation 5039A-i (X11SRA , Intel Xeon W-2295)

SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 12.2

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Sep-2019
Hardware Availability: Oct-2019
Software Availability: Aug-2019

Peak Optimization Flags (Continued)

Fortran benchmarks (continued):
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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