Dell Inc.  

PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)  

---

**SPEC CPU®2017 Floating Point Rate Result**

**Dell Inc.**

**SPECrate®2017_fp_base = 119**  
**SPECrate®2017_fp_peak = 122**

---

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Aug-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** May-2019

---

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base (119)</th>
<th>SPECrate®2017_fp_peak (122)</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>256</td>
</tr>
<tr>
<td>507.caactuBSSN_r</td>
<td>237</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>205</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>182</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>159</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>136</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>113</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>90</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>67</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>314</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>224</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>82.8</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>50.9</td>
</tr>
</tbody>
</table>

---

**Hardware**

- **CPU Name:** Intel Xeon Gold 6212U  
- **Max MHz:** 3900  
- **Nominal:** 2400  
- **Enabled:** 24 cores, 1 chip, 2 threads/core  
- **Orderable:** 1 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 35.75 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 192 GB (6 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 480 GB SATA SSD  
- **Other:** None

**Software**

- **OS:** Ubuntu 18.04.2 LTS  
- **Compiler:** C/C++; Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux  
- **Fortran:** Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux  
- **Parallel:** No  
- **Firmware:** Version 2.3.1 released May-2019  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None  
- **Power Management:** --

---

**Copies (122)**

<table>
<thead>
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</tr>
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</tr>
<tr>
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</tr>
<tr>
<td>508.namd_r</td>
</tr>
<tr>
<td>510.parest_r</td>
</tr>
<tr>
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</tr>
<tr>
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</tr>
<tr>
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<tr>
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**spec**

**Standard Performance Evaluation Corporation (info@spec.org)  https://www.spec.org/
Dell Inc.
PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)

SPECrate®2017_fp_base = 119
SPECrate®2017_fp_peak = 122

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>48</td>
<td>1866</td>
<td>258</td>
<td>1882</td>
<td>256</td>
<td>1885</td>
<td>255</td>
<td>48</td>
<td>1874</td>
<td>257</td>
<td>1884</td>
<td>256</td>
<td>1862</td>
<td>259</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>48</td>
<td>578</td>
<td>105</td>
<td>579</td>
<td>105</td>
<td>582</td>
<td>104</td>
<td>48</td>
<td>581</td>
<td>105</td>
<td>592</td>
<td>103</td>
<td>580</td>
<td>105</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>48</td>
<td>488</td>
<td>93.4</td>
<td>486</td>
<td>93.7</td>
<td>488</td>
<td>93.5</td>
<td>48</td>
<td>480</td>
<td>94.9</td>
<td>481</td>
<td>94.8</td>
<td>481</td>
<td>94.8</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>48</td>
<td>1909</td>
<td>65.8</td>
<td>1909</td>
<td>65.8</td>
<td>1912</td>
<td>65.7</td>
<td>48</td>
<td>1910</td>
<td>65.7</td>
<td>1913</td>
<td>65.6</td>
<td>1904</td>
<td>65.9</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>48</td>
<td>775</td>
<td>145</td>
<td>775</td>
<td>145</td>
<td>779</td>
<td>144</td>
<td>48</td>
<td>646</td>
<td>174</td>
<td>645</td>
<td>174</td>
<td>649</td>
<td>173</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>48</td>
<td>838</td>
<td>60.4</td>
<td>839</td>
<td>60.3</td>
<td>839</td>
<td>60.3</td>
<td>48</td>
<td>821</td>
<td>61.6</td>
<td>820</td>
<td>61.7</td>
<td>819</td>
<td>61.8</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>48</td>
<td>934</td>
<td>115</td>
<td>932</td>
<td>115</td>
<td>929</td>
<td>116</td>
<td>48</td>
<td>913</td>
<td>118</td>
<td>920</td>
<td>117</td>
<td>911</td>
<td>118</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>48</td>
<td>532</td>
<td>137</td>
<td>532</td>
<td>137</td>
<td>532</td>
<td>137</td>
<td>48</td>
<td>532</td>
<td>137</td>
<td>533</td>
<td>137</td>
<td>532</td>
<td>137</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>48</td>
<td>566</td>
<td>148</td>
<td>568</td>
<td>148</td>
<td>569</td>
<td>148</td>
<td>48</td>
<td>558</td>
<td>151</td>
<td>557</td>
<td>151</td>
<td>560</td>
<td>150</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>48</td>
<td>379</td>
<td>315</td>
<td>380</td>
<td>314</td>
<td>380</td>
<td>314</td>
<td>48</td>
<td>380</td>
<td>314</td>
<td>380</td>
<td>314</td>
<td>378</td>
<td>315</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>48</td>
<td>361</td>
<td>224</td>
<td>362</td>
<td>223</td>
<td>360</td>
<td>225</td>
<td>48</td>
<td>357</td>
<td>227</td>
<td>357</td>
<td>226</td>
<td>362</td>
<td>223</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>48</td>
<td>2262</td>
<td>82.7</td>
<td>2257</td>
<td>82.9</td>
<td>2258</td>
<td>82.8</td>
<td>48</td>
<td>2262</td>
<td>82.7</td>
<td>2261</td>
<td>82.7</td>
<td>2254</td>
<td>83.0</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>48</td>
<td>1498</td>
<td>50.9</td>
<td>1500</td>
<td>50.9</td>
<td>1502</td>
<td>50.8</td>
<td>48</td>
<td>1475</td>
<td>51.7</td>
<td>1475</td>
<td>51.7</td>
<td>1480</td>
<td>51.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/jes5.0.1-32:/home/cpu2017/jes5.0.1-64"
OMP_STACKSIZE = "192M"
Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation

(Continued on next page)
Dell Inc.

PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)

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<td>Hardware Availability: Apr-2019</td>
</tr>
</tbody>
</table>

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

**General Notes (Continued)**

Filesystem page cache synced and cleared with:
```
sync; echo 3>/proc/sys/vm/drop_caches
```

**Platform Notes**

BIOS settings:
- ADDDC setting disabled
- Sub NUMA Cluster disabled
- Virtualization Technology disabled
- DCU Streamer Prefetcher disabled
- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- Logical Processor enabled
- CPU Interconnect Bus Link Power Management disabled
- PCI ASPM L1 Link Power Management disabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) Gold 6212U CPU @ 2.40GHz
  1 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
```

From lscpu:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per core: 24
```

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

Dell Inc.

PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)

SPECrate®2017_fp_base = 119
SPECrate®2017_fp_peak = 122

Copyright 2017-2019 Standard Performance Evaluation Corporation

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Aug-2019
Hardware Availability: Apr-2019
Software Availability: May-2019

Platform Notes (Continued)

Socket(s): 1
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6212U CPU @ 2.40GHz
Stepping: 6
CPU MHz: 2900.828
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmrperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrh pdcm pcid dca seqs4_1 seqs4_2 x2apic movbe popcnt aes xsave f16c rdrand
lahf_lm abm 3dnowprefetch cpuid_fault epb cat _1 cd_ cd _1 inpcl _1 intel _p _pin
ssbd mba ibrs ibpb stibp ibrs _enhanced tpr _shadow vmni flexpriority ept vpid
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rd t _a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsavesopt xsavec xsavec xsave c q _l _c c q _m b _t _o _t _a _s c q _m b _l _o_c_a l
dtherm ida arat pin pts pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46
node 0 size: 95144 MB
node 0 free: 94251 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47
node 1 size: 96741 MB
node 1 free: 95754 MB
node distances:
node 0 1
0: 10 11
1: 11 10

(Continued on next page)
Dell Inc.  
PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)  

SPEC CPU®2017 Floating Point Rate Result

SPECrate®2017_fp_base = 119
SPECrate®2017_fp_peak = 122

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Aug-2019
Hardware Availability: Apr-2019
Software Availability: May-2019

Platform Notes (Continued)

From /proc/meminfo
MemTotal: 196490888 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
NAME="Ubuntu"
VERSION="18.04.2 LTS (Bionic Beaver)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 18.04.2 LTS"
VERSION_ID="18.04"
HOME_URL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 3 Aug 13 17:12

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 439G 31G 386G 8% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Dell Inc. 2.3.1 05/02/2019
Memory:
  3x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
  3x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
  10x Not Specified Not Specified

(End of data from sysinfo program)
Dell Inc.
PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)

SPECRate®2017_fp_base = 119
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Compiler Version Notes

==============================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
| 544.nab_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
| 554.roms_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
(Continued on next page)
Dell Inc.

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**Compiler Version Notes (Continued)**

```
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

```
-----------------------------------------------------------------------------
Fortran, C         | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
-----------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

**Base Compiler Invocation**

C benchmarks:
```
icc -m64 -std=c11
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:
```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

**Base Portability Flags**

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
```

(Continued on next page)
### Base Portability Flags (Continued)

- 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=4

**C++ benchmarks:**
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=4

**Fortran benchmarks:**
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=4
- -auto -nostandard-realloc-lhs
- -align array32byte

**Benchmarks using both Fortran and C:**
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=4
- -auto -nostandard-realloc-lhs
- -align array32byte

**Benchmarks using both C and C++:**
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=4

**Benchmarks using Fortran, C, and C++:**
- -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
- -qopt-mem-layout-trans=4
- -auto -nostandard-realloc-lhs
- -align array32byte

### Peak Compiler Invocation

**C benchmarks:**
- icc -m64 -std=c11
Dell Inc.

PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

544.nab_r: Same as 538.imagick_r

C++ benchmarks:
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

(Continued on next page)
Dell Inc. PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)

SPECrate®2017_fp_base = 119
SPECrate®2017_fp_peak = 122

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Aug-2019
Hardware Availability: Apr-2019
Software Availability: May-2019

Peak Optimization Flags (Continued)

Fortran benchmarks:
503.bwaves_r -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -03
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -03
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:

511.povray_r -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -03
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

526.blender_r -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

Dell Inc.  
PowerEdge M640 (Intel Xeon Gold 6212U, 2.40GHz)  

SPECraten®2017_fp_base = 119  
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Tested with SPEC CPU®2017 v1.0.5 on 2019-08-13 22:22:28-0400.  
Report generated on 2019-10-01 14:17:08 by CPU2017 PDF formatter v6255.  
Originally published on 2019-10-01.