## CPU2017 Integer Speed Result

**Dell Inc.**  
PowerEdge FC640 (Intel Xeon Gold 6262V, 1.90GHz)  

<table>
<thead>
<tr>
<th></th>
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<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6262V</td>
<td>OS:</td>
<td>Ubuntu 18.04.2 LTS</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Max MHz:</td>
<td>3600</td>
<td>Compiler:</td>
<td>C/C++: Version 19.0.4.227 of Intel C/C++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Nominal:</td>
<td>1900</td>
<td>Compiler Build:</td>
<td>20190416 for Linux;</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Enabled:</td>
<td>48 cores, 2 chips</td>
<td>Firmware:</td>
<td>Version 19.0.4.227 of Intel Fortran</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>Orderable:</td>
<td>1,2 chips</td>
<td>Compiler Build:</td>
<td>20190416 for Linux</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I+ 32 KB D on chip per core</td>
<td>Parallel:</td>
<td>Yes</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
<td>Firmware:</td>
<td>Version 2.2.11 released Jun-2019</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>L3:</td>
<td>33 MB I+D on chip per chip</td>
<td>File System:</td>
<td>ext4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
<td>System State:</td>
<td>Run level 5 (multi-user)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)</td>
<td>Base Pointers:</td>
<td>64-bit</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 480 GB SATA SSD</td>
<td>Peak Pointers:</td>
<td>64-bit</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<tr>
<td>Other:</td>
<td>None</td>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
<td></td>
<td></td>
<td></td>
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</tbody>
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**SPECspeed2017_int_base = 9.35**  
**SPECspeed2017_int_peak = 9.56**

### Hardware

<table>
<thead>
<tr>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>Threads</td>
</tr>
<tr>
<td>600.perlbench_s</td>
</tr>
<tr>
<td>602.gcc_s</td>
</tr>
<tr>
<td>605.mcf_s</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
</tr>
<tr>
<td>625.x264_s</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
</tr>
<tr>
<td>641.leela_s</td>
</tr>
<tr>
<td>648.exchange2_s</td>
</tr>
<tr>
<td>657.xz_s</td>
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<tr>
<td>Nominal:</td>
</tr>
<tr>
<td>Enabled:</td>
</tr>
<tr>
<td>Orderable:</td>
</tr>
<tr>
<td>Cache L1:</td>
</tr>
<tr>
<td>L2:</td>
</tr>
<tr>
<td>L3:</td>
</tr>
<tr>
<td>Other:</td>
</tr>
<tr>
<td>Memory:</td>
</tr>
<tr>
<td>Storage:</td>
</tr>
<tr>
<td>Other:</td>
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</table>

### Test Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>7.20</td>
<td>9.56</td>
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<tr>
<td>gcc</td>
<td>8.88</td>
<td>9.21</td>
</tr>
<tr>
<td>mcf</td>
<td>11.4</td>
<td>11.2</td>
</tr>
<tr>
<td>omnetpp</td>
<td>8.06</td>
<td>8.23</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>11.5</td>
<td>11.5</td>
</tr>
<tr>
<td>x264</td>
<td>12.8</td>
<td>12.7</td>
</tr>
<tr>
<td>deepsjeng</td>
<td>5.12</td>
<td>8.12</td>
</tr>
<tr>
<td>leela</td>
<td>4.42</td>
<td>4.41</td>
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<tr>
<td>exchange2</td>
<td>15.3</td>
<td>15.4</td>
</tr>
<tr>
<td>xz</td>
<td>20.0</td>
<td>20.3</td>
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---

Dell Inc.  
PowerEdge FC640 (Intel Xeon Gold 6262V, 1.90GHz)  

**CPU2017 License: 55**  
**Software Availability: Jun-2019**  
**Test Date: Jun-2019**  
**Hardware Availability: Apr-2019**
SPEC CPU2017 Integer Speed Result

Dell Inc.
PowerEdge FC640 (Intel Xeon Gold 6262V, 1.90GHz)

SPECspeed2017_int_base = 9.35
SPECspeed2017_int_peak = 9.56

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>48</td>
<td>289</td>
<td>6.14</td>
<td>289</td>
<td>6.14</td>
<td>283</td>
<td>6.27</td>
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<tr>
<td>602.gcc_s</td>
<td>48</td>
<td>435</td>
<td>9.16</td>
<td>449</td>
<td>8.87</td>
<td>448</td>
<td>8.88</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>48</td>
<td>416</td>
<td>11.4</td>
<td>419</td>
<td>11.3</td>
<td>415</td>
<td>11.4</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>48</td>
<td>196</td>
<td>8.32</td>
<td>206</td>
<td>7.91</td>
<td>202</td>
<td>8.06</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>48</td>
<td>123</td>
<td>11.5</td>
<td>123</td>
<td>11.5</td>
<td>123</td>
<td>11.6</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>48</td>
<td>138</td>
<td>12.8</td>
<td>138</td>
<td>12.8</td>
<td>138</td>
<td>12.8</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>48</td>
<td>280</td>
<td>15.3</td>
<td>280</td>
<td>15.3</td>
<td>280</td>
<td>15.3</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>48</td>
<td>386</td>
<td>4.42</td>
<td>387</td>
<td>4.41</td>
<td>386</td>
<td>4.42</td>
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<tr>
<td>648.exchange2_s</td>
<td>48</td>
<td>193</td>
<td>15.3</td>
<td>190</td>
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<td>192</td>
<td>15.3</td>
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<tr>
<td>657.xz_s</td>
<td>48</td>
<td>309</td>
<td>20.0</td>
<td>309</td>
<td>20.0</td>
<td>309</td>
<td>20.0</td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

(Continued on next page)
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Dell Inc.

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<tr>
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<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
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<td>Tested by:</td>
<td>Dell Inc.</td>
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<tr>
<td>SPECspeed2017_int_base</td>
<td>9.35</td>
</tr>
<tr>
<td>SPECspeed2017_int_peak</td>
<td>9.56</td>
</tr>
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<td>Test Date:</td>
<td>Jun-2019</td>
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<td>Hardware Availability:</td>
<td>Apr-2019</td>
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<tr>
<td>Software Availability:</td>
<td>Jun-2019</td>
</tr>
</tbody>
</table>

General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS settings:
ADDDC setting disabled
Sub NUMA Cluster enabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on intel-sut Mon Jul 22 19:10:48 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6262V CPU @ 1.90GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 1

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Dell Inc.

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Jun-2019
Tested by: Dell Inc.
Hardware Availability: Apr-2019
Software Availability: Jun-2019

Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6262V CPU @ 1.90GHz
Stepping: 7
CPU MHz: 2608.617
BogoMIPS: 3800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 33792K
NUMA node0 CPU(s):
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46
NUMA node0 CPU(s):
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47
Flags:
fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pse syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand
lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_ppi
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmmx flexpriority ept vpid
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ets invpcid rtm cmpxmp rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsavesopt xsaveopt xgetbv1 xsave esm cr3write svm cr4_legacy svm缧 avx512_vnni
dathg ida at pt pn pts ksu ospke avx512_vnni md_clear flush_lld arch_capabilities

/platforminfo cache data
    cache size: 33792 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46
node 0 size: 191912 MB
node 0 free: 191335 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47
node 1 size: 193509 MB
node 1 free: 193100 MB
node distances:
node 0 1
  0: 10 21
  1: 21 10

(Continued on next page)
Dell Inc.
PowerEdge FC640 (Intel Xeon Gold 6262V, 1.90GHz)

SPEC CPU2017 Integer Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

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SPECspeed2017_int_peak = 9.56

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Jun-2019
Hardware Availability: Apr-2019
Software Availability: Jun-2019

Platform Notes (Continued)

From /proc/meminfo
MemTotal: 394671668 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
NAME="Ubuntu"
VERSION="18.04.2 LTS (Bionic Beaver)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 18.04.2 LTS"
VERSION_ID="18.04"
HOME_URL="https://www.ubuntu.com/
SUPPORT_URL="https://help.ubuntu.com/

uname -a:
Linux intel-sut 4.15.0-54-generic #58-Ubuntu SMP Mon Jun 24 10:55:24 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 5 Jul 22 17:00

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 439G 35G 382G 9% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.2.11 06/14/2019
Memory:
3x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2400
9x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933, configured at 2400
4x Not Specified Not Specified

(Continued on next page)
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Test Date: Jun-2019
Hardware Availability: Apr-2019
Software Availability: Jun-2019

Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

FC  648.exchange2_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
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</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19 compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64 -lqkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs
## SPEC CPU2017 Integer Speed Result

**Dell Inc.**

PowerEdge FC640 (Intel Xeon Gold 6262V, 1.90GHz)  

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>9.35</td>
<td>9.56</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jun-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Jun-2019

## Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP -fno-strict-overflow -L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
SPEC CPU2017 Integer Speed Result

Dell Inc.
PowerEdge FC640 (Intel Xeon Gold 6262V, 1.90GHz)

SPECspeed2017_int_base = 9.35
SPECspeed2017_int_peak = 9.56

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Jun-2019
Hardware Availability: Apr-2019
Software Availability: Jun-2019

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s:
-Wez, -z, mundefs
-prof-gen(pass 1)
-prof-use(pass 2)
-ipo
-xCORE-AVX512
-03
-no-prec-div
-qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

623.xalancbmk_s:
-Wez, -z, mundefs
-xCORE-AVX512
-ipo
-03
-no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

Fortran benchmarks:

-xCORE-AVX512
-ipo
-03
-no-prec-div
-qopt-mem-layout-trans=4
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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