### SPEC® CPU2017 Integer Rate Result

**Supermicro**

SuperWorkstation 5039C-I (X11SCL-F, Intel Core i3-8350K)

<table>
<thead>
<tr>
<th>CPU2017 License: 001176</th>
<th>Test Date: May-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Supermicro</td>
<td>Hardware Availability: Nov-2018</td>
</tr>
<tr>
<td>Tested by: Supermicro</td>
<td>Software Availability: Nov-2018</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 26.3**

**SPECrate2017_int_peak = 27.1**

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: SUSE Linux Enterprise Server 12 SP3 (x86_64)</td>
<td>CPU Name: Intel Core i3-8350K</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 19.0.1.144 of Intel C/C++</td>
<td>Max MHz.: 4000</td>
</tr>
<tr>
<td>Compiler for Linux: Fortran: Version 19.0.1.144 of Intel Fortran</td>
<td>Nominal: 4000</td>
</tr>
<tr>
<td>Compiler for Linux</td>
<td>Enabled: 4 cores, 1 chip</td>
</tr>
<tr>
<td>Parallel: No</td>
<td>Orderable: 1 chip</td>
</tr>
<tr>
<td>Firmware: Version 1.0a released Feb-2019</td>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>File System: xfs</td>
<td>L2: 256 KB I+D on chip per core</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>L3: 8 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>Other: None</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
<td>Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
<td>Storage: 1 x 200 GB SATA III SSD</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate2017_int_base (26.3)</th>
<th>SPECrate2017_int_peak (27.1)</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>4</td>
<td>22.3</td>
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<tr>
<td>502.gcc_r</td>
<td>4</td>
<td>25.0</td>
</tr>
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<td>505.mcf_r</td>
<td>4</td>
<td>28.0</td>
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<tr>
<td>520.omnetpp_r</td>
<td>4</td>
<td>15.9</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>4</td>
<td>32.4</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>4</td>
<td>40.1</td>
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<td>531.deepsjeng_r</td>
<td>4</td>
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<tr>
<td>541.leela_r</td>
<td>4</td>
<td>31.4</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>4</td>
<td>49.8</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>4</td>
<td>13.9</td>
</tr>
</tbody>
</table>

**Hardware**

- CPU Name: Intel Core i3-8350K
- Max MHz.: 4000
- Nominal: 4000
- Enabled: 4 cores, 1 chip
- Orderable: 1 chip
- Cache L1: 32 KB I + 32 KB D on chip per core
- L2: 256 KB I+D on chip per core
- L3: 8 MB I+D on chip per chip
- Other: None
- Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)
- Storage: 1 x 200 GB SATA III SSD
- Other: None
SPEC CPU2017 Integer Rate Result

Supermicro
SuperWorkstation 5039C-I (X11SCL-F, Intel Core i3-8350K)

SPECrate2017_int_base = 26.3
SPECrate2017_int_peak = 27.1

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
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<td>557.xz_r</td>
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<td>13.9</td>
<td>311</td>
<td>13.9</td>
<td>311</td>
<td>13.9</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Supermicro
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SPECrate2017_int_base = 26.3
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General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-65nv Tue May 28 02:31:03 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-8350K CPU @ 4.00GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i3-8350K CPU @ 4.00GHz
Stepping: 11
CPU MHz: 4000.206
CPU max MHz: 4000.0000
CPU min MHz: 800.0000
BogoMIPS: 8015.98
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K

(Continued on next page)
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CPU2017 License: 001176
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Test Date: May-2019
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SPEC CPU2017 Integer Rate Result
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Platform Notes (Continued)

L2 cache: 256K
L3 cache: 8192K
NUMA node0 CPU(s): 0–3
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmon perf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma
cx16 xptr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm
hwlp_notify hwlp_act_window hwlp_epp intel_pt rsb_ctxsw spec_ctrl retpoline kaiser
tpr_shadow vmni flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms
invpcid mpx rdseed adx smap clflushopt xsaveopt xsavec xgetbv

/proc/cpuinfo cache data
  cache size: 8192 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3
  node 0 size: 64333 MB
  node 0 free: 63598 MB
  node distances:
    node: 0
      0: 10

From /proc/meminfo
  MemTotal: 65877040 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 3
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"

(Continued on next page)
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: May-2019
Hardware Availability: Nov-2018
Software Availability: Nov-2018

Platform Notes (Continued)

uname -a:
    Linux linux-65nv 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 May 28 02:20

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 145G 14G 131G 10% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0a 02/14/2019
Memory:
    4x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  500.perlbench_r(base)  502.gcc_r(base)  505.mcf_r(base, peak)
    525.x264_r(base, peak)  557.xz_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
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CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro
Test Date: May-2019
Hardware Availability: Nov-2018
Software Availability: Nov-2018

 Compiler Version Notes (Continued)

CC 500.perlbench_r(peak)

Intel(R) C 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CXXC 523.xalancbmk_r(peak)

Intel(R) C++ 64 Compiler for applications running on IA-32, Version
19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CXXC 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

Intel(R) C++ 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC 548.exchange2_r(base, peak)

Intel(R) Fortran 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

 Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64
SPEC CPU2017 Integer Rate Result

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SuperWorkstation 5039C-I (X11SCL-F, Intel Core i3-8350K)

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CPU2017 License: 001176
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Test Sponsor: Supermicro
Tested by: Supermicro
Test Hardware Availability: Nov-2018
Software Availability: Nov-2018

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/Intel Compiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/Intel Compiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/Intel Compiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11


C++ benchmarks (except as noted below):
icpc -m64

(Continued on next page)
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Peak Compiler Invocation (Continued)

523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

(Continued on next page)
### SPEC CPU2017 Integer Rate Result

| Supermicro SuperWorkstation 5039C-I (X11SCL-F, Intel Core i3-8350K) | SPECrate2017_int_base = 26.3 |
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- **CPU2017 License:** 001176
- **Test Sponsor:** Supermicro
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- **Hardware Availability:** Nov-2018
- **Software Availability:** Nov-2018

#### Peak Optimization Flags (Continued)

**C++ benchmarks:**

- `557.xz_r`: Same as `505.mcf_r`

- **520.omnetpp_r**: `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64 -lqkmalloc`

- **523.xalancbmk_r**: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4 -L/usr/local/jemalloc/lib -ljemalloc`

- **531.deepsjeng_r**: Same as `520.omnetpp_r`

- **541.leela_r**: Same as `520.omnetpp_r`

**Fortran benchmarks:**


The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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