Lenovo Global Technology
ThinkSystem SR850
(2.50 GHz, Intel Xeon Gold 5215L)

SPECspeed2017_fp_base = 131
SPECspeed2017_fp_peak = Not Run

Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>40</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>40</td>
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<tr>
<td>619.lbm_s</td>
<td>40</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>40</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>40</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>40</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>40</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>40</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>40</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>40</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base (131)

Software
OS: SUSE Linux Enterprise Server 12 SP4 (x86_64)
Kernel 4.12.14-94.41-default
Compiler: C/C++: Version 19.0.1.144 of Intel C/C++
Compiler Build 20181018 for Linux;
Fortran: Version 19.0.1.144 of Intel Fortran
Compiler Build 20181018 for Linux
Parallel: Yes
Firmware: Lenovo BIOS Version TEE135T 2.10 released Mar-2019
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
SPECF floating point speed result

Lenovo Global Technology
ThinkSystem SR850
(2.50 GHz, Intel Xeon Gold 5215L)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

Results Table

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</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 131
SPECspeed2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Tmpfs filesystem can be set with:
   mount -t tmpfs -o size=800g tmpfs /home
Process tuning setting:
   echo 50000 > /proc/sys/kernel/sched_cfs_bandwidth_slice_us
   echo 240000000 > /proc/sys/kernel/sched_latency_ns
   echo 5000000 > /proc/sys/kernel/sched_migration_cost_ns
   echo 100000000 > /proc/sys/kernel/sched_min_granularity_ns
   echo 150000000 > /proc/sys/kernel/sched_wakeup_granularity_ns

General Notes

Environment variables sets by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
   sync; echo 3> /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Continued on next page
Lenovo Global Technology
ThinkSystem SR850
(2.50 GHz, Intel Xeon Gold 5215L)

SPECspeed2017_fp_base = 131
SPECspeed2017_fp_peak = Not Run

General Notes (Continued)
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-states set to Legacy
Trusted Execution Technology set to Enable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9b6a9d6f8299c33d61f64985e45859ea9
running on linux-hxhl Thu May 9 13:05:20 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz
  4 "physical id"s (chips)
  80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  physical 2: cores 0 1 2 3 4 8 9 10 11 12
  physical 3: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 4
NUMA node(s): 4
Vendor ID: GenuineIntel

(Continued on next page)
**Platform Notes (Continued)**

CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Gold 5215L CPU @ 2.50GHz  
Stepping: 6  

CPU MHz: 2500.000  
CPU max MHz: 3400.0000  
CPU min MHz: 1000.0000  
BogoMIPS: 5000.00  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 14080K  

NUMA node0 CPU(s): 0-9,40-49  
NUMA node1 CPU(s): 10-19,50-59  
NUMA node2 CPU(s): 20-29,60-69  
NUMA node3 CPU(s): 30-39,70-79  

Flags: fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewModel srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouse x87 bp bx 2sxi efer pm mtrr pge mca cmov pat pse36 clflush dts mfw pge mca semm srvtct msr tsc msr viewm x2apic msrmerce pxr mt aperf fpu vme de pm xi apic pk mm extd pse 36ouser clflushopt clwb intel_pt avx512bw avx512vl xsaveopt xsaves xsave_c3 cqm_mbb_reclaim cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 40 41 42 43 44 45 46 47 48 49
node 0 size: 386666 MB
node 0 free: 374047 MB
node 1 cpus: 10 11 12 13 14 15 16 17 18 19 50 51 52 53 54 55 56 57 58 59
node 1 size: 387057 MB
node 1 free: 388631 MB
node 2 cpus: 20 21 22 23 24 25 26 27 28 29 60 61 62 63 64 65 66 67 68 69
node 2 size: 387057 MB
node 2 free: 386140 MB
node 3 cpus: 30 31 32 33 34 35 36 37 38 39 70 71 72 73 74 75 76 77 78 79
node 3 size: 387026 MB
node 3 free: 386764 MB

(Continued on next page)
Platform Notes (Continued)

node distances:
node     0   1   2   3
0:    10  21  21  31
1:    21  10  31  21
2:    21  31  10  21
3:    31  21  21  10

From /proc/meminfo
     MemTotal:       1584955332 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
     SuSE-release:
           SUSE Linux Enterprise Server 12 (x86_64)
           VERSION = 12
           PATCHLEVEL = 4

     os-release:
           NAME="SLES"
           VERSION="12-SP4"
           VERSION_ID="12.4"
           PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
           ID="sles"
           ANSI_COLOR="0;32"
           CPE_NAME="cpe:/o:suse:sles:12:sp4"

     uname -a:
           x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

     CVE-2017-5754 (Meltdown):          Not affected
     CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
     CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation,
                                           IBPB, IBRS_FW

run-level 3 May 9 10:18

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1

Filesystem     Type Size  Used Avail Use% Mounted on
   tmpfs        tmpfs  800G 8.3G  792G   2% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR850
(2.50 GHz, Intel Xeon Gold 5215L)

SPECspeed2017_fp_base = 131
SPECspeed2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: May-2019
Tested by: Lenovo Global Technology
Hardware Availability: Apr-2019
Software Availability: Dec-2018

Platform Notes (Continued)

hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[TEE135T-2.10]- 03/21/2019
Memory:
48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666
(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
FC  607.cactuBSSN_s(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
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FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
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SPECspeed2017_fp_base = 131
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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
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Compiler Version Notes (Continued)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation
C benchmarks:
  icc -m64 -std=c11

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
  icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
  -assume byte_recl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
  -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR850
(2.50 GHz, Intel Xeon Gold 5215L)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>131</th>
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<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>Not Run</td>
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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** May-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Dec-2018

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**Base Optimization Flags (Continued)**

Fortran benchmarks (continued):
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

---

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml

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