# Lenovo Global Technology

## ThinkSystem ST550

*2.20 GHz, Intel Xeon Platinum 8253*

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Apr-2019  
**Hardware Availability:** Apr-2019  
**Software Availability:** Nov-2018

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### SPECrate2017_fp_base = 170

### SPECrate2017_fp_peak = Not Run

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<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>170</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

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### Hardware

**CPU Name:** Intel Xeon Platinum 8253  
**Max MHz.:** 3000  
**Nominal:** 2200  
**Enabled:** 32 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 22 MB I+D on chip per chip  
**Other:** None  
**Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
**Storage:** 1 x 480 GB SATA SSD  
**Other:** None

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### Software

**OS:** SUSE Linux Enterprise Server 15 (x86_64)  
**Kernel:** 4.12.14-25.13-default  
**Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++  
**Compiler Build:** 20181018 for Linux;  
**Fortran:** Version 19.0.1.144 of Intel Fortran  
**Compiler Build:** 20181018 for Linux  
**Parallel:** No  
**Firmware:** Lenovo BIOS Version O0E135M 2.10 released Jan-2019  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** Not Applicable  
**Other:** None
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SPECrate2017_fp_base = 170
SPECrate2017_fp_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>64</td>
<td>1324</td>
<td>485</td>
<td>1325</td>
<td>485</td>
<td>1325</td>
<td>485</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>64</td>
<td>575</td>
<td>141</td>
<td>575</td>
<td>141</td>
<td>575</td>
<td>141</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>64</td>
<td>519</td>
<td>117</td>
<td>521</td>
<td>117</td>
<td>518</td>
<td>117</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>64</td>
<td>1488</td>
<td>113</td>
<td>1491</td>
<td>112</td>
<td>1494</td>
<td>112</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>64</td>
<td>819</td>
<td>182</td>
<td>808</td>
<td>185</td>
<td>822</td>
<td>182</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>64</td>
<td>634</td>
<td>106</td>
<td>633</td>
<td>107</td>
<td>634</td>
<td>106</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>64</td>
<td>750</td>
<td>191</td>
<td>739</td>
<td>194</td>
<td>752</td>
<td>191</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>64</td>
<td>571</td>
<td>171</td>
<td>570</td>
<td>171</td>
<td>571</td>
<td>171</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>64</td>
<td>693</td>
<td>161</td>
<td>691</td>
<td>162</td>
<td>692</td>
<td>162</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>64</td>
<td>484</td>
<td>329</td>
<td>486</td>
<td>327</td>
<td>484</td>
<td>329</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>64</td>
<td>428</td>
<td>252</td>
<td>430</td>
<td>250</td>
<td>424</td>
<td>254</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>64</td>
<td>1642</td>
<td>152</td>
<td>1645</td>
<td>152</td>
<td>1649</td>
<td>151</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>64</td>
<td>1179</td>
<td>86.2</td>
<td>1175</td>
<td>86.6</td>
<td>1183</td>
<td>86.0</td>
</tr>
</tbody>
</table>

SPECrate2017_fp_base = 170
SPECrate2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
   sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
   numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
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General Notes (Continued)
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes
BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
LLC dead line alloc set to Disable
SNC set to Enable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-pork Fri Apr 19 03:01:08 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8253 CPU @ 2.20GHz
  2 "physical id"s (chips)
  64 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 16
    siblings : 32
    physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
    physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 64
  On-line CPU(s) list: 0-63
  Thread(s) per core: 2
  Core(s) per socket: 16
  Socket(s): 2
  NUMA node(s): 4
  Vendor ID: GenuineIntel

(Continued on next page)
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thinksystem st550
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Platform Notes (Continued)

node distances:
node  0  1  2  3
  0:  10  11  21  21
  1:  11  10  21  21
  2:  21  21  10  11
  3:  21  21  11  10

From /proc/meminfo
  MemTotal:       395877688 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release*/etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15"
    VERSION_ID="15"
    PRETTY_NAME="SUSE Linux Enterprise Server 15"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:15"

  uname -a:
      x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

  CVE-2017-5754 (Meltdown):          Not affected
  CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
  CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Apr 19 02:44

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sdc3     xfs   445G  43G  403G  10% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Lenovo -[00E135M-2.10]- 01/16/2019
  Memory:
    12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(Continued on next page)
Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
   Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
CC  511.povray_r(base) 526.blender_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
   Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================
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Compiler Version Notes (Continued)

FC 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
Intel (R) Fortran Intel (R) 64 Compiler for applications running on Intel (R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

CC 521.wrf_r(base) 527.cam4_r(base)
Intel (R) Fortran Intel (R) 64 Compiler for applications running on Intel (R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

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Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactusBSSN_r: -DSPEC_LP64
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**Base Portability Flags (Continued)**

508.namd_r: -DSPEC_LP64  
510.parest_r: -DSPEC_LP64  
511.povray_r: -DSPEC_LP64  
519.lbm_r: -DSPEC_LP64  
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char  
527.cam4_r: -DSPEC_LP64 –DSPEC_CASE_FLAG  
538.imagick_r: -DSPEC_LP64  
544.nab_r: -DSPEC_LP64  
549.fotonik3d_r: -DSPEC_LP64  
554.roms_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs -align array32byte
# SPEC CPU2017 Floating Point Rate Result

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<table>
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<th>Hardware Availability</th>
<th>Software Availability</th>
<th>Test Date</th>
<th>Test Sponsor</th>
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<th>Hardware Availability</th>
<th>Software Availability</th>
<th>Test Date</th>
</tr>
</thead>
</table>

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Tested with SPEC CPU2017 v1.0.5 on 2019-04-18 15:01:07-0400.  