Huawei

Huawei 2288 V5 (Intel Xeon Platinum 8253)

<table>
<thead>
<tr>
<th></th>
<th>SPECspeed2017_fp_base = 108</th>
<th>SPECspeed2017_fp_peak = 111</th>
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<td>3175</td>
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<td>Huawei</td>
<td></td>
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<td>Huawei</td>
<td></td>
</tr>
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<td>Mar-2019</td>
<td></td>
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<td>Hardware Availability:</td>
<td>Apr-2019</td>
<td></td>
</tr>
<tr>
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<td>Dec-2018</td>
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<tr>
<td>603.bwaves_s 32</td>
<td>120</td>
<td>144</td>
</tr>
<tr>
<td>607.cactuBSSN_s 32</td>
<td>88.9</td>
<td>106</td>
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<tr>
<td>619.lbm_s 32</td>
<td>88.9</td>
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</tr>
<tr>
<td>621.wrf_s 32</td>
<td>97.4</td>
<td></td>
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<tr>
<td>627.cam4_s 32</td>
<td>63.5</td>
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<tr>
<td>628.pop2_s 32</td>
<td>63.6</td>
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<tr>
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<td></td>
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<td>644.nab_s 32</td>
<td>83.2</td>
<td></td>
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<tr>
<td>649.fotonik3d_s 32</td>
<td>77.1</td>
<td></td>
</tr>
<tr>
<td>654.roms_s 32</td>
<td>77.7</td>
<td></td>
</tr>
</tbody>
</table>

**Hardware**
- **CPU Name:** Intel Xeon Platinum 8253
- **Max MHz.:** 3000
- **Nominal:** 2200
- **Enabled:** 32 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 22 MB I+D on chip per chip
- **Other:** None
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2933Y-R)
- **Storage:** 1 x 1200 GB SAS, 10000 RPM
- **Other:** None

**Software**
- **OS:** SUSE Linux Enterprise Server 12 SP4 (x86_64)
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux;
  Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux
- **Parallel:** Yes
- **Firmware:** Version 6.29 Released Jan-2019
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
 SPEC CPU2017 Floating Point Speed Result

Huawei

Huawei 2288 V5 (Intel Xeon Platinum 8253)

SPECspeed2017_fp_base = 108
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>88.8</td>
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<td>621.wrf_s</td>
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<td>97.8</td>
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<td>63.8</td>
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<td>57.8</td>
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<tr>
<td>649.fotonik3d_s</td>
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<td>119</td>
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</table>

SPECspeed2017_fp_base = 108
SPECspeed2017_fp_peak = 111

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/spec2017/lib/ia32:/spec2017/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Load Balance
Hyper-Threading Set to Disable

(Continued on next page)
Huawei 2288 V5 (Intel Xeon Platinum 8253)

<table>
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<td>111</td>
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CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Mar-2019
Hardware Availability: Apr-2019
Tested by: Huawei
Software Availability: Dec-2018

Platform Notes (Continued)

XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e456ad54c135fd618bccc091c0f
running on linux-0o4j Mon Mar 4 15:46:16 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8253 CPU @ 2.20GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Platinum 8253 CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2200.000
CPU max MHz: 3000.0000
CPU min MHz: 1000.0000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov

(Continued on next page)
Huawei

Huawei 2288 V5 (Intel Xeon Platinum 8253)

**SPECspeed2017_fp_base = 108**

**SPECspeed2017_fp_peak = 111**

**Platform Notes (Continued)**

```
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnumi flexpriority ept vpid
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f
avx512dq rdseed adx clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt x saves xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities
```

/proc/cpuinfo cache data
- cache size : 22528 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
- available: 2 nodes (0-1)
  - node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  - node 0 size: 95136 MB
  - node 0 free: 94124 MB
  - node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
  - node 1 size: 96528 MB
  - node 1 free: 95673 MB

node distances:
- node 0 1
  - 0: 10 21
  - 1: 21 10

From /proc/meminfo
- MemTotal: 196265040 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- SuSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 4
  - # This file is deprecated and will be removed in a future service pack or release.
  - # Please check /etc/os-release for details about this release.
- os-release:
  - NAME="SLES"
  - VERSION="12-SP4"
  - VERSION_ID="12.4"
  - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
  - ID="sles"
  - ANSI_COLOR="0;32"
```
(Continued on next page)
# SPEC CPU2017 Floating Point Speed Result

## Huawei

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### SPECspeed2017_fp_base = 108

### SPECspeed2017_fp_peak = 111

| **Test Date:** Mar-2019               |
| **Hardware Availability:** Apr-2019  |
| **Software Availability:** Dec-2018  |

## Platform Notes (Continued)

```
cpe_NAME="cpe:/o:suse:sles:12:sp4"
```

```
uname -a:
  x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 4 09:56
```

```
SPEC is set to: /spec2017
```

### Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**BIOS INSYDE Corp. 6.29 2019/01/22**

**Memory:**
- 4x NO DIMM NO DIMM
- 12x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

**(End of data from sysinfo program)**

## Compiler Version Notes

```bash
== CC  619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak) ==
```

```
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```bash
== FC  607.cactuBSSN_s(base, peak) ==
```

```
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

(Continued on next page)
Huawei

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Test Date: Mar-2019
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Compiler Version Notes (Continued)

```
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base, peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
FC  603.bwaves_s(peak) 649.fotonik3d_s(peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

```
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
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```
CC  621.wrf_s(peak) 628.pop2_s(peak)
```

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
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Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

**Base Compiler Invocation**

C benchmarks:
```
icc -m64 -std=c11
```

Fortran benchmarks:
```
ifort -m64
```

(Continued on next page)
Huawei 2288 V5 (Intel Xeon Platinum 8253)

- SPECspeed2017_fp_base = 108
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CPU2017 License: 3175
Test Sponsor: Huawei
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Base Compiler Invocation (Continued)

- ifort -m64 icc -m64 -std=c11
- icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64

Base Optimization Flags

- C benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
- Fortran benchmarks:
  -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
  -nostandard-realloc-lhs
- Benchmarks using both Fortran and C:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
  -nostandard-realloc-lhs
- Benchmarks using Fortran, C, and C++:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
  -nostandard-realloc-lhs
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Test Sponsor: Huawei
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Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4
-qopenmp -nstandard-realloc-lhs

649.fotonik3d_s: Same as 603.bwaves_s

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nstandard-realloc-lhs

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp

(Continued on next page)
## Huawei

**Huawei 2288 V5 (Intel Xeon Platinum 8253)**

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### CPU2017 License: 3175

**Test Sponsor:** Huawei  
**Tested by:** Huawei

- **Test Date:** Mar-2019  
- **Hardware Availability:** Apr-2019  
- **Software Availability:** Dec-2018

---

### Peak Optimization Flags (Continued)

627.cam4_s (continued):
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

---

The flags files that were used to format this result can be browsed at:


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You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2019-03-04 02:46:15-0500.  