SPEC® CPU2017 Floating Point Rate Result

Fujitsu
PRIMERGY RX2530 M5, Intel Xeon Gold 6244, 3.60GHz

SPECrater2017_fp_base = 150
SPECrater2017_fp_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

CPU Name: Intel Xeon Gold 6244
Max MHz.: 4400
Nominal: 3600
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 24.75 MB I+D on chip per chip
Other: None
Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x SATA M.2 SSD, 240GB
Other: None

OS: SUSE Linux Enterprise Server 15
Compiler: C/C++: Version 19.0.0.117 of Intel C/C++ Compiler for Linux;
Fortran: Version 19.0.0.117 of Intel Fortran Compiler for Linux
Parallel: No
Firmware: Fujitsu BIOS Version V5.0.0.14 R1.8.0 for D3383-B1x. Released Jun-2019 tested as V5.0.0.14 R1.2.0 for D3383-B1x Feb-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>32</td>
<td>690</td>
<td>465</td>
<td>691</td>
<td>464</td>
<td>690</td>
<td>465</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>32</td>
<td>408</td>
<td>99.2</td>
<td>408</td>
<td>99.2</td>
<td>408</td>
<td>99.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>32</td>
<td>316</td>
<td>96.1</td>
<td>316</td>
<td>96.1</td>
<td>316</td>
<td>96.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>32</td>
<td>775</td>
<td>108</td>
<td>775</td>
<td>108</td>
<td>776</td>
<td>108</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>32</td>
<td>495</td>
<td>151</td>
<td>494</td>
<td>151</td>
<td>494</td>
<td>151</td>
<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>32</td>
<td>393</td>
<td>85.8</td>
<td>393</td>
<td>85.7</td>
<td>393</td>
<td>85.8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>32</td>
<td>417</td>
<td>172</td>
<td>421</td>
<td>170</td>
<td>414</td>
<td>173</td>
<td></td>
<td></td>
<td></td>
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</tr>
<tr>
<td>526.blender_r</td>
<td>32</td>
<td>341</td>
<td>143</td>
<td>341</td>
<td>143</td>
<td>342</td>
<td>142</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>32</td>
<td>376</td>
<td>149</td>
<td>374</td>
<td>150</td>
<td>370</td>
<td>151</td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>32</td>
<td>256</td>
<td>110</td>
<td>255</td>
<td>111</td>
<td>257</td>
<td>109</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>32</td>
<td>239</td>
<td>225</td>
<td>244</td>
<td>221</td>
<td>239</td>
<td>226</td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>32</td>
<td>920</td>
<td>135</td>
<td>924</td>
<td>135</td>
<td>927</td>
<td>135</td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>32</td>
<td>570</td>
<td>89.3</td>
<td>561</td>
<td>90.7</td>
<td>567</td>
<td>89.7</td>
<td></td>
<td></td>
<td></td>
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</tbody>
</table>

**SPECrate2017_fp_base** = 150
**SPECrate2017_fp_peak** = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with: nohz_full=1-111
Process tuning settings:
```
  echo 10000000 > /proc/sys/kernel/sched_min_granularity_ns
```

## General Notes

Environment variables set by runcpu before the start of the run:
```
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-1.0.5/icc19-lib/intel64"
```
Binaries compiled on a system with 2x Intel Xeon E5-2667 v2 CPU + 64GB RAM memory using SUSE Linux Enterprise Server 12 SP2
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
```
sync; echo 3 > /proc/sys/vm/drop_caches
```
runcpu command invoked through numactl i.e.:
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PRIMERGY RX2530 M5, Intel Xeon Gold 6244, 3.60GHz

| SPECrate2017_fp_base = | 150 |
| SPECrate2017_fp_peak = | Not Run |

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Mar-2019
Hardware Availability: May-2019
Software Availability: Feb-2019

General Notes (Continued)

numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Patrol Scrub = Disabled
WR CRC feature Control = Disabled
Fan Control = Full
Sysinfo program /home/Benchmark/speccpu2017-1.0.5/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on RX2530M5 Fri Mar 29 20:16:10 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 3 4 8 17 18 24 25 27
physical 1: cores 2 4 9 18 19 24 25 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85

(Continued on next page)
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Tested by: Fujitsu

Test Date: Mar-2019
Hardware Availability: May-2019
Software Availability: Feb-2019

Platform Notes (Continued)

Model name: Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
Stepping: 6
CPU MHz: 3600.000
CPU max MHz: 4400.0000
CPU min MHz: 1200.0000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,3,5,6,18,19,21,22
NUMA node1 CPU(s): 1,2,4,7,16,17,20,23
NUMA node2 CPU(s): 8,10,13,14,24,26,29,30
NUMA node3 CPU(s): 9,11,12,15,25,27,28,31

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority
ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmp mpx rdt_a
avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni
flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 3 5 6 18 19 21 22
  node 0 size: 191940 MB
  node 0 free: 191589 MB
  node 1 cpus: 1 2 4 7 16 17 20 23
  node 1 size: 193534 MB
  node 1 free: 193286 MB
  node 2 cpus: 8 10 13 14 24 26 29 30
  node 2 size: 193534 MB
  node 2 free: 193323 MB
  node 3 cpus: 9 11 12 15 25 27 28 31
  node 3 size: 193322 MB
  node 3 free: 193111 MB
  node distances:

(Continued on next page)
Platform Notes (Continued)

<table>
<thead>
<tr>
<th>node</th>
<th>0</th>
<th>1</th>
<th>2</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>0:</td>
<td>10</td>
<td>11</td>
<td>21</td>
<td>21</td>
</tr>
<tr>
<td>1:</td>
<td>11</td>
<td>10</td>
<td>21</td>
<td>21</td>
</tr>
<tr>
<td>2:</td>
<td>21</td>
<td>21</td>
<td>10</td>
<td>11</td>
</tr>
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<td>3:</td>
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<td>21</td>
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</tr>
</tbody>
</table>

From /proc/meminfo
- MemTotal: 790867880 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*/
- os-release:
  - NAME="SLES"
  - VERSION="15"
  - VERSION_ID="15"
  - PRETTY_NAME="SUSE Linux Enterprise Server 15"
  - ID="sles"
  - ID_LIKE="suse"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
- Linux RX2530M5 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019 (dd6077c)
- x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Mar 29 20:13

SPEC is set to: /home/Benchmark/speccpu2017-1.0.5
- Filesystem   Type  Size  Used Avail Use% Mounted on
- /dev/sda5    xfs   191G   57G  135G  30% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
- BIOS FUJITSU // American Megatrends Inc. V5.0.0.14 R1.2.0 for D3383-B1x 02/28/2019
- Memory:
  - 24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934
Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
__________________________________________________________________________
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXCC 508.namd_r(base) 510.parest_r(base)
__________________________________________________________________________
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  511.povray_r(base) 526.blender_r(base)
__________________________________________________________________________
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  507.cactuBSSN_r(base)
__________________________________________________________________________
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
__________________________________________________________________________
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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Compiler Version Notes (Continued)

==============================================================================
CC 521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64

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</tr>
</tbody>
</table>

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<thead>
<tr>
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<th>150</th>
</tr>
</thead>
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<tr>
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</tr>
</tbody>
</table>

Test Date: Mar-2019
Hardware Availability: May-2019
Software Availability: Feb-2019

Base Portability Flags (Continued)

554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-03-29 07:16:09-0400.
Report generated on 2019-05-23 13:00:24 by CPU2017 PDF formatter v6067.