SPEC® CPU2017 Floating Point Rate Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant ML350 Gen10
(2.50 GHz, Intel Xeon Gold 5215)

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

SPECrater2017_fp_base = 124
SPECrater2017_fp_peak = Not Run

Hardware

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 5215</td>
</tr>
<tr>
<td>Max MHz.</td>
<td>3400</td>
</tr>
<tr>
<td>Nominal</td>
<td>2500</td>
</tr>
<tr>
<td>Enabled</td>
<td>20 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1, 2 chip(s)</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>13.75 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R, running at 2666)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 400 GB SAS SSD, RAID 0</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

Software

<table>
<thead>
<tr>
<th>Name</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 15 (x86_64)</td>
</tr>
<tr>
<td>Kernel</td>
<td>4.12.14-23-default</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.2.187 of Intel C/C++</td>
</tr>
<tr>
<td>Compiler Build</td>
<td>20190117 for Linux;</td>
</tr>
<tr>
<td>Firmware</td>
<td>HPE BIOS Version U41 02/02/2019 released Apr-2019</td>
</tr>
<tr>
<td>File System</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Base</td>
<td></td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>1189</td>
<td>337</td>
<td>1186</td>
<td>338</td>
<td>1187</td>
<td>338</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>40</td>
<td>541</td>
<td>93.7</td>
<td>542</td>
<td>93.4</td>
<td>541</td>
<td>93.5</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>40</td>
<td>452</td>
<td>84.1</td>
<td>459</td>
<td>82.9</td>
<td>454</td>
<td>83.7</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>40</td>
<td>1529</td>
<td>68.5</td>
<td>1525</td>
<td>68.6</td>
<td>1522</td>
<td>68.7</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>40</td>
<td>724</td>
<td>129</td>
<td>723</td>
<td>129</td>
<td>721</td>
<td>130</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>40</td>
<td>520</td>
<td>81.1</td>
<td>518</td>
<td>81.4</td>
<td>519</td>
<td>81.2</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>40</td>
<td>621</td>
<td>144</td>
<td>619</td>
<td>145</td>
<td>646</td>
<td>139</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>40</td>
<td>483</td>
<td>126</td>
<td>484</td>
<td>126</td>
<td>484</td>
<td>126</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>40</td>
<td>552</td>
<td>127</td>
<td>558</td>
<td>125</td>
<td>564</td>
<td>124</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>40</td>
<td>371</td>
<td>268</td>
<td>376</td>
<td>265</td>
<td>375</td>
<td>265</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>40</td>
<td>348</td>
<td>193</td>
<td>345</td>
<td>195</td>
<td>351</td>
<td>192</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>40</td>
<td>1261</td>
<td>124</td>
<td>1258</td>
<td>124</td>
<td>1257</td>
<td>124</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>40</td>
<td>1034</td>
<td>61.5</td>
<td>1032</td>
<td>61.6</td>
<td>1036</td>
<td>61.4</td>
</tr>
</tbody>
</table>

SPECrate2017_fp_base = 124
SPECrate2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017_u2/lib/ia32:/home/cpu2017_u2/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
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(Test Sponsor: HPE)  
ProLiant ML350 Gen10  
(2.50 GHz, Intel Xeon Gold 5215)

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<th>Apr-2019</th>
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<td>Software Availability: Feb-2019</td>
</tr>
</tbody>
</table>

**SPECraten2017_fp_base = 124**  
**SPECraten2017_fp_peak = Not Run**

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:  
Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Enhanced Processor Performance set to Enabled  
Workload Profile set to General Throughput Compute  
Workload Profile set to Custom  
Energy/Performance Bias set to Balanced Performance  
Sysinfo program /home/cpu2017_u2/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on ml350-sles15 Thu Apr 11 14:19:25 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5215 CPU @ 2.50GHz  
2  "physical id"s (chips)  
40 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 10  
siblings : 20  
physical 0: cores 0 1 2 3 4 8 9 10 11 12  
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:  
Architecture: x86_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 40  
On-line CPU(s) list: 0-39  
Thread(s) per core: 2  
Core(s) per socket: 10  
Socket(s): 2  
NUMA node(s): 2  
Vendor ID: GenuineIntel

(Continued on next page)
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<td>Software Availability: Feb-2019</td>
</tr>
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**SPECrate2017_fp_base** = 124

**SPECrate2017_fp_peak** = Not Run

---

**Platform Notes (Continued)**

- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 5215 CPU @ 2.50GHz
- Stepping: 6
- CPU MHz: 2500.000
- BogoMIPS: 5000.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 14080K
- NUMA node0 CPU(s): 0-9, 20-29
- NUMA node1 CPU(s): 10-19, 30-39

Flags:
- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
- pat pse3 cli flush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
- lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
- aperfmerge tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
- sdbg cx16 xtpr pdcm pclid dca sse4_1 sse4_2 x2apic movbe popcnt
- tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
- epb cat_l3 cdp_l3 invpcid_single intel_p Hin pcr tpr_shadow vmmi flexpriority ept
- vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ersed invpcid rtm cqm mpx rdt_a
- avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
- xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local
- ibpb ibs tibp dtherm ida arat pln pts puck ospe avx512_vnni arch_capabilities ssbd

```
From /proc/cpuinfo cache data
  cache size: 14080 KB
```

```
From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29
  node 0 size: 193118 MB
  node 0 free: 192621 MB
  node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39
  node 1 size: 193502 MB
  node 1 free: 193224 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10
```

```
From /proc/meminfo
  MemTotal: 395899948 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB
```

(Continued on next page)
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Tested by: HPE

Platform Notes (Continued)

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15"
    VERSION_ID="15"
    PRETTY_NAME="SUSE Linux Enterprise Server 15"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
  Linux ml350-sles15 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Apr 11 14:15

SPEC is set to: /home/cpu2017_u2
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sdb2 btrfs 371G 206G 164G 56% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS HPE U41 02/02/2019
  Memory:
    24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2933, configured at 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

(Continued on next page)
Compiler Version Notes (Continued)

------------------------------------------------------------------------------------------------------------------
CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------

------------------------------------------------------------------------------------------------------------------
CC 511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------

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FC 507.cactuBSSN_r(base)
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Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.2.187 Build 20190117
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.2.187 Build 20190117
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------------------------------------------------------------------------------------------------------------------

------------------------------------------------------------------------------------------------------------------
FC 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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CC 521.wrf_r(base) 527.cam4_r(base)
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Compiler Version Notes (Continued)

64, Version 19.0.2.187 Build 20190117
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Intel (R) C Intel (R) 64 Compiler for applications running on Intel(R) 64,
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Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.rom_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
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<tr>
<td>3</td>
<td>Apr-2019</td>
<td>Apr-2019</td>
<td>Feb-2019</td>
</tr>
</tbody>
</table>

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-CLX-revA.xml
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-03.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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