## SPEC® CPU2017 Floating Point Speed Result

**New H3C Technologies Co., Ltd.**

**H3C UniServer R4900 G3 (Intel Xeon Gold 5122)**

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Apr-2019  
**Hardware Availability:** Jul-2017  
**Software Availability:** Mar-2019

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
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<tr>
<td>8</td>
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<td></td>
</tr>
<tr>
<td>8</td>
<td>51.4</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5122  
- **Max MHz.:** 3700  
- **Nominal:** 3600  
- **Enabled:** 8 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 16.5 MB I+D on chip per chip  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)  
- **Storage:** 1 x 1 TB 7200RPM SATA HDD  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.5 (Maipo)  
  3.10.0-957.el7.x86_64  
- **Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++ Compiler Build 20181018 for Linux; Fortran: Version 19.0.1.144 of Intel Fortran Compiler Build 20181018 for Linux  
- **Parallel:** Yes  
- **Firmware:** Version 2.00.24 released Mar-2019 BIOS  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 5122)  

SPECspeed2017_fp_base = 55.3
SPECspeed2017_fp_peak = 55.9

Results Table

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<tr>
<th>Benchmark</th>
<th>Threads</th>
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<th>Ratio</th>
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<td>52.8</td>
<td>314</td>
<td>50.2</td>
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<td>51.4</td>
</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 55.3
SPECspeed2017_fp_peak = 55.9

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/speccpu/lib/ia32:/home/speccpu/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Set SNC to Disabled
Set Hyper-Threading to Disabled

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECspeed2017_fp_base = 55.3
H3C UniServer R4900 G3 (Intel Xeon Gold 5122) | SPECspeed2017_fp_peak = 55.9

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| Test Date: | Apr-2019 |
| Hardware Availability: | Jul-2017 |
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Platform Notes (Continued)

Set Autonomous Core C-State to Enabled
Set Stale Atos to Enabled
Set Intel VT for Directed I/O (VT-d) to Disabled
Sysinfo program /home/speccpu/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on localhost.localdomain Wed Apr 10 15:13:08 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
  2 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
 physical 0: cores 1 5 9 13
 physical 1: cores 1 5 9 13
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                8
On-line CPU(s) list:   0-7
Thread(s) per core:    1
Core(s) per socket:    4
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
Stepping:              4
CPU MHz:               1212.890
CPU max MHz:           3700.0000
CPU min MHz:           1200.0000
BogoMIPS:              7200.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              16896K
```

(Continued on next page)
New H3C Technologies Co., Ltd.  
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SPEC CPU2017 Floating Point Speed Result  

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**Platform Notes (Continued)**

NUMA node0 CPU(s): 0-3  
NUMA node1 CPU(s): 4-7  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc  
aperfmpref perfprofli pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg  
fma cx16 xtpmr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes  
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_13 cdp_13 intel_pt ssbd mba  
ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle  
avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap  
c1flushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 cqm_llc  
cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts hwp hwp_act_window  
hwp_epp hwp_pkg_req pkup ospke spec_ctrl intel_stibp flush_l1d

/proc/cpuinfo cache data  
cache size: 16896 KB

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a physical chip.  
available: 2 nodes (0-1)  
node 0 cpus: 0 1 2 3  
node 0 size: 195225 MB  
node 0 free: 181288 MB  
node 1 cpus: 4 5 6 7  
node 1 size: 196608 MB  
node 1 free: 188490 MB  
node distances:  
node 0 1  
0: 10 21  
1: 21 10

From /proc/meminfo  
MemTotal: 394623840 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*  
os-release:  
NAME="Red Hat Enterprise Linux Server"  
VERSION="7.5 (Maipo)"  
ID=rhel  
ID_LIKE=fedora  
VARIANT=Server  
VARIANT_ID=server  
VERSION_ID="7.5"  
PRETTY_NAME="OpenShift Enterprise"  
redhat-release: Red Hat Enterprise Linux Server release 7.5 (Maipo)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 5122)  

**SPEC** SPECspeed2017_fp_base = 55.3  
SPECspeed2017_fp_peak = 55.9  

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**Platform Notes (Continued)**

```text
system-release: Red Hat Enterprise Linux Server release 7.5 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.5:ga:server

uname -a:  
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018  
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI  
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization  
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Apr 10 09:56

SPEC is set to: /home/speccpu
  Filesystem  Type  Size  Used Avail Use% Mounted on  
  /dev/sda2  xfs   293G   20G  274G   7% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret  
this section. The 'dmidecode' program reads system data which is "intended to allow  
hardware to be accurately determined", but the intent may not be met, as there are  
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

 BIOS American Megatrends Inc. 2.00.24 03/08/2019  
Memory:  
  12x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666  
  12x NO DIMM NO DIMM

(End of data from sysinfo program)
```

**Compiler Version Notes**

```text
==============================================================================
CC  619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)  
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  607.cactuBSSN_s(base, peak)  
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

(Continued on next page)
New H3C Technologies Co., Ltd. | SPEC CPU2017 Floating Point Speed Result

H3C UniServer R4900 G3 (Intel Xeon Gold 5122)

SPECspeed2017_fp_base = 55.3
SPECspeed2017_fp_peak = 55.9

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Compiler Version Notes (Continued)

Intel (R) C Intel (R) 64 Compiler for applications running on Intel (R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel (R) Fortran Intel (R) 64 Compiler for applications running on Intel (R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel (R) Fortran Intel (R) 64 Compiler for applications running on Intel (R) 64, Version 19.0.1.144 Build 20181018
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Intel (R) C Intel (R) 64 Compiler for applications running on Intel (R) 64, Version 19.0.1.144 Build 20181018
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**SPECspeed2017_fp_base** = 55.3  
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---

### Base Compiler Invocation

- **C benchmarks:**
  - icc -m64 -std=c11

- **Fortran benchmarks:**
  - ifort -m64

- **Benchmarks using both Fortran and C:**
  - ifort -m64 icc -m64 -std=c11

- **Benchmarks using Fortran, C, and C++:**
  - icpc -m64 icc -m64 -std=c11 ifort -m64

---

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64

---

### Base Optimization Flags

- **C benchmarks:**
  - -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  - -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

- **Fortran benchmarks:**
  - -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  - -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
  - -nostandard-realloc-lhs

- **Benchmarks using both Fortran and C:**
  - -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  - -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
  - -nostandard-realloc-lhs

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
603.bwaves_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=4
-qopenmp -nostandard-realloc-lhs
649.fotonik3d_s: Same as 603.bwaves_s
654.roms_s: -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4

(Continued on next page)
New H3C Technologies Co., Ltd. | SPECspeed2017_fp_peak = 55.9
---|---
H3C UniServer R4900 G3 (Intel Xeon Gold 5122) | SPECspeed2017_fp_base = 55.3

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---

**Peak Optimization Flags (Continued)**

654.roms_s (continued):
-qopenmp -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs

---

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevD.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.3-SKL-RevD.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-10 03:13:07-0400.  