## NEC Corporation

**Express5800/T110j-S (Intel Core i3-8300)**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>27.5</td>
<td>27.9</td>
</tr>
</tbody>
</table>

### CPU2017 License: 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation  
**Test Date:** Dec-2018  
**Hardware Availability:** Jan-2019  
**Software Availability:** Aug-2018

### Hardware

| Copies | 0 | 3.00 | 6.00 | 9.00 | 12.0 | 15.0 | 18.0 | 21.0 | 24.0 | 27.0 | 30.0 | 33.0 | 36.0 | 39.0 | 42.0 | 45.0 | 48.0 | 51.0 | 54.0 | 57.0 | 60.0 | 63.0 | 66.0 | 69.0 | 72.0 |
|--------|---|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| 503.bwaves_r | 4 |       | 23.3 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 507.cactuBSSN_r | 4 |       | 23.3 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 508.namd_r | 4 |       | 19.1 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 510.parest_r | 4 |       | 19.1 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 511.povray_r | 4 |       | 17.9 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 519.lbm_r | 4 |       | 16.6 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 521.wrf_r | 4 |       | 32.9 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 526.blender_r | 4 |       | 32.9 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 527.cam4_r | 4 |       | 31.0 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 538.imagick_r | 4 |       | 67.3 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 544.nab_r | 4 |       | 67.4 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 549.fotonik3d_r | 4 |       | 67.4 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 554.roms_r | 4 |       | 67.4 |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |

### Software

| OS: Red Hat Enterprise Linux Server release 7.5 (Maipo) |
| Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux |
| Firmware: NEC BIOS Version F07 10/31/2018 released Dec-2018 |
| File System: ext4 |
| System State: Run level 3 (multi-user) |
| Base Pointers: 64-bit |
| Peak Pointers: 64-bit |
| Other: None |

### CPU Name: Intel Core i3-8300  
**Max MHz.:** 3700  
**Nominal:** 3700  
**Enabled:** 4 cores, 1 chip  
**Orderable:** 1 chip  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**Cache L2:** 256 KB I+D on chip per core  
**Cache L3:** 8 MB I+D on chip per chip  
**Other:** None  
**Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)  
**Storage:** 1 x 1 TB SATA, 7200 RPM  
**Other:** None
SPEC CPU2017 Floating Point Rate Result

NEC Corporation

Express5800/T110j-S (Intel Core i3-8300)

SPECrate2017_fp_base = 27.5

SPECrate2017_fp_peak = 27.9

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>4</td>
<td>594</td>
<td>67.5</td>
<td>594</td>
<td>67.5</td>
<td>594</td>
<td>67.5</td>
<td>4</td>
<td>594</td>
<td>67.5</td>
<td>594</td>
<td>67.5</td>
<td>594</td>
<td>67.5</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>4</td>
<td>217</td>
<td>23.3</td>
<td>218</td>
<td>23.3</td>
<td>218</td>
<td>23.2</td>
<td>4</td>
<td>217</td>
<td>23.4</td>
<td>217</td>
<td>23.3</td>
<td>217</td>
<td>23.3</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>4</td>
<td>199</td>
<td>19.1</td>
<td>199</td>
<td>19.1</td>
<td>208</td>
<td>18.3</td>
<td>4</td>
<td>198</td>
<td>19.2</td>
<td>199</td>
<td>19.1</td>
<td>200</td>
<td>19.0</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>4</td>
<td>584</td>
<td>17.9</td>
<td>582</td>
<td>18.0</td>
<td>585</td>
<td>17.9</td>
<td>4</td>
<td>583</td>
<td>18.0</td>
<td>586</td>
<td>17.8</td>
<td>585</td>
<td>17.9</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>4</td>
<td>309</td>
<td>30.2</td>
<td>307</td>
<td>30.5</td>
<td>306</td>
<td>30.3</td>
<td>4</td>
<td>262</td>
<td>35.7</td>
<td>265</td>
<td>35.3</td>
<td>266</td>
<td>35.2</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>4</td>
<td>255</td>
<td>16.5</td>
<td>255</td>
<td>16.5</td>
<td>255</td>
<td>16.5</td>
<td>4</td>
<td>255</td>
<td>16.5</td>
<td>255</td>
<td>16.6</td>
<td>254</td>
<td>16.6</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>4</td>
<td>271</td>
<td>33.1</td>
<td>272</td>
<td>32.9</td>
<td>273</td>
<td>32.8</td>
<td>4</td>
<td>269</td>
<td>33.3</td>
<td>277</td>
<td>32.4</td>
<td>271</td>
<td>33.1</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>4</td>
<td>238</td>
<td>25.6</td>
<td>237</td>
<td>25.7</td>
<td>238</td>
<td>25.6</td>
<td>4</td>
<td>237</td>
<td>25.7</td>
<td>237</td>
<td>25.7</td>
<td>237</td>
<td>25.7</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>4</td>
<td>233</td>
<td>30.1</td>
<td>232</td>
<td>30.2</td>
<td>231</td>
<td>30.2</td>
<td>4</td>
<td>226</td>
<td>31.0</td>
<td>226</td>
<td>31.0</td>
<td>226</td>
<td>31.0</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>4</td>
<td>148</td>
<td>67.3</td>
<td>147</td>
<td>67.5</td>
<td>152</td>
<td>65.5</td>
<td>4</td>
<td>148</td>
<td>67.4</td>
<td>147</td>
<td>67.6</td>
<td>155</td>
<td>64.2</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>4</td>
<td>178</td>
<td>37.9</td>
<td>177</td>
<td>37.9</td>
<td>177</td>
<td>38.0</td>
<td>4</td>
<td>178</td>
<td>37.9</td>
<td>177</td>
<td>38.0</td>
<td>178</td>
<td>37.8</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>4</td>
<td>754</td>
<td>20.7</td>
<td>754</td>
<td>20.7</td>
<td>753</td>
<td>20.7</td>
<td>4</td>
<td>752</td>
<td>20.7</td>
<td>753</td>
<td>20.7</td>
<td>753</td>
<td>20.7</td>
</tr>
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<td>554.roms_r</td>
<td>4</td>
<td>457</td>
<td>13.9</td>
<td>459</td>
<td>13.8</td>
<td>456</td>
<td>13.9</td>
<td>4</td>
<td>444</td>
<td>14.3</td>
<td>441</td>
<td>14.4</td>
<td>443</td>
<td>14.3</td>
</tr>
</tbody>
</table>

SPECrate2017_fp_base = 27.5

SPECrate2017_fp_peak = 27.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
IRQ balance service was stopped using "systemctl stop irqbalance.service"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result
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NEC Corporation
Express5800/T110j-S (Intel Core i3-8300)

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CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Dec-2018
Hardware Availability: Jan-2019
Software Availability: Aug-2018

General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
VT-x: Disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on t110js Fri Dec 14 23:24:40 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-8300 CPU @ 3.70GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
  siblings : 4
  physical 0: cores 0 1 2 3

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i3-8300 CPU @ 3.70GHz
Stepping: 11
CPU MHz: 3696.838
CPU max MHz: 3700.000

(Continued on next page)
### NEC Corporation

**Express5800/T110j-S (Intel Core i3-8300)**

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</table>

**SPECrate2017_fp_base = 27.5**

**SPECrate2017_fp_peak = 27.9**

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### Platform Notes (Continued)

- **CPU min MHz:** 800.0000
- **BogoMIPS:** 7392.00
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 256K
- **L3 cache:** 8192K
- **NUMA node0 CPU(s):** 0-3
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1_l ssse3 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrnd lahf_lm abm 3dnowprefetch intel_pt ssbd ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid mxpd rdseed adx smap clflushopt xsaveopt xsavec xgetbv1 dtherm arat pln pts hwp hwp_notify hwp_act_window hwp_epp spec_ctrl intel_stibp flush_l1d

**/proc/cpuinfo cache data**

```
cache size: 8192 KB
```

From `numactl --hardware`

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

- **available:** 1 nodes (0)
- **node 0 cpus:** 0 1 2 3
- **node 0 size:** 65455 MB
- **node 0 free:** 63589 MB
- **node distances:**
  - node 0
    - 0: 10

From `/proc/meminfo`

```
MemTotal: 65895716 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

From `/etc/*release*` `/etc/*version*`

```
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.5 (Maipo)"
  ID=rhel
  ID_LIKE="fedora"
  VARIANT="Server"
  VARIANT_ID="server"
  VERSION_ID="7.5"
```

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

NEC Corporation
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Platform Notes (Continued)

system-release: Red Hat Enterprise Linux Server release 7.5 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.5:ga:server

uname -a:
Linux t110js 3.10.0-862.11.6.el7.x86_64 #1 SMP Fri Aug 10 16:55:11 UTC 2018 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

run-level 3 Dec 14 23:19
SPEC is set to: /home/cpu2017

Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda3      ext4  909G   74G  789G   9% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. F07 10/31/2018
Memory:
4x Samsung M391A2K43BB1-CTD 16 GB 2 rank 2667, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
<table>
<thead>
<tr>
<th>CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icc (ICC) 18.0.2 20180210</td>
</tr>
<tr>
<td>Copyright (C) 1985-2018 Intel Corporation. All rights reserved.</td>
</tr>
<tr>
<td>------------------------------------------------------------------------------</td>
</tr>
</tbody>
</table>

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<table>
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<tr>
<th>CC  519.lbm_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>icc (ICC) 18.0.2 20180210</td>
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</table>

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Compiler Version Notes (Continued)

CXXC 508.namd_r(base) 510.parest_r(base, peak)
icpc (ICC) 18.0.2 20180210
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CXXC 508.namd_r(peak)
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icc (ICC) 18.0.2 20180210
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CC 511.povray_r(peak)
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FC 507.cactuBSSN_r(base, peak)
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
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FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
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Compiler Version Notes (Continued)

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ifort (IFORT) 18.0.2 20180210
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CC 521.wrf_r(base) 527.cam4_r(base)
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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
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CC 521.wrf_r(peak) 527.cam4_r(peak)
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icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
---

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
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Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
SPEC CPU2017 Floating Point Rate Result

NEC Corporation
Express5800/T110j-S (Intel Core i3-8300)

SPECrate2017_fp_base = 27.5
SPECrate2017_fp_peak = 27.9

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Dec-2018
Hardware Availability: Jan-2019
Software Availability: Aug-2018

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 538.imagick_r

C++ benchmarks:
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

(Continued on next page)
Peak Optimization Flags (Continued)

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
    -ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
    -ffinite-math-only -qopt-mem-layout-trans=3 -auto
    -nostandard-realloc-lhs

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
    -no-prec-div -qopt-prefetch -ffinite-math-only
    -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
    -no-prec-div -qopt-prefetch -ffinite-math-only
    -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
    -no-prec-div -qopt-prefetch -ffinite-math-only
    -qopt-mem-layout-trans=3

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
    -ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
    -qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
<table>
<thead>
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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