Fujitsu
PRIMERGY TX1330 M3, Intel Core i3-7100, 3.90GHz

<table>
<thead>
<tr>
<th>SPECrate2017_fp_peak</th>
<th>SPECrate2017_fp_base</th>
</tr>
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<tbody>
<tr>
<td>19.6</td>
<td>19.2</td>
</tr>
</tbody>
</table>

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Core i3-7100</td>
<td>OS: SUSE Linux Enterprise Server 15 4.12.14-23-default</td>
</tr>
<tr>
<td>Max MHz.: 3900</td>
<td>Compiler: C/C++: Version 19.0.0.117 of Intel C/C++ Compiler for Linux; Fortran: Version 19.0.0.117 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Nominal: 3900</td>
<td>Parallel: No</td>
</tr>
<tr>
<td>Enabled: 2 cores, 1 chip, 2 threads/core</td>
<td>Firmware: Fujitsu BIOS Version V5.0.0.11 R1.21.0 for D3373-B1x. Released Nov-2018</td>
</tr>
<tr>
<td>Orderable: 1 chip</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 32 KB D on chip per core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>L2: 256 KB I+D on chip per core</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>L3: 3 MB I+D on chip per chip</td>
<td>Peak Pointers: 64-bit</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: None</td>
</tr>
<tr>
<td>Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2400T-E)</td>
<td></td>
</tr>
<tr>
<td>Storage: 1 x SATA HDD, 2TB, 7200RPM</td>
<td></td>
</tr>
</tbody>
</table>

Test Date: Nov-2018
Hardware Availability: May-2017
Software Availability: Sep-2018

Tested by: Fujitsu
Hardware Availability: May-2017
Software Availability: Sep-2018
SPEC CPU2017 Floating Point Rate Result

Fujitsu
PRIMERGY TX1330 M3, Intel Core i3-7100, 3.90GHz

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

SPECrerate2017_fp_base = 19.2
SPECrerate2017_fp_peak = 19.6

Results Table

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<td>511.povray_r</td>
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<td>16.5</td>
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<td>18.6</td>
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<td>38.9</td>
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<td>38.9</td>
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<td>544.nab_r</td>
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<td>256</td>
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<td>256</td>
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<td>549.fotonik3d_r</td>
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<td>841</td>
<td>18.5</td>
<td>843</td>
<td>18.5</td>
</tr>
</tbody>
</table>

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
echo always > /sys/kernel/mm/transparent_hugepage/enabled
echo 1 > /proc/sys/vm/drop_caches
echo 1000000000 > /proc/sys/vm/sched_min_granularity_ns
echo 1000000000 > /proc/sys/vm/sched_wakeup_granularity_ns

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-ic19-20181011/icc19-lib/intel64"
Binaries compiled on a system with 2x Intel Xeon Silver 4108 CPU + 384GB RAM memory using SUSE Linux Enterprise Server 12 SP2
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3 > /proc/sys/vm/drop_caches

(Continued on next page)
**General Notes (Continued)**

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

**Platform Notes**

BIOS configuration:
Pan Control = Full
Sysinfo program /home/Benchmark/speccpu2017-ic19-20181011/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on TX1330M3 Wed Nov 28 01:44:07 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-7100 CPU @ 3.90GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 2
  siblings : 4
  physical 0: cores 0 1

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 2
Core(s) per socket: 2
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i3-7100 CPU @ 3.90GHz
Stepping: 9
CPU MHz: 3900.000

(Continued on next page)
<table>
<thead>
<tr>
<th>Platform Notes (Continued)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU max MHz: 3900.0000</td>
</tr>
<tr>
<td>CPU min MHz: 800.0000</td>
</tr>
<tr>
<td>BogoMIPS: 7824.00</td>
</tr>
<tr>
<td>Virtualization: VT-x</td>
</tr>
<tr>
<td>L1d cache: 32K</td>
</tr>
<tr>
<td>L1i cache: 32K</td>
</tr>
<tr>
<td>L2 cache: 256K</td>
</tr>
<tr>
<td>L3 cache: 3072K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s): 0-3</td>
</tr>
<tr>
<td>Flags:</td>
</tr>
<tr>
<td>/proc/cpuinfo cache data</td>
</tr>
<tr>
<td>cache size : 3072 KB</td>
</tr>
</tbody>
</table>

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

| available: 1 nodes (0)  |
| node 0 cpus: 0 1 2 3     |
| node 0 size: 64034 MB    |
| node 0 free: 63564 MB    |
| node distances:          |
| node 0                   |
| 0: 10                    |

From /proc/meminfo

MemTotal: 65571408 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME=cpe:/o:suse:sles:15

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Fujitsu
PRIMERGY TX1330 M3, Intel Core i3-7100, 3.90GHz

SPECrate2017_fp_base = 19.2
SPECrate2017_fp_peak = 19.6

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Platform Notes (Continued)

uname -a:
    Linux TX1330M3 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b) x86_64
    x86_64 x86_64 GNU/Linux

run-level 3 Nov 27 18:37

SPEC is set to: /home/Benchmark/speccpu2017-ic19-20181011
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda4      xfs   1.7T   27G  1.7T   2% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.21.0 for D3373-B1x
    11/20/2018
    Memory:
    4x Samsung M391A2K43BB1-CRC 16 GB 2 rank 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
    CC 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
==============================================================================
    icc (ICC) 19.0.0.117 20180804
    Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
    CC 519.lbm_r(peak) 538.imagick_r(peak) 544.nab_r(peak)
==============================================================================
    icc (ICC) 19.0.0.117 20180804
    Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
    CXXC 508.namd_r(base) 510.parest_r(base)
==============================================================================
    icpc (ICC) 19.0.0.117 20180804
    Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
==============================================================================

(Continued on next page)
Fujitsu
PRIMERGY TX1330 M3, Intel Core i3-7100, 3.90GHz

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Fujitsu

SPECrate2017_fp_base = 19.2
SPECrate2017_fp_peak = 19.6

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Nov-2018
Hardware Availability: May-2017
Software Availability: Sep-2018

Compiler Version Notes (Continued)

CXXC  508.namd_r(peak)  510.parest_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

CC  511.povray_r(base)  526.blender_r(base)
------------------------------------------------------------------------------
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

CC   511.povray_r(peak) 526.blender_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

FC  507.cactuBSSN_r(base)
------------------------------------------------------------------------------
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

FC   507.cactuBSSN_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
Fujitsu
PRIMERGY TX1330 M3, Intel Core i3-7100, 3.90GHz

SPEC CPU2017 Floating Point Rate Result

Fujitsu

SPECrate2017_fp_base = 19.2
SPECrate2017_fp_peak = 19.6

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Nov-2018
Hardware Availability: May-2017
Software Availability: Sep-2018

Compiler Version Notes (Continued)

```
FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
------------------
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
FC  503.bwaves_r(peak) 549.fotonik3d_r(peak) 554.roms_r(peak)
------------------
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CC  521.wrf_r(base) 527.cam4_r(base)
---------------------
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

```
CC  521.wrf_r(peak) 527.cam4_r(peak)
---------------------
ifort (IFORT) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 19.0.0.117 20180804
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

(Continued on next page)
Base Compiler Invocation (Continued)

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only

(Continued on next page)
Fujitsu
PRIMERGY TX1330 M3, Intel Core i3-7100, 3.90GHz

SPECrate2017_fp_base = 19.2
SPECrate2017_fp_peak = 19.6

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

**Base Optimization Flags (Continued)**

Benchmarks using both C and C++ (continued):
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
519.tbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Fujitsu
PRIMERGY TX1330 M3, Intel Core i3-7100, 3.90GHz

SPECrate2017_fp_base = 19.2
SPECrate2017_fp_peak = 19.6

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Nov-2018
Hardware Availability: May-2017
Software Availability: Sep-2018

Peak Optimization Flags (Continued)

538.imagick_r: basepeak = yes
544.nab_r: basepeak = yes

C++ benchmarks:
508.namd_r: basepeak = yes
510.parest_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
503.bwaves_r: basepeak = yes
549.fotonik3d_r: basepeak = yes
554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:
507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-BDW-RevF.xml
<table>
<thead>
<tr>
<th>SPEC CPU2017 Floating Point Rate Result</th>
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<tbody>
<tr>
<td><strong>Fujitsu</strong></td>
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<td>PRIMERGY TX1330 M3, Intel Core i3-7100,</td>
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<td><strong>SPECrate2017_fp_peak = 19.6</strong></td>
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-11-27 11:44:06-0500.
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