## NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5115)

### SPECrate2017_fp_base = 110

### SPECrate2017_fp_peak = 113

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>85.4</td>
<td>85.4</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>40</td>
<td>73.8</td>
<td>73.8</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>40</td>
<td>74.3</td>
<td>74.3</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>40</td>
<td>61.5</td>
<td>61.5</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>40</td>
<td>116</td>
<td>116</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>40</td>
<td>71.3</td>
<td>71.3</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>40</td>
<td>129</td>
<td>129</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>40</td>
<td>110</td>
<td>110</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>40</td>
<td>109</td>
<td>109</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>40</td>
<td>244</td>
<td>244</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>40</td>
<td>174</td>
<td>174</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>40</td>
<td>107</td>
<td>107</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>40</td>
<td>54.6</td>
<td>54.6</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name**: Intel Xeon Gold 5115
- **Max MHz.**: 3200
- **Nominal**: 2400
- **Enabled**: 20 cores, 2 chips, 2 threads/core
- **Orderable**: 12 chips
- **Cache L1**: 32 KB I & 32 KB D on chip per core
- **L2**: 1 MB I+D on chip per core
- **L3**: 13.75 MB I+D on chip per chip
- **Memory**: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
- **Storage**: 1 x 1 TB SATA, 7200 RPM, RAID 0
- **Other**: None

### Software

- **OS**: Red Hat Enterprise Linux Server release 7.4 (Maipo)
- **Compiler**: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux
- **Parallel**: No
- **Firmware**: NEC BIOS Version U31 06/20/2018 released Sep-2018
- **File System**: ext4
- **System State**: Run level 3 (multi-user)
- **Base Pointers**: 64-bit
- **Peak Pointers**: 64-bit
- **Other**: None
## SPEC CPU2017 Floating Point Rate Result

**NEC Corporation**  
**Express5800/R120h-1E (Intel Xeon Gold 5115)**

---

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>1322</td>
<td>303</td>
<td>1321</td>
<td>304</td>
<td>1318</td>
<td>304</td>
<td>40</td>
<td>1321</td>
<td>304</td>
<td>1320</td>
<td>304</td>
<td>1316</td>
<td>305</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>40</td>
<td>593</td>
<td>85.4</td>
<td>593</td>
<td>85.5</td>
<td>594</td>
<td>85.3</td>
<td>40</td>
<td>593</td>
<td>85.4</td>
<td>592</td>
<td>85.6</td>
<td>594</td>
<td>85.3</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>40</td>
<td>514</td>
<td>73.9</td>
<td>515</td>
<td>73.8</td>
<td>520</td>
<td>73.0</td>
<td>40</td>
<td>512</td>
<td>74.3</td>
<td>509</td>
<td>74.6</td>
<td>514</td>
<td>73.9</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>40</td>
<td>1703</td>
<td>61.5</td>
<td>1702</td>
<td>61.5</td>
<td>1700</td>
<td>61.6</td>
<td>40</td>
<td>1703</td>
<td>61.5</td>
<td>1702</td>
<td>61.5</td>
<td>1700</td>
<td>61.6</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>40</td>
<td>806</td>
<td>116</td>
<td>804</td>
<td>116</td>
<td>806</td>
<td>116</td>
<td>40</td>
<td>685</td>
<td>136</td>
<td>683</td>
<td>137</td>
<td>685</td>
<td>136</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>40</td>
<td>591</td>
<td>71.3</td>
<td>592</td>
<td>71.2</td>
<td>591</td>
<td>71.3</td>
<td>40</td>
<td>556</td>
<td>75.8</td>
<td>557</td>
<td>75.7</td>
<td>559</td>
<td>75.4</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>40</td>
<td>699</td>
<td>128</td>
<td>700</td>
<td>128</td>
<td>698</td>
<td>128</td>
<td>40</td>
<td>691</td>
<td>130</td>
<td>695</td>
<td>129</td>
<td>695</td>
<td>129</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>40</td>
<td>555</td>
<td>110</td>
<td>556</td>
<td>109</td>
<td>555</td>
<td>110</td>
<td>40</td>
<td>556</td>
<td>109</td>
<td>555</td>
<td>110</td>
<td>555</td>
<td>110</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>40</td>
<td>689</td>
<td>102</td>
<td>691</td>
<td>101</td>
<td>691</td>
<td>101</td>
<td>40</td>
<td>666</td>
<td>105</td>
<td>663</td>
<td>105</td>
<td>665</td>
<td>105</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>40</td>
<td>405</td>
<td>246</td>
<td>407</td>
<td>244</td>
<td>409</td>
<td>243</td>
<td>40</td>
<td>406</td>
<td>245</td>
<td>406</td>
<td>245</td>
<td>410</td>
<td>243</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>40</td>
<td>391</td>
<td>172</td>
<td>386</td>
<td>175</td>
<td>387</td>
<td>174</td>
<td>40</td>
<td>387</td>
<td>174</td>
<td>386</td>
<td>174</td>
<td>387</td>
<td>174</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>40</td>
<td>1455</td>
<td>107</td>
<td>1450</td>
<td>108</td>
<td>1456</td>
<td>107</td>
<td>40</td>
<td>1450</td>
<td>107</td>
<td>1449</td>
<td>108</td>
<td>1456</td>
<td>107</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>40</td>
<td>1164</td>
<td>54.6</td>
<td>1165</td>
<td>54.6</td>
<td>1168</td>
<td>54.4</td>
<td>40</td>
<td>1132</td>
<td>56.1</td>
<td>1134</td>
<td>56.0</td>
<td>1133</td>
<td>56.1</td>
</tr>
</tbody>
</table>

**SPECrate2017_fp_base = 110**  
**SPECrate2017_fp_peak = 113**

---

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/i32:/home/cpu2017/lib/intel64:/home/cpu2017/jes.5.0.1-32:/home/cpu2017/jes.5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
umactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
SPEC CPU2017 Floating Point Rate Result

NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 110
SPECrate2017_fp_peak = 113

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Nov-2018
Hardware Availability: Nov-2017
Software Availability: Mar-2018

General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Thermal Configuration: Maximum Cooling
Workload Profile: General Throughput Compute
Memory Patrol Scrubbing: Disabled
LLC Dead Line Allocation: Disabled
LLC Prefetch: Enabled
Workload Profile: Custom
Sub-NUMA Clustering: Disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on r120h1e Thu Nov 29 03:46:29 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
  2  "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 110
SPECrate2017_fp_peak = 113

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Nov-2018

Tested by: NEC Corporation
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Platform Notes (Continued)

Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
Stepping: 4
CPU MHz: 2400.000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0-9,20-29
NUMA node1 CPU(s): 10-19,30-39

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant-tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch epb cat_l3 cdp_l3 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vmmid flexpriority ept vpid fsgsbase
tsc_adjust bm1 hle avx2 smep bmi2 3msr invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavesc xgetbv1

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo

From /proc/cpuinfo

(Continued on next page)
### Platform Notes (Continued)

From `/etc/*release*` /etc/*version*
```
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server
```

```
uname -a:
  Linux r120h1e 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
  x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2017-5754 (Meltdown): Mitigation: PTI
- CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences
- CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)

```
run-level 3 Nov 29 03:40
```

SPEC is set to: `/home/cpu2017`
```
  Filesystem  Type  Size  Used Avail Use% Mounted on
  /dev/sda3     ext4  909G  111G  752G  13% /
```

Additional information from `dmidecode` follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS NEC U31 06/20/2018
- Memory:
  - 4x UNKNOWN NOT AVAILABLE
  - 12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2400

(End of data from `sysinfo` program)

### Compiler Version Notes

```
CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)
```

(Continued on next page)
NEC Corporation
Express5800/R120h-1E (Intel Xeon Gold 5115)

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 110
SPECrate2017_fp_peak = 113

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Nov-2018
Tested by: NEC Corporation
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Compiler Version Notes (Continued)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC 519.lbm_r(peak)

icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base, peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(base) 526.blender_r(base, peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC 507.cactuBSSN_r(base, peak)

icpc (ICC) 18.0.2 20180210

(Continued on next page)
NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5115)

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 110
SPECrate2017_fp_peak = 113

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Nov-2018

Tested by: NEC Corporation
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Compiler Version Notes (Continued)
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Base Compiler Invocation

(Continued on next page)
## NEC Corporation

**Express5800/R120h-1E (Intel Xeon Gold 5115)**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>110</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>113</td>
</tr>
</tbody>
</table>

### CPU2017 License: 9006

**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Nov-2018  
**Hardware Availability:** Nov-2017  
**Software Availability:** Mar-2018

---

### Base Compiler Invocation (Continued)

- Fortran benchmarks:
  
  ifort -m64

- Benchmarks using both Fortran and C:
  
  ifort -m64 icc -m64 -std=c11

- Benchmarks using both C and C++:
  
  icpc -m64 icc -m64 -std=c11

- Benchmarks using Fortran, C, and C++:
  
  icpc -m64 icc -m64 -std=c11 ifort -m64

---

### Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64
- 507.cactuBSSN_r: -DSPEC_LP64
- 508.namd_r: -DSPEC_LP64
- 510.parest_r: -DSPEC_LP64
- 511.povray_r: -DSPEC_LP64
- 519.lbm_r: -DSPEC_LP64
- 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64

---

### Base Optimization Flags

- C benchmarks:
  
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
  
  -qopt-mem-layout-trans=3

- C++ benchmarks:
  
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
  
  -qopt-mem-layout-trans=3

- Fortran benchmarks:
  
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only

(Continued on next page)
Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags
# SPEC CPU2017 Floating Point Rate Result

## NEC Corporation

**Express5800/R120h-1E (Intel Xeon Gold 5115)**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>110</td>
<td>113</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Test Date:** Nov-2018  
**Hardware Availability:** Nov-2017  
**Tested by:** NEC Corporation  
**Software Availability:** Mar-2018

---

### Peak Optimization Flags

**C benchmarks:**

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 538.imagick_r

**C++ benchmarks:**

508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

510.parest_r: basepeak = yes

**Fortran benchmarks:**

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -auto  
-nostandard-realloc-lhs

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

**Benchmarks using both Fortran and C:**

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

**Benchmarks using both C and C++:**

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

NEC Corporation

Express5800/R120h-1E (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 110
SPECrate2017_fp_peak = 113

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Nov-2018
Tested by: NEC Corporation
Hardware Availability: Nov-2017
Software Availability: Mar-2018

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Report generated on 2018-12-26 12:58:14 by CPU2017 PDF formatter v6067.
Originally published on 2018-12-25.