Lenovo Global Technology

**ThinkSystem SR150**

(3.30 GHz, Intel Xeon E-2126G)

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

---

**SPECrate2017_int_base = 35.3**

**SPECrate2017_int_peak = Not Run**

---

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>OS:</strong> SUSE Linux Enterprise Server 12 SP3 (x86_64)</td>
<td><strong>CPU Name:</strong> Intel Xeon E-2126G</td>
</tr>
<tr>
<td>Kernel 4.4.131-94.29-default</td>
<td><strong>Max MHz.:</strong> 4500</td>
</tr>
<tr>
<td><strong>Compiler:</strong> C/C++: Version 18.0.2.199 of Intel C/C++</td>
<td><strong>Nominal:</strong> 3300</td>
</tr>
<tr>
<td>Compiler for Linux;</td>
<td><strong>Enabled:</strong> 6 cores, 1 chip</td>
</tr>
<tr>
<td>Fortran: Version 18.0.2.199 of Intel Fortran</td>
<td><strong>Orderable:</strong> 1 chip</td>
</tr>
<tr>
<td>Compiler for Linux</td>
<td><strong>Cache L1:</strong> 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>Parallel:</strong> No</td>
<td><strong>L2:</strong> 256 KB I+D on chip per core</td>
</tr>
<tr>
<td><strong>Firmware:</strong> Lenovo BIOS Version ISE105G 1.01 released Oct-2018</td>
<td><strong>L3:</strong> 12 MB I+D on chip per chip</td>
</tr>
<tr>
<td><strong>File System:</strong> btrfs</td>
<td><strong>Other:</strong> None</td>
</tr>
<tr>
<td><strong>System State:</strong> Run level 3 (multi-user)</td>
<td><strong>Memory:</strong> 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E)</td>
</tr>
<tr>
<td><strong>Base Pointers:</strong> 64-bit</td>
<td><strong>Storage:</strong> 1 x 480 GB SATA SSD</td>
</tr>
<tr>
<td><strong>Peak Pointers:</strong> Not Applicable</td>
<td><strong>Other:</strong> None</td>
</tr>
<tr>
<td><strong>Other:</strong> jemalloc memory allocator V5.0.1</td>
<td></td>
</tr>
</tbody>
</table>
## Lenovo Global Technology

**ThinkSystem SR150**

**(3.30 GHz, Intel Xeon E-2126G)**

| CPU2017 License: | 9017 |
| **Test Sponsor:** | Lenovo Global Technology |
| **Tested by:** | Lenovo Global Technology |

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>500.perlbench_r</strong></td>
<td>6</td>
<td>305</td>
<td>31.4</td>
<td>306</td>
<td>31.2</td>
<td><strong>305</strong></td>
<td><strong>31.3</strong></td>
</tr>
<tr>
<td><strong>502.gcc_r</strong></td>
<td>6</td>
<td>261</td>
<td><strong>32.6</strong></td>
<td>260</td>
<td>32.6</td>
<td>262</td>
<td>32.4</td>
</tr>
<tr>
<td><strong>505.mcf_r</strong></td>
<td>6</td>
<td>248</td>
<td>39.1</td>
<td><strong>249</strong></td>
<td><strong>38.9</strong></td>
<td>253</td>
<td>38.4</td>
</tr>
<tr>
<td><strong>520.omnetpp_r</strong></td>
<td>6</td>
<td>415</td>
<td>19.0</td>
<td><strong>414</strong></td>
<td><strong>19.0</strong></td>
<td>413</td>
<td>19.0</td>
</tr>
<tr>
<td><strong>523.xalancbmk_r</strong></td>
<td>6</td>
<td>185</td>
<td><strong>34.2</strong></td>
<td>185</td>
<td>34.2</td>
<td>184</td>
<td>34.5</td>
</tr>
<tr>
<td><strong>525.x264_r</strong></td>
<td>6</td>
<td>131</td>
<td>80.0</td>
<td>132</td>
<td>79.8</td>
<td><strong>131</strong></td>
<td><strong>80.0</strong></td>
</tr>
<tr>
<td><strong>531.deepsjeng_r</strong></td>
<td>6</td>
<td>206</td>
<td>33.4</td>
<td>206</td>
<td>33.3</td>
<td><strong>206</strong></td>
<td><strong>33.4</strong></td>
</tr>
<tr>
<td><strong>541.leela_r</strong></td>
<td>6</td>
<td>359</td>
<td><strong>27.7</strong></td>
<td>360</td>
<td>27.6</td>
<td>359</td>
<td>27.7</td>
</tr>
<tr>
<td><strong>548.exchange2_r</strong></td>
<td>6</td>
<td>205</td>
<td>76.7</td>
<td>209</td>
<td>75.3</td>
<td><strong>207</strong></td>
<td><strong>76.1</strong></td>
</tr>
<tr>
<td><strong>557.xz_r</strong></td>
<td>6</td>
<td>309</td>
<td>21.0</td>
<td><strong>309</strong></td>
<td><strong>21.0</strong></td>
<td>309</td>
<td>20.9</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 35.3**

**SPECrate2017_int_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic18.0u2/lib/ia32:/home/cpu2017-1.0.5-ic18.0u2/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017-1.0.5-ic18.0u2/je5.0.1-32:/home/cpu2017-1.0.5-ic18.0u2/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR150
(3.30 GHz, Intel Xeon E-2126G)

SPECrate2017_int_peak = Not Run
SPECrate2017_int_base = 35.3

General Notes (Continued)
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
CPU P-state Control set to Legacy
Execute Disable Bit set to Disable
Per Core P-state set to Disable
Adjacent Cache Prefetch set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic18.0u2/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-tsni Fri Nov 23 09:49:42 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) E-2126G CPU @ 3.30GHz
  1 "physical id"s (chips)
  6 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 6
  siblings : 6
  physical 0: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 6
On-line CPU(s) list: 0-5
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Xeon(R) E-2126G CPU @ 3.30GHz
Stepping: 10

(Continued on next page)
SPEC CPU2017 Integer Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology
ThinkSystem SR150
(3.30 GHz, Intel Xeon E-2126G)

SPECrate2017_int_base = 35.3
SPECrate2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Nov-2018
Hardware Availability: Jan-2019
Software Availability: May-2018

Platform Notes (Continued)

CPU MHz: 4367.610
CPU max MHz: 4500.0000
CPU min MHz: 800.0000
BogoMIPS: 6623.97
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 12288K
NUMA node0 CPU(s): 0-5
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
       fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
       xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat ept vpid fsgsbase tsc_adjust
       bmi1 hle avx2 smep bmi2  0
       ems xsaveopt xsavec xgetbv1

/proc/cpuinfo cache data
   cache size : 12288 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
   physical chip.
   available: 1 nodes (0)
   node 0 cpus: 0 1 2 3 4 5
   node 0 size: 64381 MB
   node 0 free: 63843 MB
   node distances:
   node 0
   0: 10

From /proc/meminfo
   MemTotal: 65926172 kB
   HugePages_Total: 0
   Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 3
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
      os-release:

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Lenovo Global Technology
ThinkSystem SR150
(3.30 GHz, Intel Xeon E-2126G)

SPECRate2017_int_base = 35.3
SPECRate2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Nov-2018
Hardware Availability: Jan-2019
Software Availability: May-2018

Platform Notes (Continued)

NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-tsni 4.4.131-94.29-default #1 SMP Mon May 21 14:41:34 UTC 2018 (f49bc78)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Nov 23 09:29

SPEC is set to: /home/cpu2017-1.0.5-ic18.0u2
Filesystem Type Size Used Avail Use% Mounted on
/dev/md126p2 btrfs 446G 18G 428G 4% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo --[ISE105G-1.01]-- 10/25/2018
Memory:
4x Micron 18ASF2G72AZ-2G6D1 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)
557.xz_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR150
(3.30 GHz, Intel Xeon E-2126G)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrater2017_int_base = 35.3
SPECrater2017_int_peak = Not Run

Compiler Version Notes (Continued)

541.leela_r(base)
-------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC 548.exchange2_r(base)
-------------------------------
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology
ThinkSystem SR150
(3.30 GHz, Intel Xeon E-2126G)

SPECrate2017_int_base = 35.3
SPECrate2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test Date: Nov-2018
Hardware Availability: Jan-2019
Software Availability: May-2018

Base Optimization Flags (Continued)

C benchmarks (continued):
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-H.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2018-11-22 20:49:41-0500.
Report generated on 2018-12-11 14:54:38 by CPU2017 PDF formatter v6067.
Originally published on 2018-12-11.