



# SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## NEC Corporation

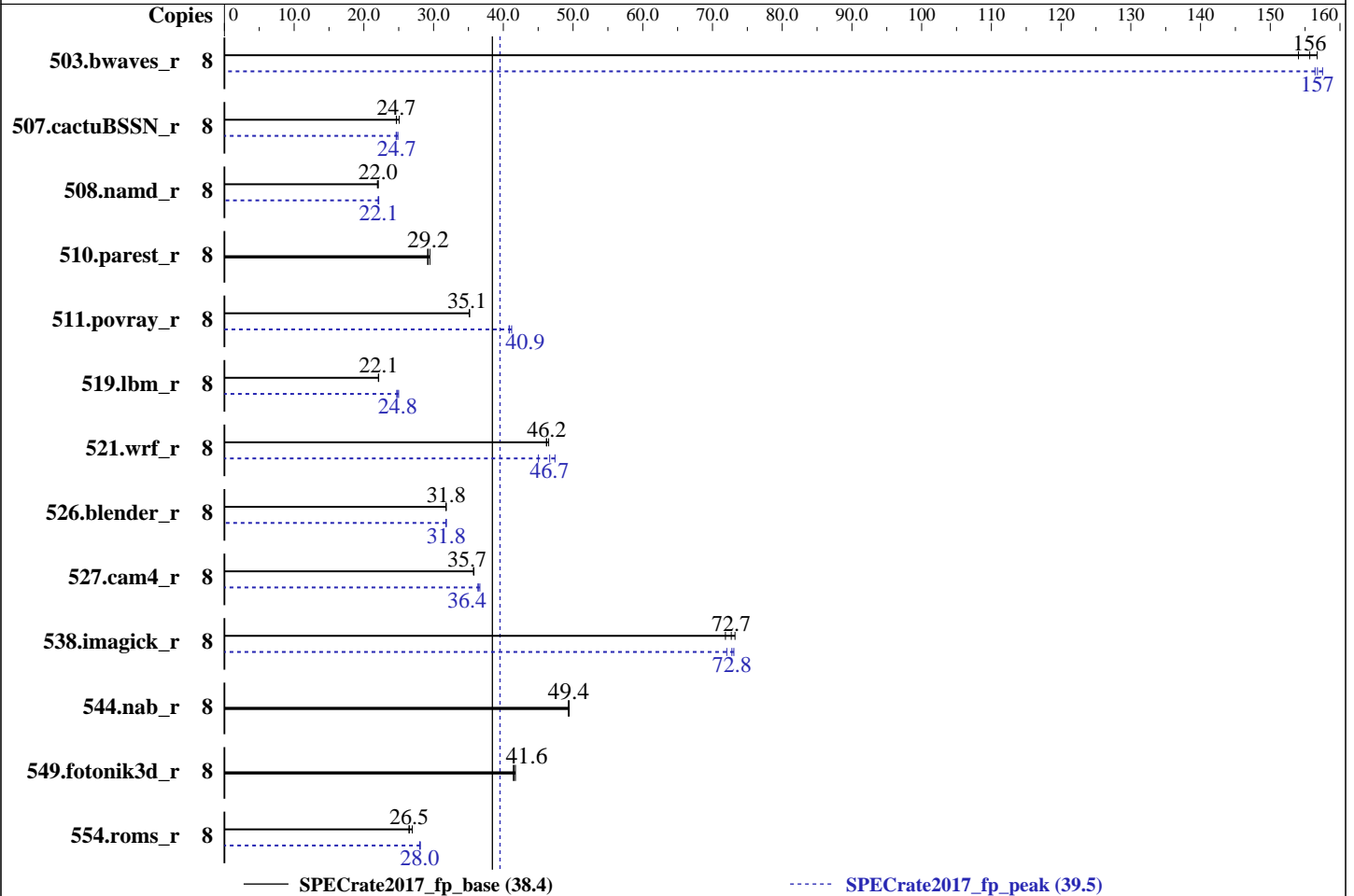
SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

CPU2017 License: 9006  
Test Sponsor: NEC Corporation  
Tested by: NEC Corporation

Test Date: Nov-2018  
Hardware Availability: Aug-2017  
Software Availability: Mar-2018



### Hardware

CPU Name: Intel Xeon Gold 5122  
 Max MHz.: 3700  
 Nominal: 3600  
 Enabled: 4 cores, 1 chip, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 16.5 MB I+D on chip per chip  
 Other: None  
 Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)  
 Storage: 1 x 1 TB SATA, 7200 RPM, RAID 0  
 Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)  
 Kernel 3.10.0-693.21.1.el7.x86\_64  
 Compiler: C/C++: Version 18.0.2.199 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.2.199 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: NEC BIOS Version U30 02/15/2018 released Mar-2018  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## NEC Corporation

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

CPU2017 License: 9006  
Test Sponsor: NEC Corporation  
Tested by: NEC Corporation

Test Date: Nov-2018  
Hardware Availability: Aug-2017  
Software Availability: Mar-2018

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	8	521	154	512	157	<b>515</b>	<b>156</b>	8	<b>512</b>	<b>157</b>	509	158	513	156
507.cactuBSSN_r	8	<b>410</b>	<b>24.7</b>	404	25.1	411	24.7	8	<b>410</b>	<b>24.7</b>	407	24.9	411	24.6
508.namd_r	8	346	21.9	344	22.1	<b>345</b>	<b>22.0</b>	8	<b>343</b>	<b>22.1</b>	343	22.1	345	22.0
510.parest_r	8	710	29.5	718	29.1	<b>716</b>	<b>29.2</b>	8	710	29.5	718	29.1	<b>716</b>	<b>29.2</b>
511.povray_r	8	531	35.2	<b>532</b>	<b>35.1</b>	532	35.1	8	<b>457</b>	<b>40.9</b>	458	40.8	453	41.2
519.lbm_r	8	381	22.1	<b>381</b>	<b>22.1</b>	382	22.1	8	341	24.7	337	25.0	<b>339</b>	<b>24.8</b>
521.wrf_r	8	<b>388</b>	<b>46.2</b>	385	46.5	388	46.2	8	378	47.4	<b>384</b>	<b>46.7</b>	398	45.0
526.blender_r	8	<b>383</b>	<b>31.8</b>	384	31.8	383	31.8	8	383	31.8	383	31.8	<b>383</b>	<b>31.8</b>
527.cam4_r	8	391	35.8	<b>391</b>	<b>35.7</b>	392	35.7	8	384	36.4	382	36.6	<b>384</b>	<b>36.4</b>
538.imagick_r	8	<b>274</b>	<b>72.7</b>	272	73.2	277	71.9	8	276	72.1	<b>273</b>	<b>72.8</b>	272	73.1
544.nab_r	8	273	49.3	272	49.5	<b>273</b>	<b>49.4</b>	8	273	49.3	272	49.5	<b>273</b>	<b>49.4</b>
549.fotonik3d_r	8	752	41.4	<b>750</b>	<b>41.6</b>	747	41.7	8	752	41.4	<b>750</b>	<b>41.6</b>	747	41.7
554.roms_r	8	480	26.5	<b>479</b>	<b>26.5</b>	472	26.9	8	<b>454</b>	<b>28.0</b>	452	28.1	454	28.0

SPECrate2017\_fp\_base = 38.4

SPECrate2017\_fp\_peak = 39.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## NEC Corporation

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Nov-2018

Hardware Availability: Aug-2017

Software Availability: Mar-2018

### General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

BIOS Settings:

Thermal Configuration: Maximum Cooling  
Workload Profile: General Throughput Compute  
Memory Patrol Scrubbing: Disabled  
LLC Dead Line Allocation: Disabled  
LLC Prefetch: Enabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on r120h2m Fri Nov 16 17:15:47 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
 1 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 2 3 4 10
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 8
On-line CPU(s) list: 0-7
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 1
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## NEC Corporation

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Nov-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

```

Model name: Intel(R) Xeon(R) Gold 5122 CPU @ 3.60GHz
Stepping: 4
CPU MHz: 3600.000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0,1,4,5
NUMA node1 CPU(s): 2,3,6,7
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx fl6c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

```

```

/proc/cpuinfo cache data
cache size : 16896 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0 1 4 5
node 0 size: 97964 MB
node 0 free: 95557 MB
node 1 cpus: 2 3 6 7
node 1 size: 98303 MB
node 1 free: 95956 MB
node distances:
node  0  1
 0:  10  21
 1:  21  10

```

```

From /proc/meminfo
MemTotal: 197751988 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"

```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## NEC Corporation

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Nov-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Mar-2018

### Platform Notes (Continued)

```
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server
```

```
uname -a:
Linux r120h2m 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS (kernel)
```

run-level 3 Nov 16 17:10

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  909G  384G  479G  45% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS NEC U30 02/15/2018
Memory:
12x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666
```

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
CC 519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base, peak)
-----
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
-----
```

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## NEC Corporation

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Nov-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Mar-2018

### Compiler Version Notes (Continued)

=====  
CC 519.lbm\_r(peak)  
-----

icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 508.namd\_r(base) 510.parest\_r(base, peak)  
-----

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CXXC 508.namd\_r(peak)  
-----

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 511.povray\_r(base) 526.blender\_r(base, peak)  
-----

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
CC 511.povray\_r(peak)  
-----

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

=====  
FC 507.cactuBSSN\_r(base, peak)  
-----

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**NEC Corporation**

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Nov-2018

Hardware Availability: Aug-2017

Software Availability: Mar-2018

## Compiler Version Notes (Continued)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
FC 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
FC 554.roms\_r(peak)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 521.wrf\_r(base) 527.cam4\_r(base)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====  
CC 521.wrf\_r(peak) 527.cam4\_r(peak)

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

NEC Corporation

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Nov-2018

Hardware Availability: Aug-2017

Software Availability: Mar-2018

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
```

```
507.cactuBSSN_r: -DSPEC_LP64
```

```
508.namd_r: -DSPEC_LP64
```

```
510.parest_r: -DSPEC_LP64
```

```
511.povray_r: -DSPEC_LP64
```

```
519.lbm_r: -DSPEC_LP64
```

```
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
```

```
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
```

```
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
```

```
538.imagick_r: -DSPEC_LP64
```

```
544.nab_r: -DSPEC_LP64
```

```
549.fotonik3d_r: -DSPEC_LP64
```

```
554.roms_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

(Continued on next page)





# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**NEC Corporation**

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Nov-2018

Hardware Availability: Aug-2017

Software Availability: Mar-2018

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

Benchmarks using both C and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
ifort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

(Continued on next page)



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

NEC Corporation

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Nov-2018

Hardware Availability: Aug-2017

Software Availability: Mar-2018

## Peak Optimization Flags (Continued)

519.lbm\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

538.imagick\_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab\_r: basepeak = yes

C++ benchmarks:

508.namd\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -auto  
-nostandard-realloc-lhs

549.fotonik3d\_r: basepeak = yes

554.roms\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:

511.povray\_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3  
-no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

526.blender\_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs



# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**NEC Corporation**

SPECrate2017\_fp\_base = 38.4

Express5800/R120h-2M (Intel Xeon Gold 5122)

SPECrate2017\_fp\_peak = 39.5

**CPU2017 License:** 9006

**Test Date:** Nov-2018

**Test Sponsor:** NEC Corporation

**Hardware Availability:** Aug-2017

**Tested by:** NEC Corporation

**Software Availability:** Mar-2018

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevB.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.5 on 2018-11-16 03:15:47-0500.

Report generated on 2018-12-11 14:54:18 by CPU2017 PDF formatter v6067.

Originally published on 2018-12-11.