# SPEC® CPU2017 Floating Point Speed Result

**Supermicro**  
SuperServer 5019C-L (X11SCL-IF, Intel Xeon E-2174G)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
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<tbody>
<tr>
<td>24.2</td>
<td>24.5</td>
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</tbody>
</table>

**CPU2017 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro  
**Test Date:** Nov-2018  
**Hardware Availability:** Nov-2018  
**Software Availability:** Mar-2018

### Threads

<table>
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<tr>
<th>Benchmark</th>
<th>Threads</th>
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<tr>
<td>603.bwaves_s</td>
<td>4</td>
<td>4.00</td>
<td>84.0</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>4</td>
<td>7.00</td>
<td>39.9</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>4</td>
<td>7.00</td>
<td>39.9</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>4</td>
<td>17.6</td>
<td>32.6</td>
</tr>
<tr>
<td>627.cam4_s</td>
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</tr>
<tr>
<td>628.pop2_s</td>
<td>4</td>
<td>28.9</td>
<td>30.1</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>4</td>
<td>18.9</td>
<td>39.1</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>4</td>
<td>37.0</td>
<td></td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>4</td>
<td>17.7</td>
<td></td>
</tr>
<tr>
<td>654.roms_s</td>
<td>4</td>
<td>15.3</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon E-2174G  
- **Max MHz.:** 4700  
- **Nominal:** 3800  
- **Enabled:** 4 cores, 1 chip  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I+ 32 KB D on chip per core  
- **Cache L2:** 256 KB I+D on chip per core  
- **Cache L3:** 8 MB I+D on chip per core  
- **Memory:** 32 GB (2 x 16 GB 2Rx8 PC4-2666V-E)  
- **Storage:** 1 x 512 GB NVMe SSD  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP3 (x86_64)  
- **Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++  
- **Compiler for Linux:** Fortran: Version 18.0.2.199 of Intel Fortran  
- **Parallel:** Yes  
- **Firmware:** Supermicro BIOS version 1.0 released Sep-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator library V5.0.1


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### Results Table

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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
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<tr>
<td>607.cactuBSSN_s</td>
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<td>7.00</td>
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<td>7.00</td>
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<td>621.wrf_s</td>
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<td>764</td>
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<td>644.nab_s</td>
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<tr>
<td>649.fotonik3d_s</td>
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<td>17.7</td>
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<td>1023</td>
<td>15.4</td>
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</table>

**SPECspeed2017_fp_base = 24.2**  
**SPECspeed2017_fp_peak = 24.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3>/proc/sys/vm/drop_caches
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

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**SPECspeed2017_fp_base** = 24.2  
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<table>
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<tr>
<th>CPU2017 License:</th>
<th>001176</th>
<th>Test Date:</th>
<th>Nov-2018</th>
</tr>
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<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Supermicro</td>
<td>Hardware Availability:</td>
<td>Nov-2018</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Supermicro</td>
<td>Software Availability:</td>
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</table>

**Platform Notes**

BIOS Settings:
Hyper-Threading = Disable
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcede8f2999c33d61f64985e45859ea9
running on linux-0lg1 Mon Nov  5 05:42:55 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name: Intel(R) Xeon(R) E-2174G CPU @ 3.80GHz
  - 4 cores, 4 siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 4
  - siblings: 4
  - physical 0: cores 0 1 2 3

From lscpu:

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 4
- On-line CPU(s) list: 0-3
- Thread(s) per core: 1
- Core(s) per socket: 4
- Socket(s): 1
- NUMA node(s): 1
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 158
- Model name: Intel(R) Xeon(R) E-2174G CPU @ 3.80GHz
- Stepping: 10
- CPU MHz: 4406.134
- CPU max MHz: 4500.0000
- CPU min MHz: 800.0000
- BogoMIPS: 7583.98
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 256K
- L3 cache: 8192K
- NUMA node 0 CPU(s): 0-3
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp

(Continued on next page)
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Platform Notes (Continued)

lm constant_tsc art arch_perfmon pebs bts rep_good nop1 xtobiology nonstop_tsc aperf perf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 sse3 sdbg fma cx16 xpr rdm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand 1ahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtihrm hwp hwp_notify hwp_act_window hwp_epp intel_pt rsb_ctxsw spec_ctrl retpol ine kaiser tpr_shadow vnmi flexpriority ept vpid fsqbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rt mpx rdseed adx smap clflushopt xsaveopt sxsave xgetbv1

From /proc/cpuinfo cache data

cache size: 8192 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)
node 0 cpus: 0 1 2 3
node 0 size: 32089 MB
node 0 free: 24812 MB
node distances:
node 0
0: 10

From /proc/meminfo

MemTotal: 32860044 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-0lg1 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

(Continued on next page)
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Platform Notes (Continued)

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: Barriers
CVE-2017-5715 (Spectre variant 2): Mitigation: IBRS+IBPB

run-level 3 Nov 5 00:43

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p4 xfs 435G 33G 402G 8% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0 09/14/2018
Memory:
2x Micron 18ADF2G72AZ-2G6H1R 16 GB 2 rank 2667

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC   619.lbm_s(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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**Compiler Version Notes (Continued)**

---

```
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base, peak)
```

ifort (IFORT) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

---

```
FC  603.bwaves_s(peak) 649.fotonik3d_s(peak)
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ifort (IFORT) 18.0.2 20180210  
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CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
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```
CC  621.wrf_s(peak) 628.pop2_s(peak)
```

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icc (ICC) 18.0.2 20180210  
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---

**Base Compiler Invocation**

C benchmarks:  
`icc -m64 -std=c11`

Fortran benchmarks:  
`ifort -m64`

Benchmarks using both Fortran and C:  
`ifort -m64 icc -m64 -std=c11`

(Continued on next page)
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Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-W1, -z, muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using both Fortran and C:
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-W1, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -L/usr/local/je5.0.1-64/lib -ljemalloc
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Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs

654.roms_s: -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3
-qopenmp -nostandard-realloc-lhs

(Continued on next page)
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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMPMP -nostandard-realloc-lhs

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMPMP -nostandard-realloc-lhs

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SKL-revD.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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